

SEMICONDUCTOR TECHNICAL DATA

FA431ZCSR

PROGRAMMABLE PRECISION REFERENCES

The FA431ZCSR integrated circuits are three-terminal programmable shunt regulator diodes.

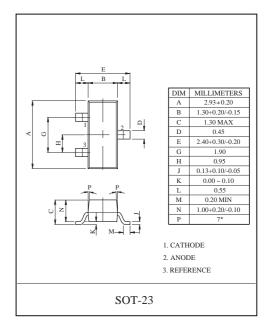
These monolithic IC voltage reference operate as a low temperature coefficient zener which is programmable from V_{ref} to 40 volts with two external resistors.

These devices exhibit a wide operating current range of 1.0 to 100mA with a typical dynamic impedance of 0.2Ω .

The characteristics of these references make them excellent replacements for zener diodes in many applications such as digital voltmeters, power supplies, and op amp circuitry. The 2.5 volt reference makes it convenient to obtain a stable reference

from 5.0 volt logic supplies, and since the FA431ZCSR operates as a shunt regulator,

it can be used as either a positive or negative voltage reference.



FEATURES

· Divice Code Name :FA431 + V_{ref} Code + Package Code

ITEM	7	V _{ref} Code	Package Code			
FA431ZCSR	Code	Tolerance (%)	Code	Package		
	С	±0.5	S	SOT-23		

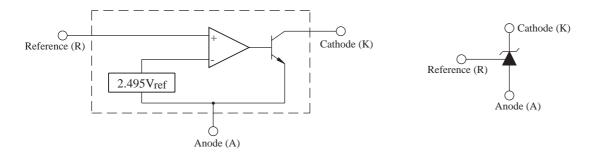
- Low Dynamic Output Impedance : 0.2Ω (Typ.).
- Sink Current Capability of 1.0 to 100mA.
- Equivalent Full-Range Temperature Coefficient of 50ppm/°C (Typ.).
- \cdot Temperature Compensated for Operation Over Full Rated Operating Temperature Range.
- Low Output Noise Voltage.

MARK: LA3



FA431ZCSR

BLOCK DIAGRAM



MAXIMUM RATINGS (Ta=25°C)

(Full operating ambient temperature range applies unless otherwise noted.)

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Cathode To Anode Voltage	V_{KA}	36	V	
Cathode Current Range, Continuous	I_{K}	-100 ~ 150	mA	
Reference Input Current Range, Continuous	I_{ref}	-0.05 ~ 10	mA	
Operating Junction Temperature	$T_{\rm j}$	150	°C	
Operating Temperature	T_{opr}	T _{opr} -40 ~125		
Storage Temperature	T_{stg}	-65 ~ 150	°C	
Total Power Dissipation	P_{D}	200	mW	

ELECTRICAL CHARACTERISTICS (Ta=25 $^{\circ}$ C)

CHARACTERISTICS		SYMBOL	TEST CIRCUIT	TEST CONDITION		MIN.	TYP.	MAX.	UNIT
Reference Input Voltage	FA431ZCSR (0.5%)	V_{ref}	Figure 1	$V_{KA}=V_{ref}$, $I_{K}=10mA$		2.482	2.495	2.507	V
Reference Input Voltage Deviation Over Temperature Range		ΔV_{ref}	Figure 1 (Note 1)	V _{KA} =V _{ref} , I _K =10mA		-	3	17	mV
Ratio of Change in Reference Input Voltage to Change in Cathode to Anode Voltage		$\Delta V_{\rm ref} / \Delta V_{\rm KA}$	Figure 2	I _K =10mA	$\Delta V_{KA} = 10V \sim V_{ref}$	-	-1.4	-2.7	- mV/V
					$\Delta V_{KA} = 36V \sim 10V$	-	-1.0	-2.0	
Reference Input Current		I _{ref}	Figure 2	I_K =10mA, R1=10kΩ, R2=∞		-	0.5	2.0	μΑ
Reference Input Current Deviation Over Temperature Range		ΔI_{ref}	Figure 2	I_K =10mA, R1=10kΩ, R2=∞		-	0.4	1.2	μΑ
Minimum Cathode Current For Regulation		I_{min}	Figure 1	$V_{KA} = V_{ref}$		-	0.25	0.5	mA
Off-State Cathode Current		I_{off}	Figure 3	V _{KA} =40V, V _{ref} =0V		-	170	900	nA
Dynamic Impedance		Z_{ka}	Figure 1 (Note 2)	$V_{KA}=V_{ref}, I_{K}=1.0 \sim 100 mA,$ $f \leq 1.0 kHz$		-	0.27	0.5	Ω



FA431ZCSR

FIGURE 1-TEST CIRCUIT FOR $V_{KA} = V_{ref}$

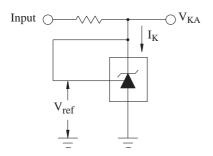
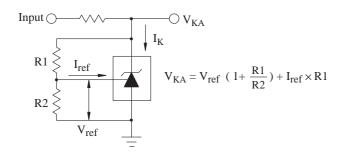
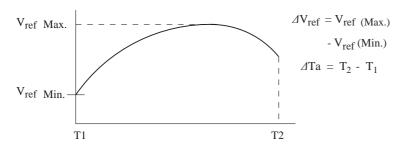


FIGURE 2-TEST CIRCUIT FOR $V_{KA} > V_{ref}$



Note 1:

The deviation parameter ΔV_{ref} is defined as the differences between the maximum and minimum values obtained over the full operating ambient temperature range that applies.



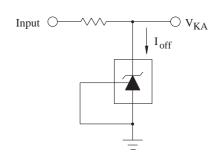
AMBIENT TEMPERATURE

The average temperature coefficient of the Reference input voltage, $\ \ \, \varDelta V_{ref}$, is defined as:

$$\alpha V_{ref} \left(\frac{ppm}{^{\circ}C} \right) = \frac{\left(\frac{\Delta V_{ref}}{V_{ref} \text{ at } 25 ^{\circ}C} \right) \times 10^{6}}{\Delta Ta}$$
$$= \frac{\Delta V_{ref} \times 10^{6}}{\Delta Ta(V_{ref} \text{ at } 25 ^{\circ}C)}$$

 $\alpha V_{ref} \ \ can \ be \ positive \ or \ negative \ depending \ on \ whether \\ V_{ref} \ \ Min. \ or \ V_{ref} \ Max. \ occurs \ at the \ lower \ ambient \ temperature.$

FIGURE 3-TEST CIRCUIT FOR $\rm\,I_{off}$



Example: $\Delta V_{ref} = 8.0 \text{mV}$ and slope is positive, V_{ref} at 25 °C=2.500V, $\Delta Ta=70$ °C

$$\alpha V_{ref} \! = \! \frac{0.008 \times \! 10^6}{70 \times \! (2.500)} \! = 45.8 \text{ ppm/°C}$$

Note 2: The dynamic impedance Z_{ka} is defined as:

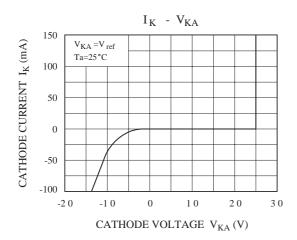
$$|Z_{ka}| = \frac{\Delta VKA}{\Delta Ik}$$

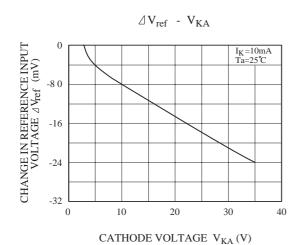
When the device is programmed with two external resistors, R1 and R2, (refer to Figure 2) the total dynamic impedance of the circuit is defined as:

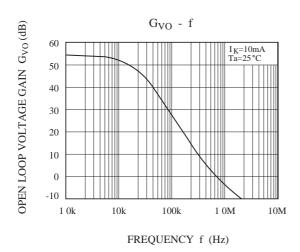
$$|Z_{ka'}| = |Z_{ka}| (1 + \frac{R1}{R2})$$

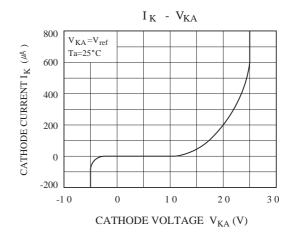


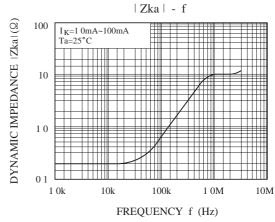
FA431ZCSR

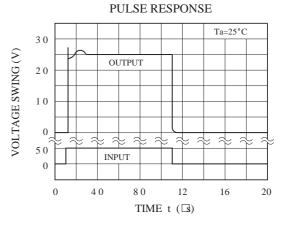


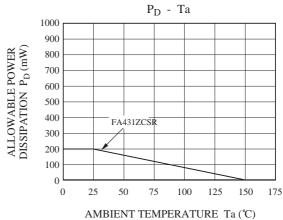












73

FA431ZCSR

PRECAUTION FOR USE

SOLDERING

Flat Package (SMD Package)

Elements mounting styles of electronic devices are gaining in further diversification over recent years, and needs for components are all the more expanding in varieties. Especially, surface mounting is steadily penetrating into industrial segments as a world-wide popular technical trend.

Although exposure to high temperature is inevitable during soldering we recommend limiting the soldering temperature to low levels as shown in figure for the sake of retaining inherent excellent reliability.

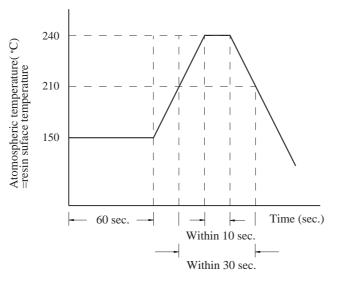


Fig. 6

- (a) When employing solder reflow method
 - ① Atmospheric temperature around resin surfaces must be less than 240 °C, not exceeding the time length of 10 sec.
 - (2) Recommend temperature profile
 - ③ Precautions on heating method

When resin in kept exposed to high temperature for a long time, device reliability may be marred.

Therefore, it is essential to complete soldering in the shortest time possible to prevent temperature of resin from rising.

(b) When employing halogen lamps or infrared-ray heaters

When halogen lamps or infrared-ray heaters are used, avoid direct irradiation onto resin surfaces; such devices cause extensive localized temperature rise.

 $\ensuremath{\mathbb{X}}$ Please keep a reflow solder operating when SMD package's soldering.

Revision No: 0