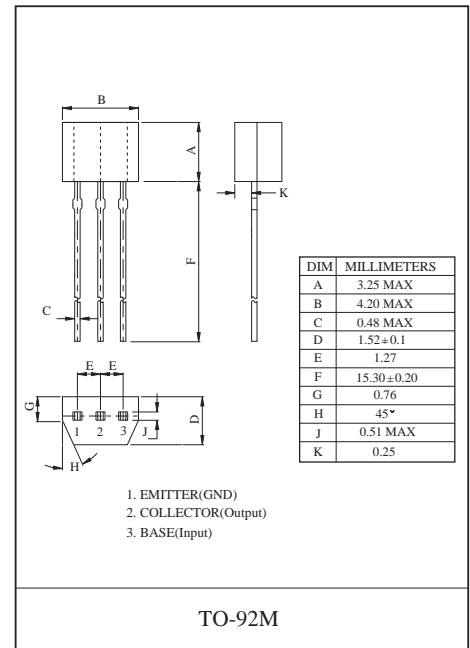


# Bias Resistor Transistor

## PNP Silicon Surface Mount Transistor with Monolithic Bias Resistor Network

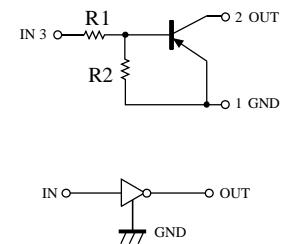
This new series of digital transistors is designed to replace a single device and its external resistor bias network. The BRT (Bias Resistor Transistor) contains a single transistor with a monolithic bias network resistor. The BRT eliminates these individual components by integrating them into a single device. The use of a BRT can reduce both system cost and board space.

- Simplifies Circuit Design
- Reduces Board Space and Component Count



### Absolute maximum ratings(Ta=25°C)

Parameter	Symbol	Value	Unit
Supply voltage	V <sub>CC</sub>	-50	V
Input voltage	V <sub>IN</sub>	-40~10	V
Output current	I <sub>O</sub>	-50	mA
	I <sub>C(MAX)</sub>	-100	
Power dissipation	P <sub>d</sub>	300	mW
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55~150	°C



### Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ	Max.	Unit	Conditions
Input voltage	V <sub>I(off)</sub>	-0.5			V	V <sub>CC</sub> =-5V, I <sub>O</sub> =-100μA
	V <sub>I(on)</sub>			-3		V <sub>O</sub> =-0.3V, I <sub>O</sub> =-10 mA
Output voltage	V <sub>O(on)</sub>			-0.3	V	I <sub>O</sub> /I <sub>I</sub> =-10mA/-0.5mA
Input current	I <sub>I</sub>			-0.88	mA	V <sub>I</sub> =-5V
Output current	I <sub>O(off)</sub>			-0.5	μA	V <sub>CC</sub> =-50V, V <sub>I</sub> =0
DC current gain	G <sub>I</sub>	30				V <sub>O</sub> =-5V, I <sub>O</sub> =-5mA
		50	80			V <sub>CC</sub> =-5V, I <sub>O</sub> =-10mA
Input resistance	R <sub>1</sub>	7	10	13	KΩ	
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2		
Transition frequency	f <sub>T</sub>		250		MHz	V <sub>CE</sub> =-10V, I <sub>E</sub> =5mA, f=100MHz
Switching Time	Rise Time (tr)		0.06		μS	V <sub>O</sub> =-5V Vin=-5V RL=1kΩ
	Storage Time (tstg)		1.1		μS	
	Fall Time (tf)		0.24		μS	

## Typical Characteristics

### ● Electrical characteristic curves

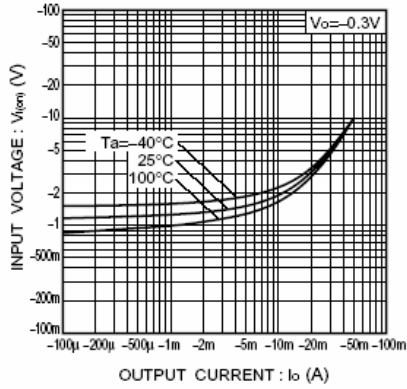


Fig.1 Input voltage vs. output current (ON characteristics)

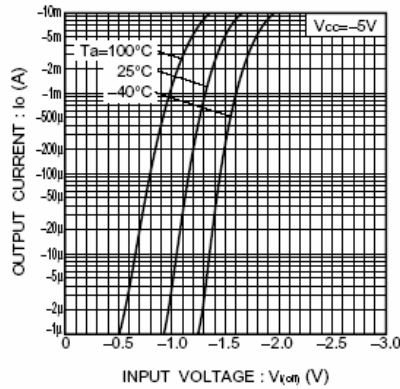


Fig.2 Output current vs. input voltage (OFF characteristics)

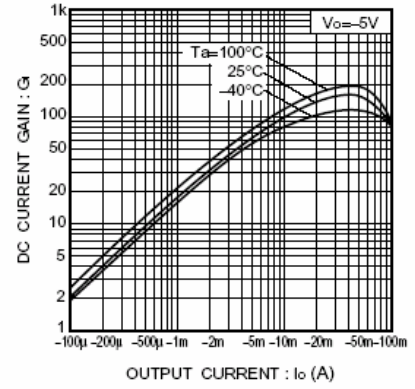


Fig.3 DC current gain vs. output current

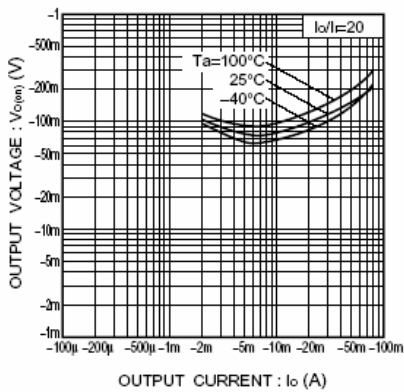


Fig.4 Output voltage vs. output current