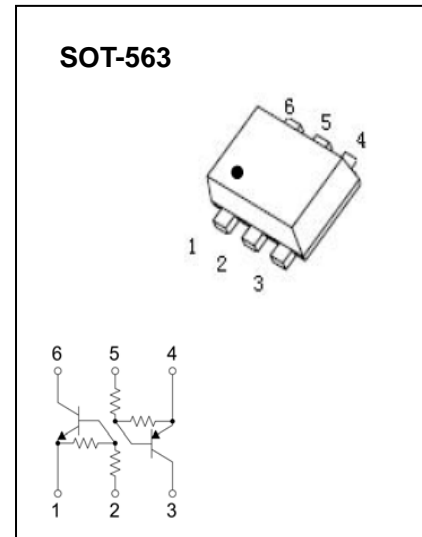


Digital Transistors (Built-in Resistors)

Dual Digital Transistors (NPN+PNP)

FEATURES

- DTA114E and DTC114E transistors are built-in a package.
- Transistor elements are independent, eliminating interference.
- Mounting cost and area can be cut in half.



MARKING: D3

Absolute maximum ratings(Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V_{CC}	50	V
Input voltage	V_{IN}	-10~40	V
Output current	I_O	50	mA
	$I_{C(MAX)}$	100	
Power dissipation	P_d	150(TOTAL)	mW
Operation Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	°C

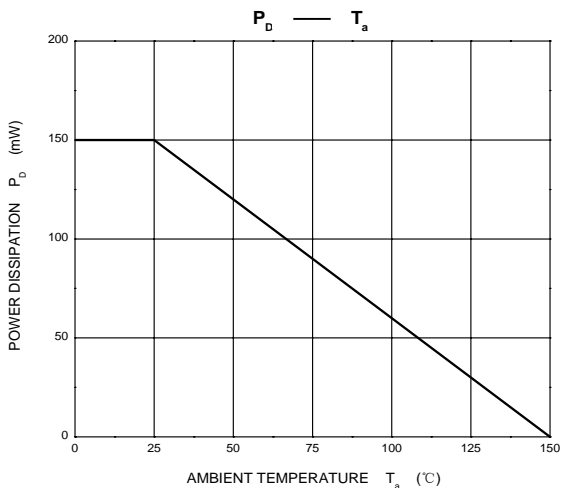
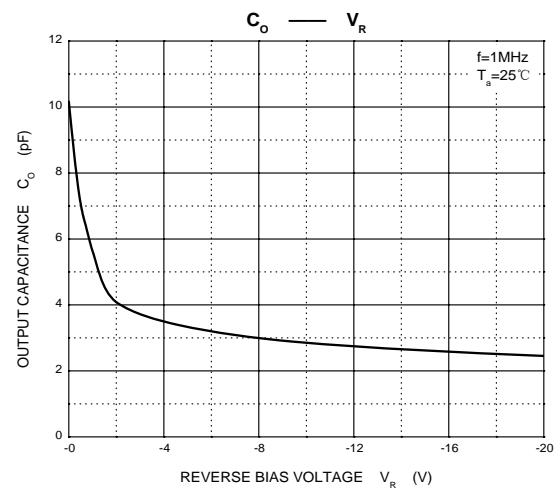
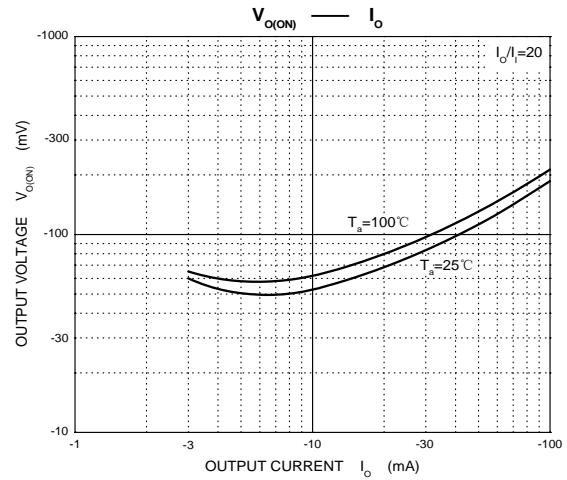
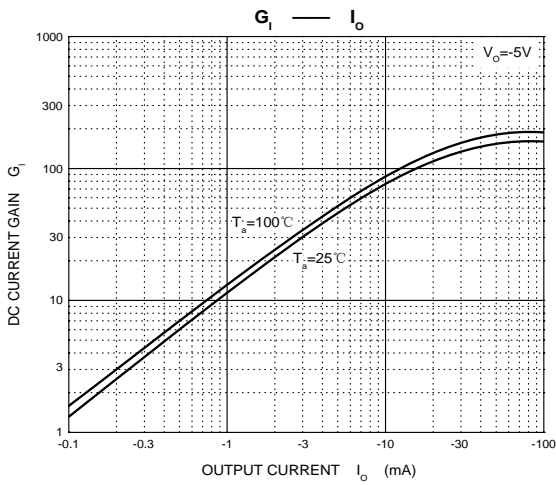
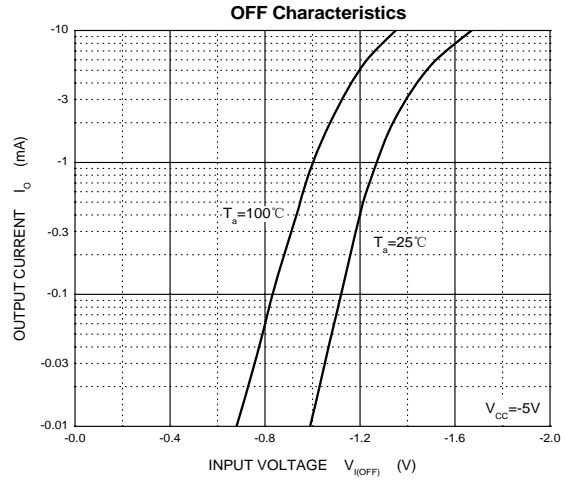
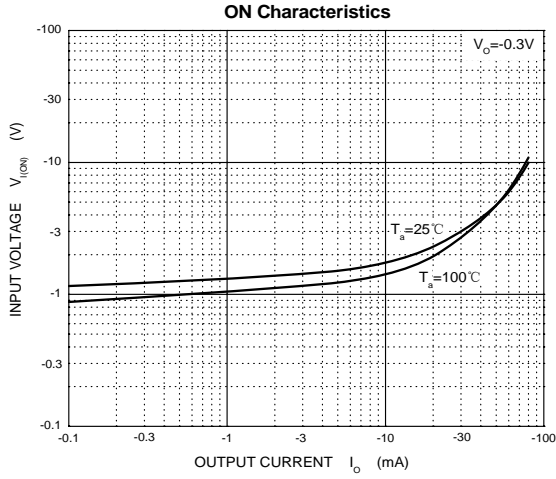
Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	0.5			V	$V_{CC}=5V, I_O=100\mu A$
	$V_{I(on)}$			3		$V_O=0.3V, I_O=10mA$
Output voltage	$V_{O(on)}$			0.3	V	$I_O/I_I=10mA/0.5mA$
Input current	I_I			0.88	mA	$V_I=5V$
Output current	$I_{O(off)}$			0.5	μA	$V_{CC}=50V, V_I=0$
DC current gain	G_I	30				$V_O=5V, I_O=5mA$
Input resistance	R_1	7	10	13	K Ω	
Resistance ratio	R_2/R_1	0.8	1	1.2		
Transition frequency	f_T		250		MHz	$V_{CE}=10V, I_E=-5mA, f=100MHz$



EMD3

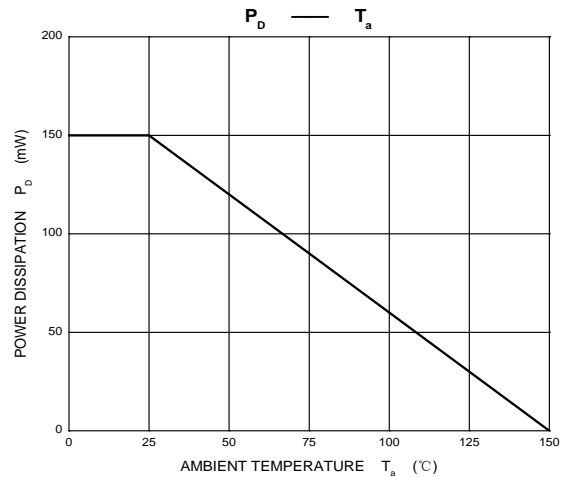
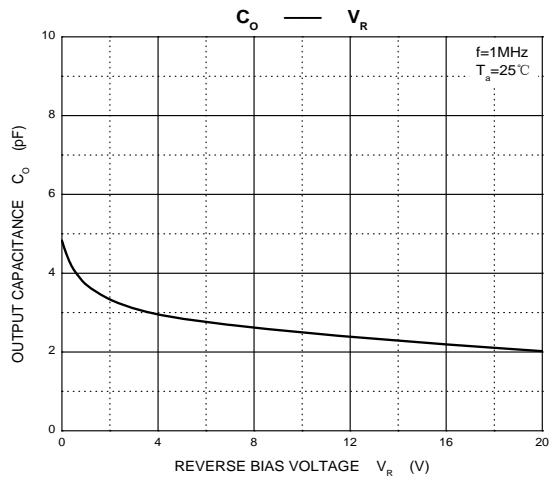
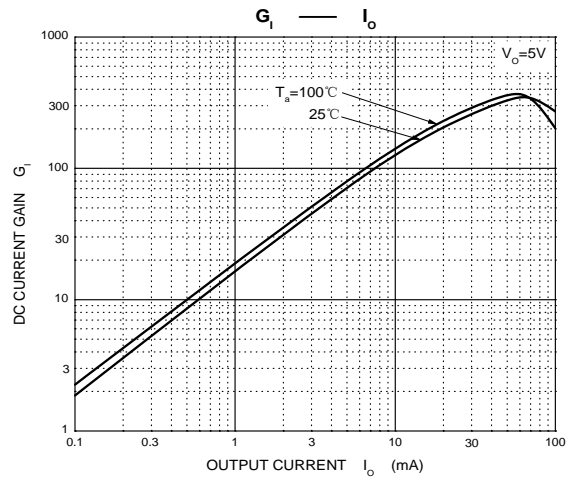
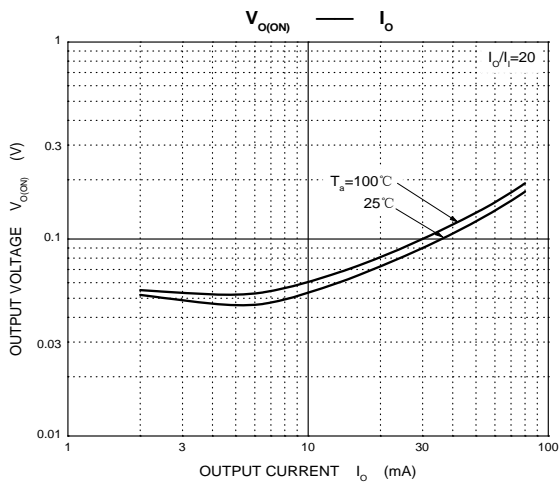
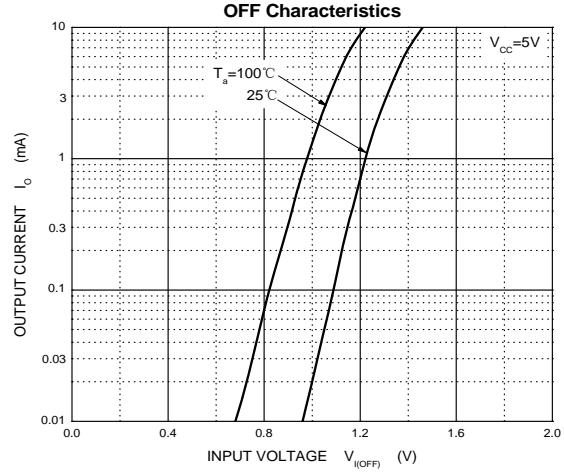
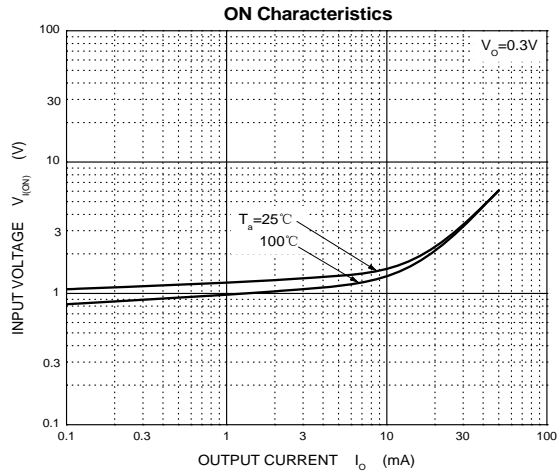
Typical Characteristics DTA114E(PNP)



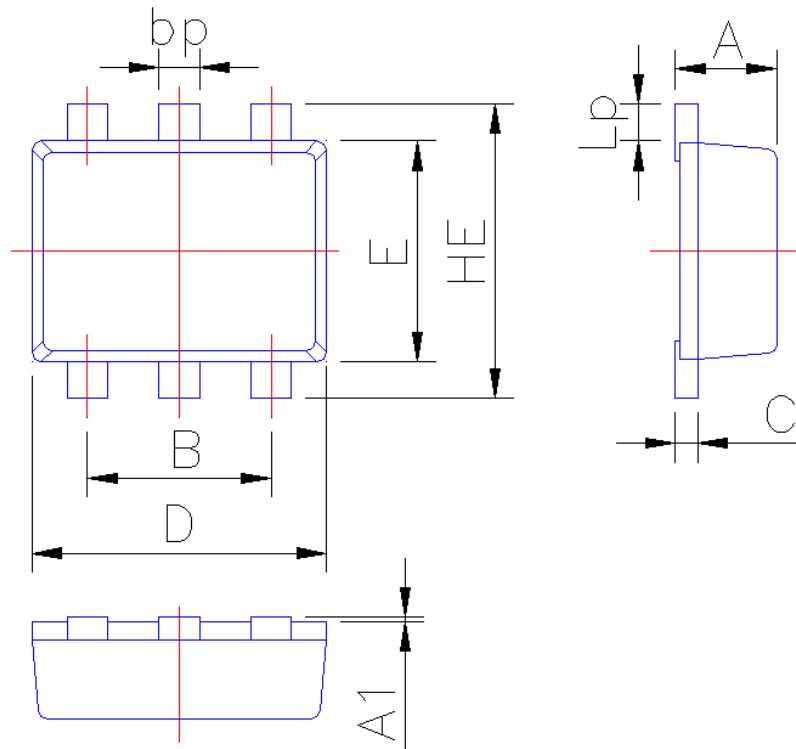


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Typical Characteristics DTC114E(NPN)



SOT-563-Package Outline Dimensions



Symbol	Dimension in Millimeters	
	Min	Max
A	0.50	0.60
A1	0	0.05
B	0.95	1.05
bp	0.13	0.30
C	0.09	0.150
D	1.50	1.70
E	1.15	1.35
HE	1.40	1.80
Lp	0.13	0.30