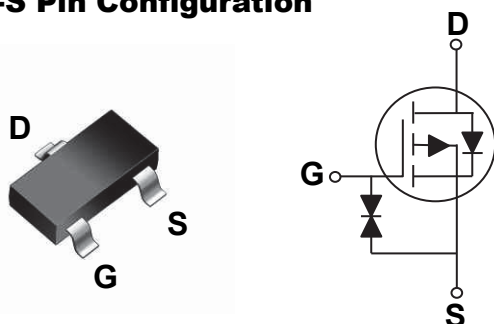


60V P-Channel MOSFETs

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT23-S Pin Configuration



BVDSS	RDSON	ID
-60V	4Ω	-0.5A

Features

- -60V,-0.5A, $R_{DS(ON)} = 4\Omega @ V_{GS} = -10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- G-S ESD Protection Diode Embedded
- ESD protected up to 2KV

Applications

- Notebook
- Load Switch
- Battery Protection
- Hand-held Instruments

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_A=25^\circ\text{C}$)	-0.5	A
	Drain Current – Continuous ($T_A=70^\circ\text{C}$)	-0.4	A
I_{DM}	Drain Current – Pulsed ¹	-2	A
P_D	Power Dissipation ($T_A=25^\circ\text{C}$)	1.56	W
	Power Dissipation – Derate above 25°C	12.5	mW/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	$^\circ\text{C}/\text{W}$



60V P-Channel MOSFETs

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V$, $I_D=-250\mu A$	-60	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-48V$, $V_{GS}=0V$, $T_J=25^{\circ}\text{C}$	---	---	-10	μA
		$V_{DS}=-48V$, $V_{GS}=0V$, $T_J=85^{\circ}\text{C}$	---	---	-1	mA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V$, $V_{DS}=0V$	---	---	± 20	μA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V$, $I_D=-0.3A$	---	2.5	4	Ω
		$V_{GS}=-4.5V$, $I_D=-0.2A$	---	3.3	5.5	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu A$	-1.2	-1.7	-2.5	V
g_{fs}	Forward Transconductance	$V_{DS}=-10V$, $I_D=-0.3A$	---	0.4	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=-30V$, $V_{GS}=-10V$, $I_D=-0.5A$	---	2.8	4.2	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	0.96	1.5	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	0.6	0.9	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=-30V$, $V_{GS}=-10V$, $R_G=6\Omega$ $I_D=-0.5A$	---	3	6	ns
T_r	Rise Time ^{2, 3}		---	5	10	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	14	27	
T_f	Fall Time ^{2, 3}		---	9	17	
C_{iss}	Input Capacitance	$V_{DS}=-30V$, $V_{GS}=0V$, $F=1\text{MHz}$	---	30.5	45	pF
C_{oss}	Output Capacitance		---	15.1	22.5	
C_{rss}	Reverse Transfer Capacitance		---	7	10.5	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	-0.5	A
I_{SM}	Pulsed Source Current		---	---	-1	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V$, $I_S=-0.2A$, $T_J=25^{\circ}\text{C}$	---	---	-1	V
T_{rr}	Reverse Recovery Time	$V_R=-50V$, $I_S=-0.5A$	---	13.5	---	nS
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$, $T_J=25^{\circ}\text{C}$	---	3	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

60V P-Channel MOSFETs

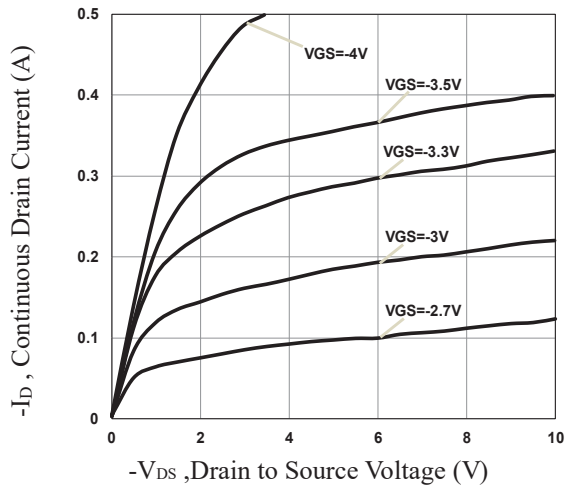


Fig.1 Typical Output Characteristics

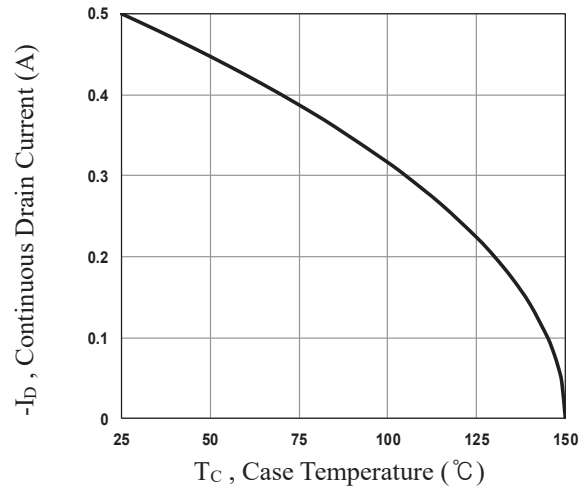


Fig.2 Continuous Drain Current vs. T_C

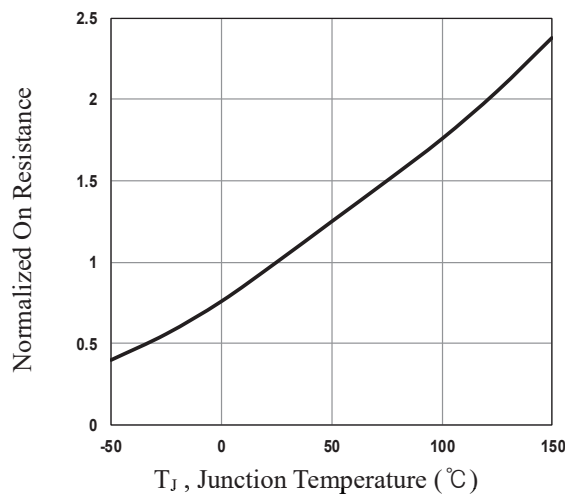


Fig.3 Normalized $R_{DS(on)}$ vs. T_J

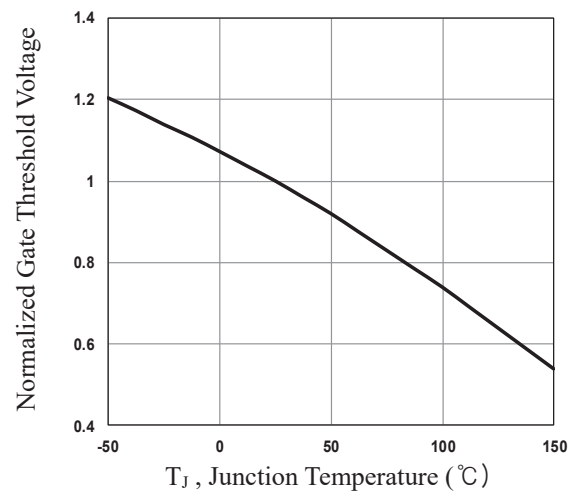


Fig.4 Normalized V_{th} vs. T_J

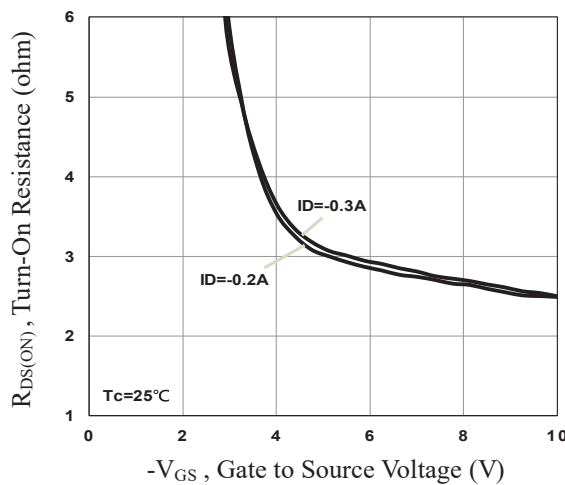


Fig.5 Turn-On Resistance vs. V_{GS}

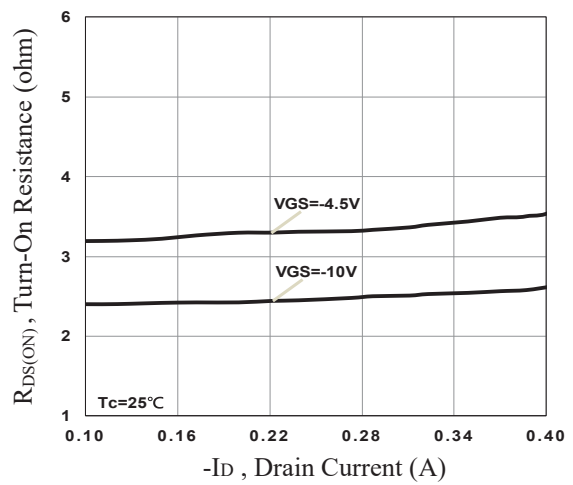


Fig.6 Turn-On Resistance vs. I_D

60V P-Channel MOSFETs

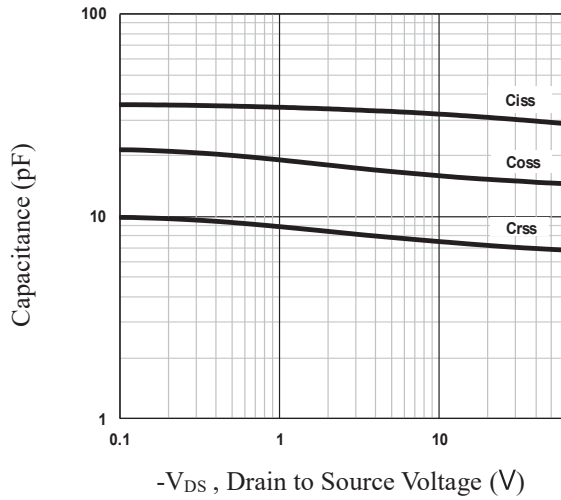


Fig.7 Capacitance Characteristics

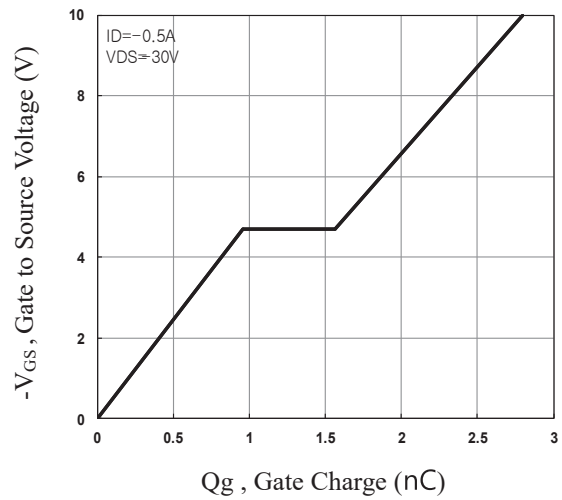


Fig.8 Gate Charge Characteristics

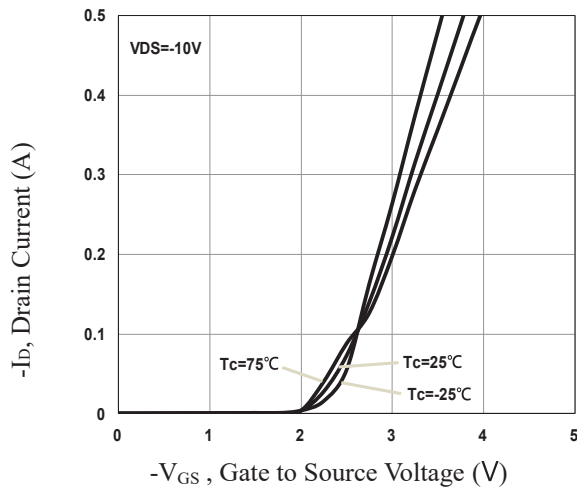


Fig.9 Transfer Characteristics

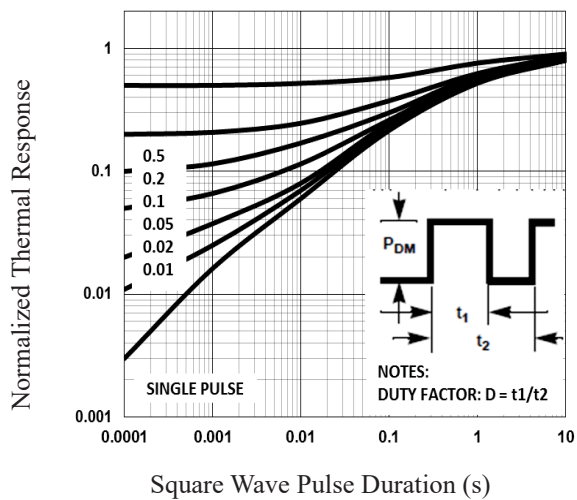


Fig.10 Normalized Transient Impedance

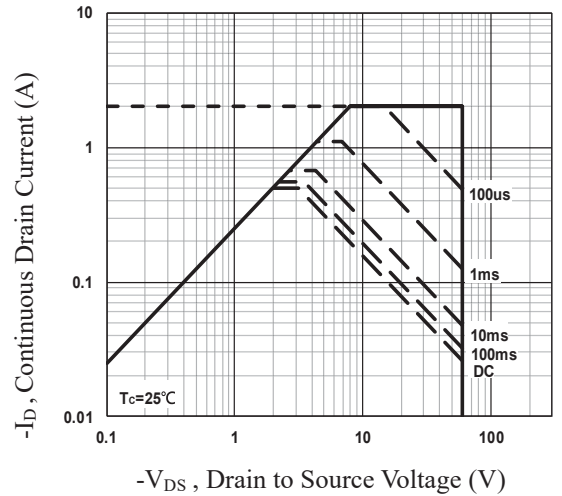
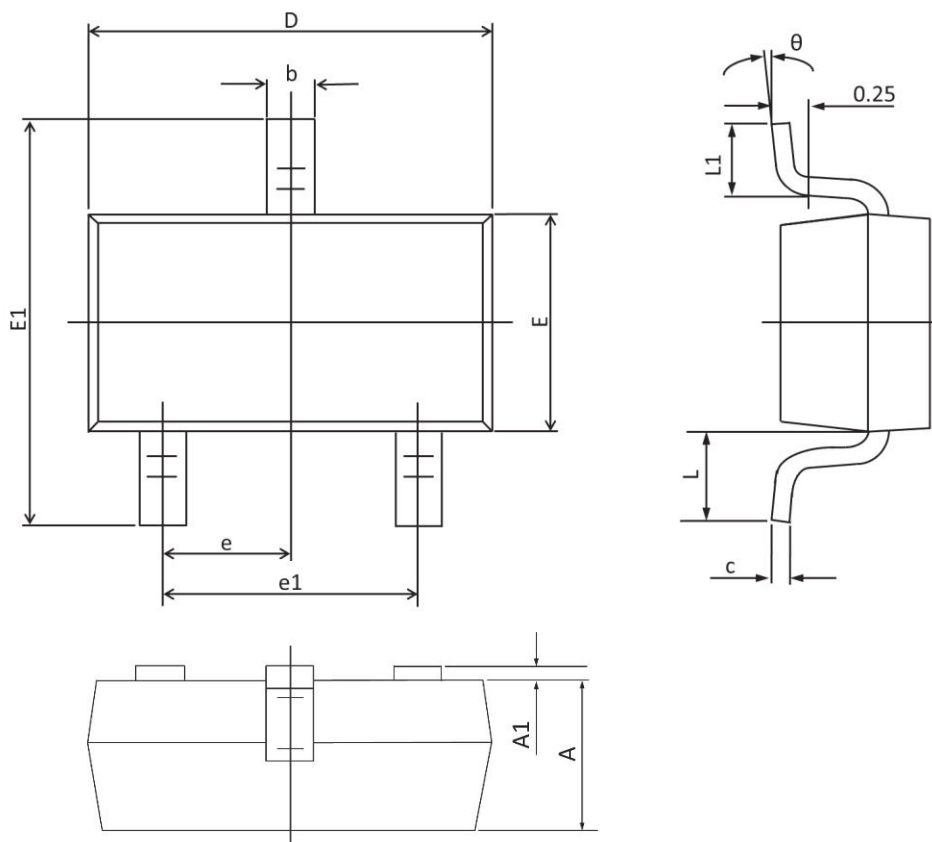


Fig.11 Maximum Safe Operation Area

60V P-Channel MOSFETs
SOT23-3S PACKAGE INFORMATION


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.700	3.100	0.106	0.122
E	1.100	1.500	0.043	0.059
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
θ	1°	10°	1°	10°