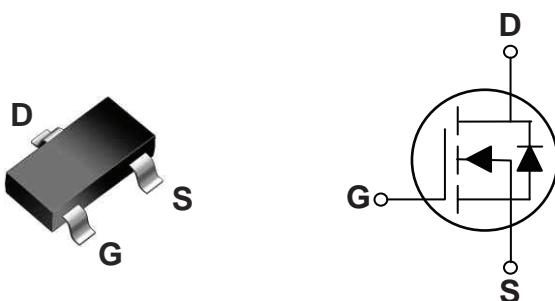


# 100V N-Channel MOS FETs

## General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

## SOT23-3S Pin Configuration



BVDSS	RDS(ON)	ID
100V	200mΩ	2A

## Features

- 100V,2A, RDS(ON) = 200mΩ @VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed

## Applications

- Networking
- Load Switch
- LED applications

## Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	2	A
$I_D$	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	1.3	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	8	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	1.56	W
$P_D$	Power Dissipation – Derate above 25°C	0.012	W/°C
$T_{STG}$	Storage Temperature Range	-50 to 150	°C
$T_J$	Operating Junction Temperature Range	-50 to 150	°C

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	80	°C/W



## Electrical Characteristics ( $T_J=25^\circ\text{C}$ )

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=250\mu\text{A}$	100	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	$\text{BV}_{\text{DSS}}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.10	---	$\text{V}/^\circ\text{C}$
$I_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=100\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{\text{DS}}=80\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	nA

### On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$ , $I_D=2\text{A}$	---	161	200	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$ , $I_D=1\text{A}$	---	169	210	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = 250\mu\text{A}$	1.2	1.8	2.5	V
			---	-4	---	$\text{mV}/^\circ\text{C}$
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}$ , $I_D=1\text{A}$	---	5	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $I_D=2\text{A}$	---	13.4	21	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>2, 3</sup>		---	2.9	6	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>2, 3</sup>		---	1.7	4	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{\text{DD}}=30\text{V}$ , $V_{\text{GS}}=10\text{V}$ , $R_G=3.3\Omega$	---	1.6	3	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	6.6	13	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>2, 3</sup>		---	11.5	22	
$T_f$	Fall Time <sup>2, 3</sup>		---	3.6	7	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=50\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	820	1190	pF
$C_{\text{oss}}$	Output Capacitance		---	35	55	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	20	30	
$R_g$	Gate resistance	$V_{\text{GS}}=0\text{V}$ , $V_{\text{DS}}=0\text{V}$ , $F=1\text{MHz}$	---	1.3	2.6	$\Omega$

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	2	A
			---	---	8	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.

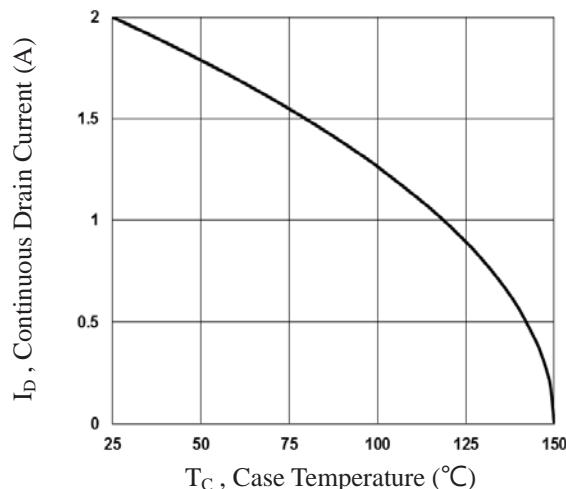
**ELECTRICAL CHARACTERISTICS CURVES**


Fig.1 Continuous Drain Current vs. T<sub>C</sub>

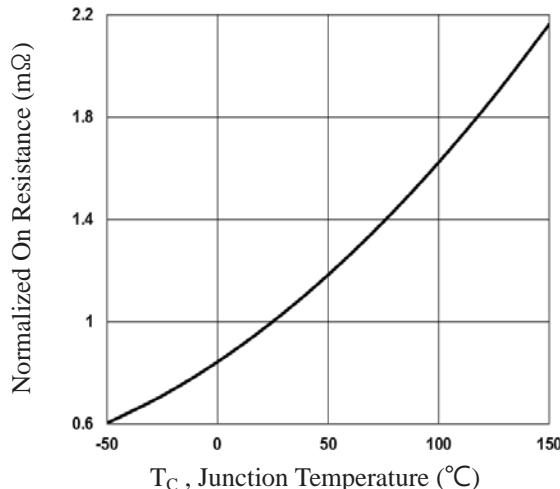


Fig.2 Continuous Drain Current vs. T<sub>c</sub>

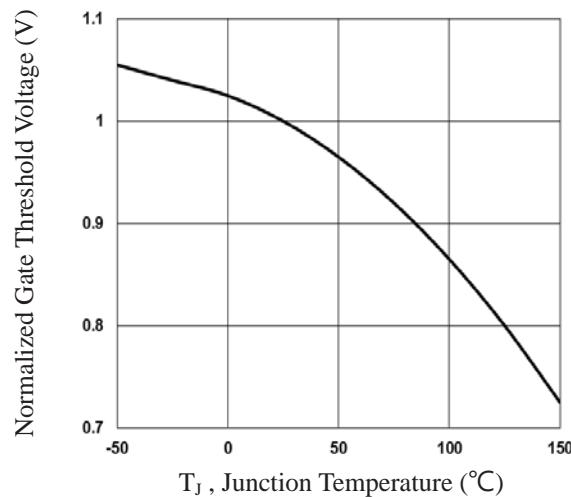


Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>

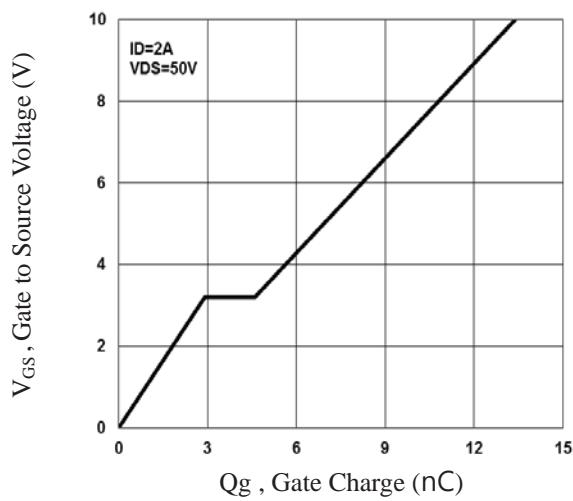


Fig.4 Gate Charge Waveform

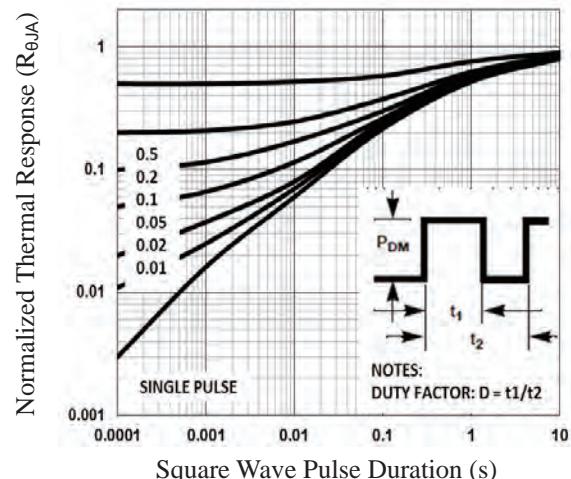


Fig.5 Normalized Transient Impedance

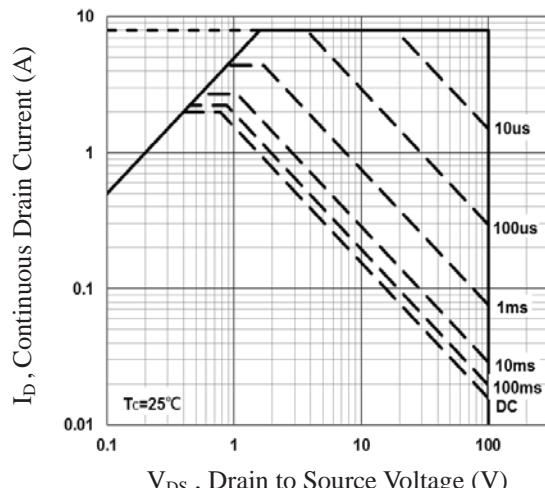


Fig.6 Maximum Safe Operation Area

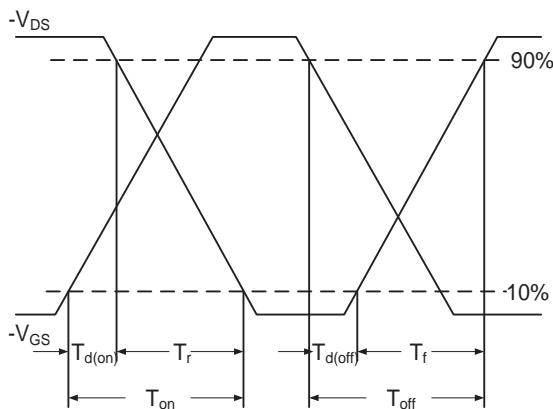
**ELECTRICAL CHARACTERISTICS CURVES (Con.)**


Fig.7 Switching Time Waveform

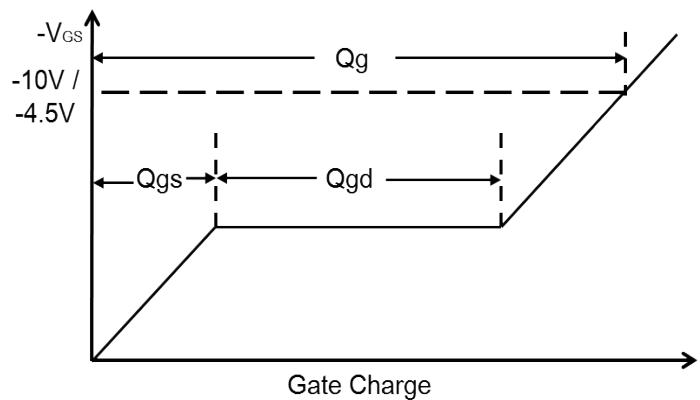
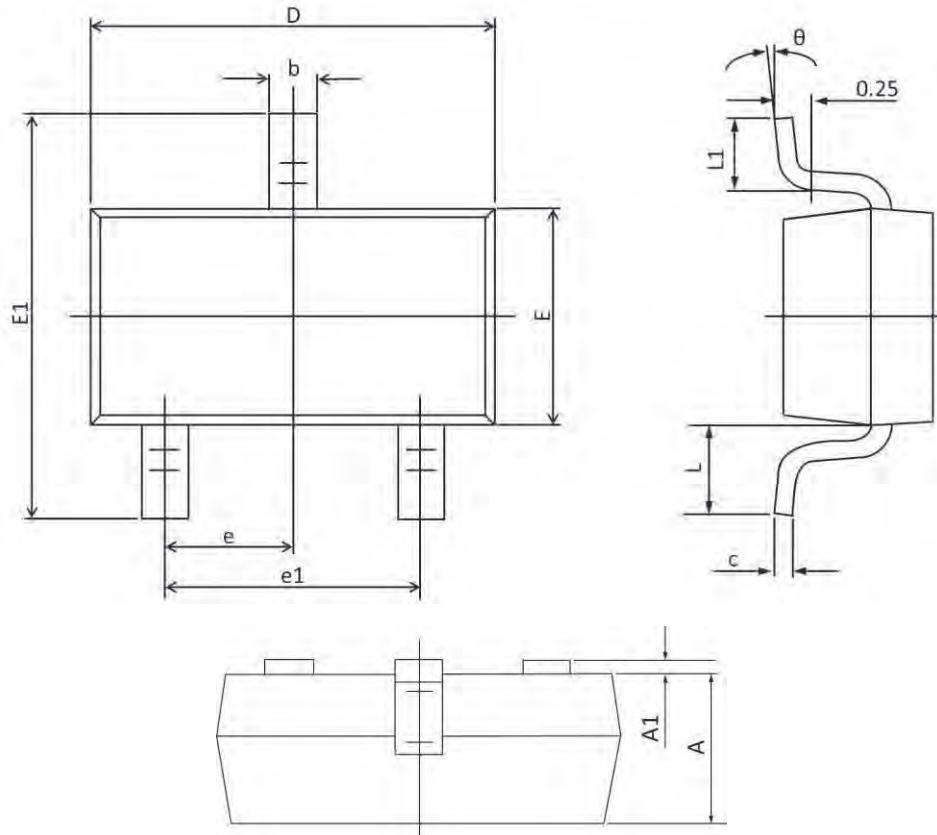


Fig.8 Gate Charge Waveform



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.000	0.035	0.039
A1	0.000	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.090	0.110	0.003	0.004
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	1°	7°	1°	7°