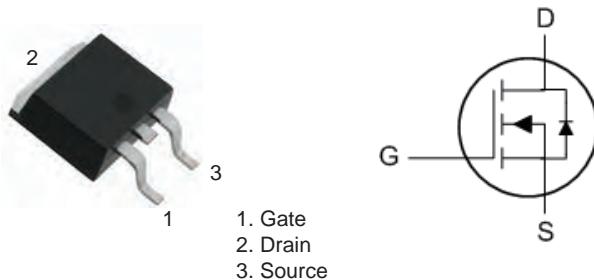


40V N-Channel MOSFETs

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO-252 Pin Configuration



BVDSS	RDS(ON)	ID
40V	8.5mΩ	50A

Features

- 40V, 50A, RDS(ON) = 8.5mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Notebook
- Load Switch
- LED applications
- Hand-Held Device

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{GS}	Gate-Source Voltage	± 20	V
I _D	Drain Current – Continuous (T _c =25 °C)	50	A
	Drain Current – Continuous (T _c =100 °C)	31.6	A
I _{DM}	Drain Current – Pulsed ¹	200	A
P _D	Power Dissipation (T _c =25 °C)	54	W
	Power Dissipation – Derate above 25 °C	0.43	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	2.31	°C/W



FTK4906D

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	40	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.03	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=40\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=32\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=85^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=8\text{A}$	---	6.5	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=4\text{A}$	---	9	12	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	1.2	1.8	2.5	V
			---	-5	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=10\text{A}$	---	13	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $I_D=8\text{A}$	---	12.2	24	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	3.3	7	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	6.7	13	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=15\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3.3\Omega$	---	13.2	25	ns
T_r	Rise Time ^{2,3}		---	2.2	5	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	72	130	
T_f	Fall Time ^{2,3}		---	4.5	10	
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	1220	2200	pF
C_{oss}	Output Capacitance		---	130	250	
C_{rss}	Reverse Transfer Capacitance		---	55	110	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	2.2	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	50	A
			---	---	100	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V
			---	17	---	ns
Q_{rr}	Reverse Recovery Charge ³	$V_{\text{GS}}=0\text{V}, I_s=1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$	---	2.8	---	nC
		$T_J=25^\circ\text{C}$				

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

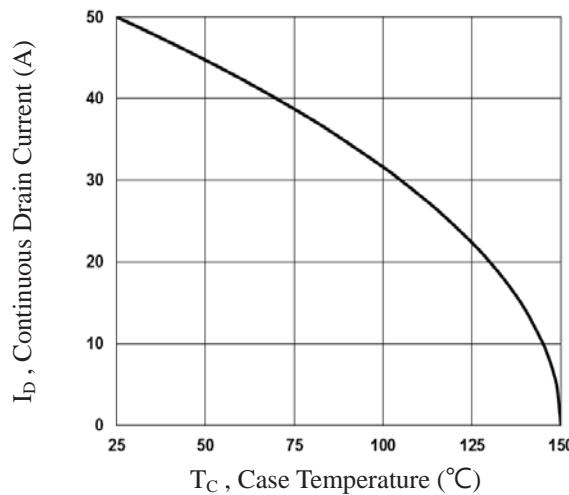
ELECTRICAL CHARACTERISTICS CURVES


Fig.1 Continuous Drain Current vs. T_c

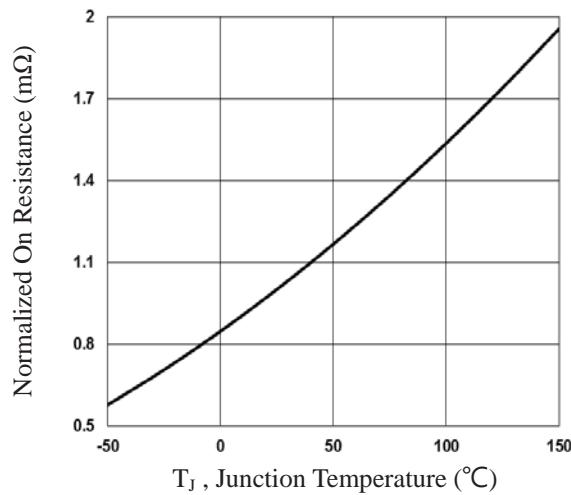


Fig.2 Normalized RDSON vs. T_j

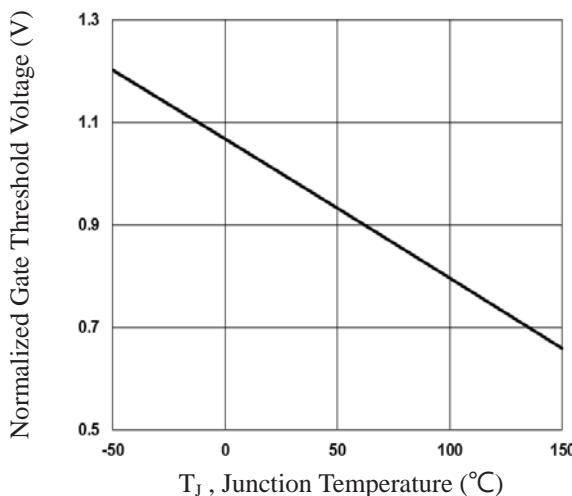


Fig.3 Normalized V_{th} vs. T_j

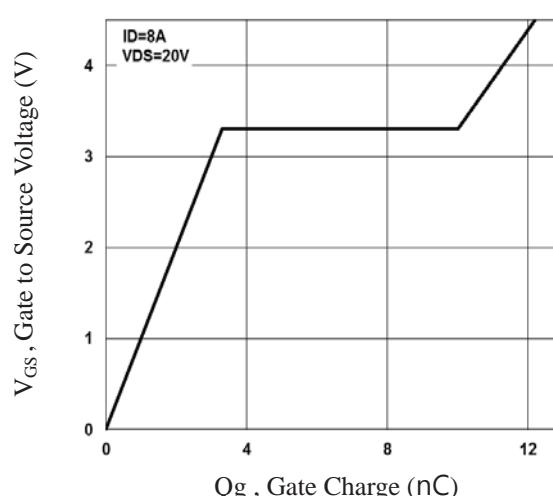


Fig.4 Gate Charge Waveform

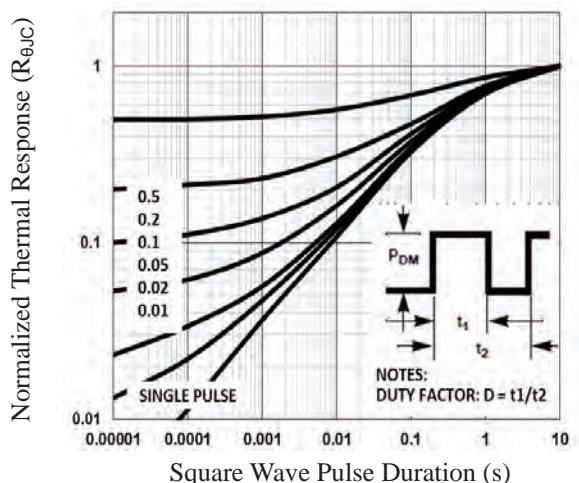


Fig.5 Normalized Transient Impedance

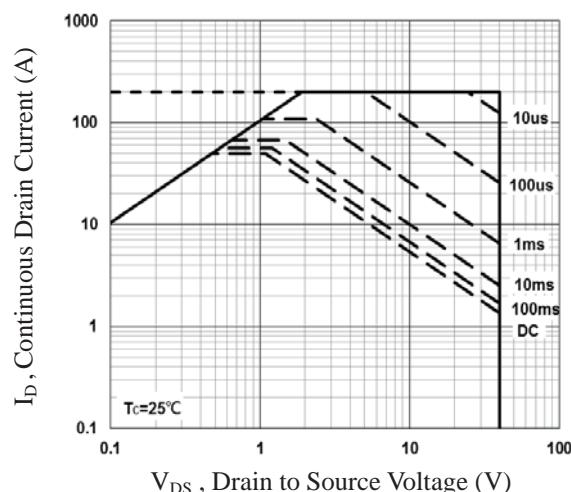


Fig.6 Maximum Safe Operation Area

ELECTRICAL CHARACTERISTICS CURVES (Con.)

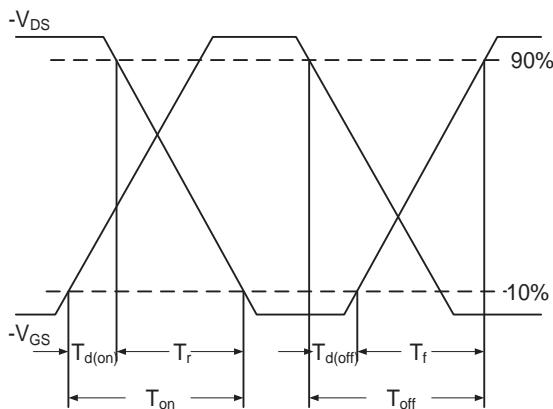


Fig.7 Switching Time Waveform

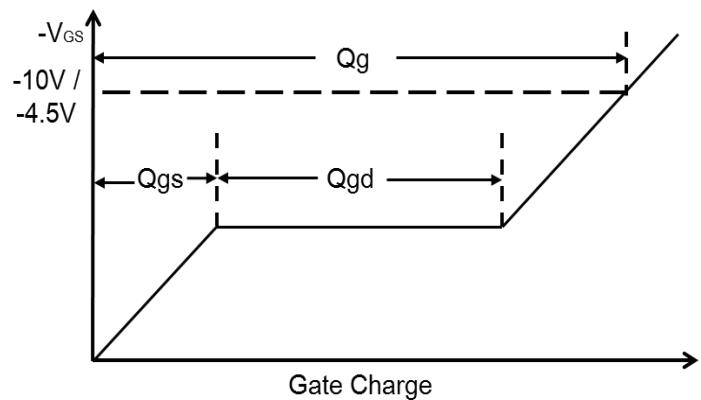
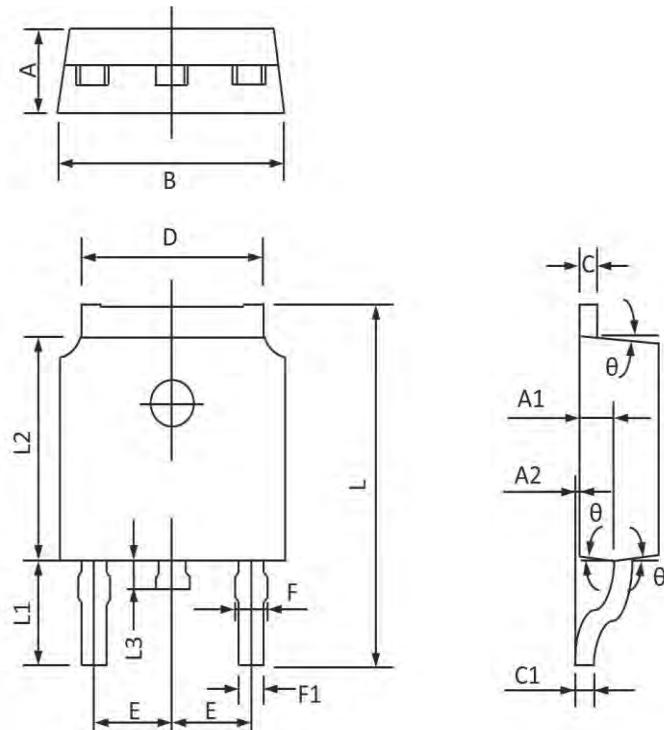


Fig.8 Gate Charge Waveform

TO-252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
A2	0.150	0.000	0.006	0.000
B	6.800	6.400	0.268	0.252
C	0.580	0.450	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.500	5.100	0.217	0.201
E	2.386	2.186	0.094	0.086
F	1.140	0.600	0.045	0.024
F1	0.880	0.500	0.035	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.223	5.400	0.245	0.213
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°