

60V P-Channel MOSFETs

General Description

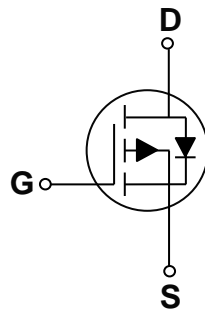
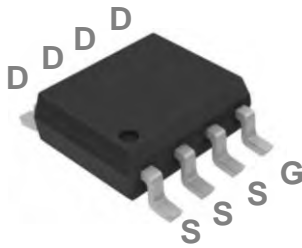
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-60V	30mΩ	-8.5A

Features

- -60V, -8.5A, $R_{DS(ON)} = 30m\Omega @ V_{GS} = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

SOP-8 Pin Configuration



Applications

- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _C =25 °C)	-8.5	A
	Drain Current – Continuous (T _C =100 °C)	-5.4	A
I _{DM}	Drain Current – Pulsed ¹	-34	A
EAS	Single Pulse Avalanche Energy ²	105	mJ
IAS	Single Pulse Avalanched Current ²	-46	A
P _D	Power Dissipation (T _C =25 °C)	4.1	W
	Power Dissipation – Derate above 25 °C	0.033	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJC}	Thermal Resistance Junction to Case	---	30	°C/W
R _{θJA}	Thermal Resistance Junction to Ambient	---	62	°C/W



FTK6903

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	-1	μA
		$V_{DS}=-48V, V_{GS}=0V, T_J=125^{\circ}\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-8A$	---	23	30	m Ω
		$V_{GS}=-4.5V, I_D=-6A$	---	28	40	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.0	-1.6	-2.5	V
gfs	Forward Transconductance	$V_{DS}=-10V, I_D=-3A$	---	18	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=-30V, V_{GS}=-10V, I_D=-5A$	---	43.8	88	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	4.6	9	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	8.3	17	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=-30V, V_{GS}=-10V, R_G=6\Omega$ $I_D=-1A$	---	25	50	ns
T_r	Rise Time ^{3, 4}		---	13.8	28	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	148	290	
T_f	Fall Time ^{3, 4}		---	51	100	
C_{iss}	Input Capacitance	$V_{DS}=-25V, V_{GS}=0V, F=1\text{MHz}$	---	2595	3900	pF
C_{oss}	Output Capacitance		---	162	240	
C_{rss}	Reverse Transfer Capacitance		---	115	170	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V, \text{Force Current}$	---	---	-8.5	A
I_{SM}	Pulsed Source Current		---	---	-17	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A, T_J=25^{\circ}\text{C}$	---	---	-1	V
t_{rr}	Reverse Recovery Time	$V_R=-50V, I_S=-5A$	---	40	---	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	30	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=-25V, V_{GS}=-10V, L=0.1\text{mH}, I_{AS}=-46A., R_G=25\Omega, \text{Starting } T_J=25^{\circ}\text{C}.$
3. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

ELECTRICAL CHARACTERISTICS CURVES

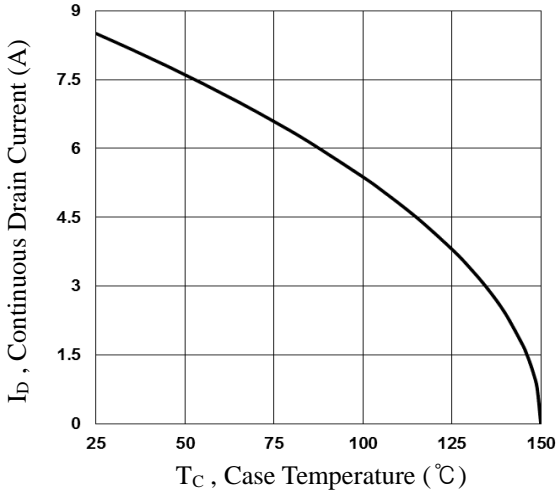


Fig.1 Continuous Drain Current vs. T_c

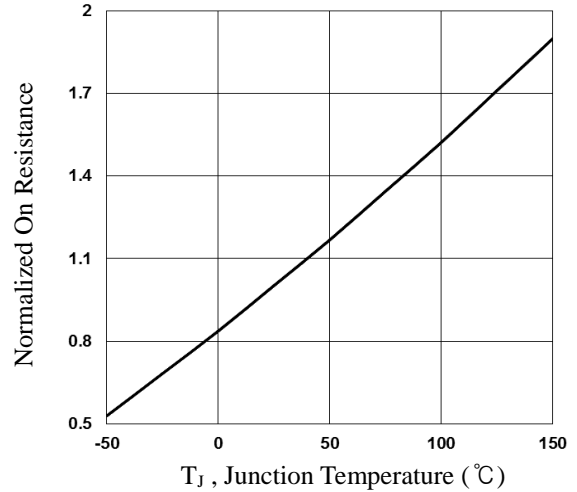


Fig.2 Normalized RDSON vs. T_j

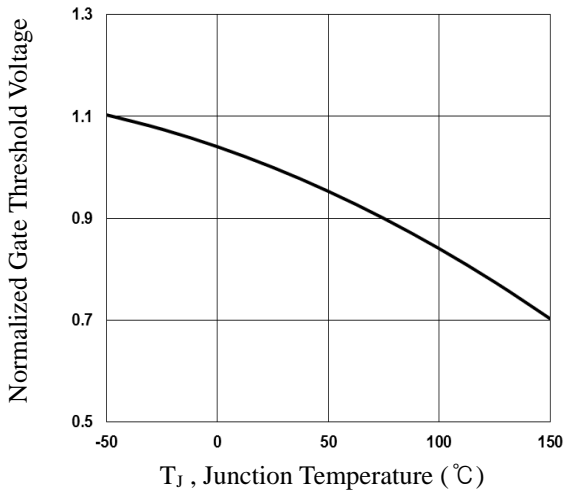


Fig.3 Normalized V_{th} vs. T_j

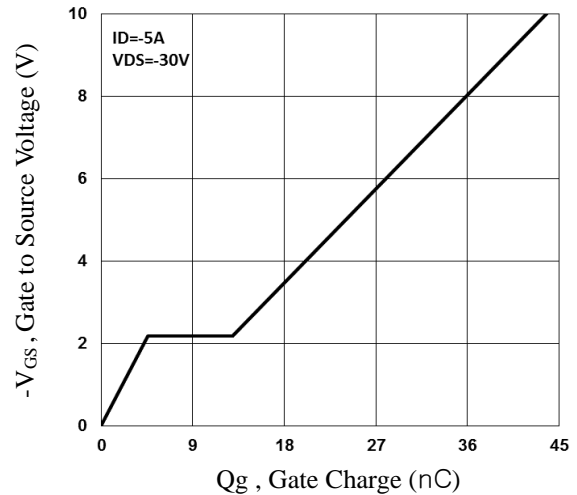


Fig.4 Gate Charge Waveform

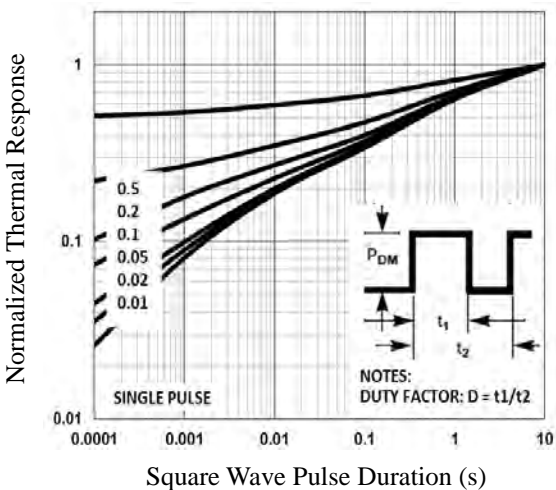


Fig.5 Normalized Transient Impedance

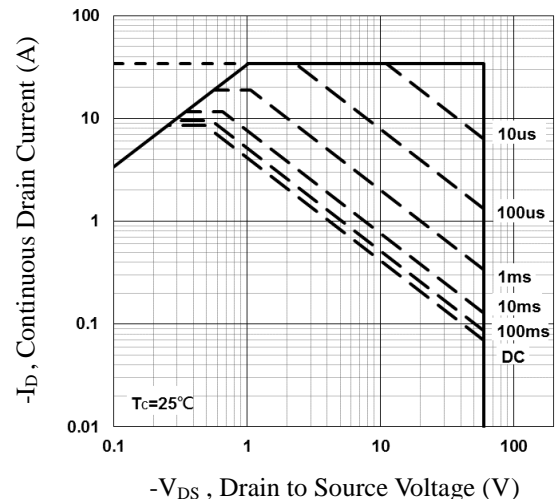


Fig.6 Maximum Safe Operation Area

ELECTRICAL CHARACTERISTICS CURVES (Con.)

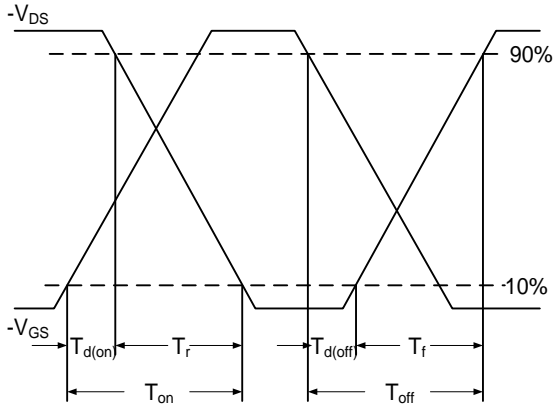


Fig.7 Switching Time Waveform

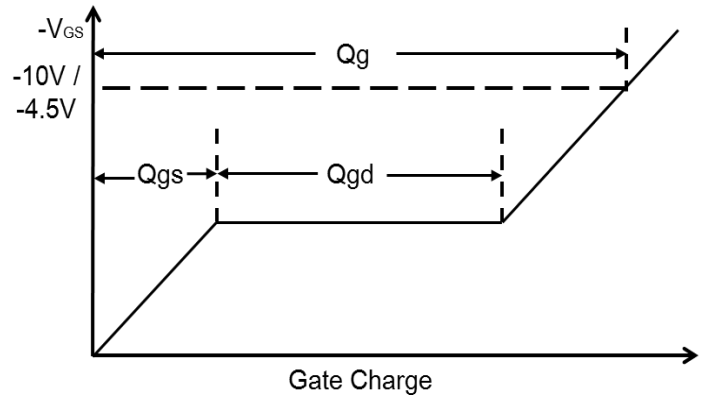
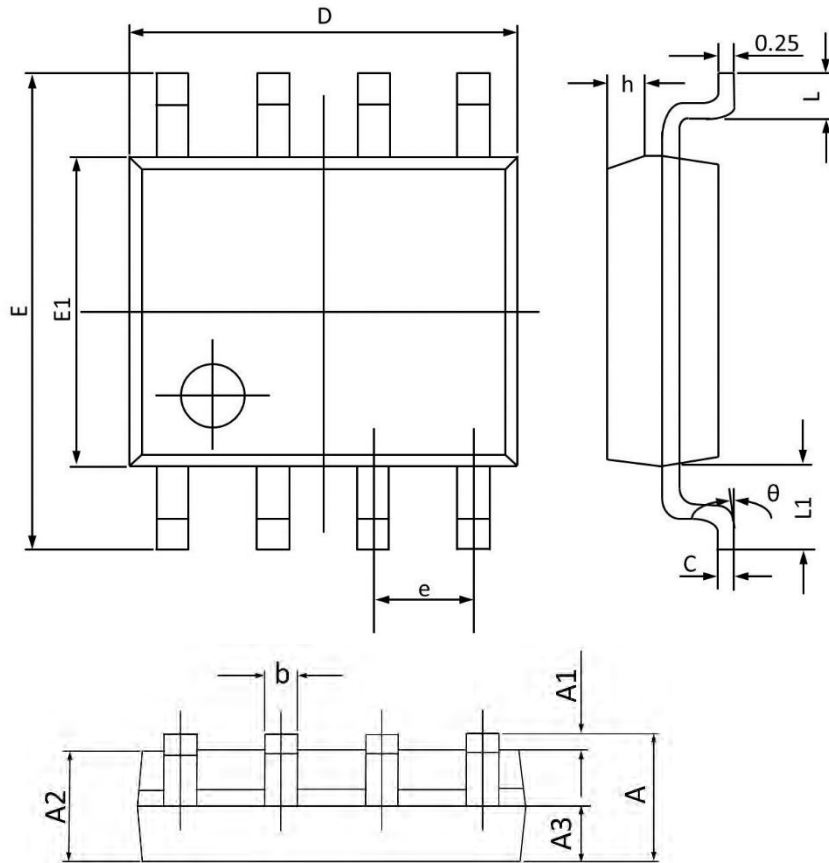


Fig.8 Gate Charge Waveform

SOP-8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.800	0.053	0.069
A1	0.050	0.250	0.002	0.010
A2	1.250	1.650	0.049	0.065
A3	0.500	0.700	0.020	0.028
b	0.300	0.510	0.012	0.020
c	0.150	0.260	0.006	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.020
L	0.400	1.000	0.016	0.039
L1	1.050(BSC)		0.041(BSC)	
θ	0°	8°	0°	8°