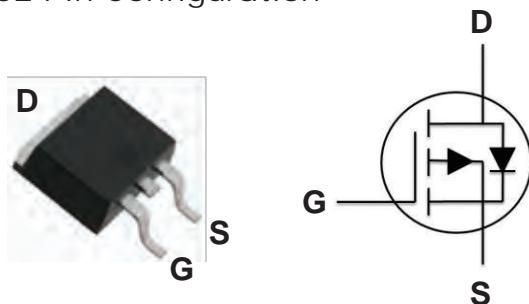


# 40V P-channel MOS FET

## General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

## TO-252 Pin Configuration



BVDSS	RDS(ON)	ID
-40V	15mΩ	-45A

## Features

- -40V, -45A, RDS(ON) 15mΩ @ VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

## Applications

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

## Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-40	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current – Continuous (Tc=25 °C)	-45	A
	Drain Current – Continuous (Tc=100 °C)	-28	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	-180	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	130	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	51	A
P <sub>D</sub>	Power Dissipation (Tc=25 °C)	73.5	W
	Power Dissipation – Derate above 25 °C	0.59	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJC</sub>	Thermal Resistance Junction to Case	---	1.7	°C/W
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	62	°C/W



# FTK4903D

## Electrical Characteristics ( $T_J=25^\circ\text{C}$ )

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-40	---	---	V
$\text{I}_{\text{DSS}}$	Drain-Source Leakage Current	$V_{\text{DS}}=-40\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
		$V_{\text{DS}}=-32\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	-10	$\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

### On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$ , $I_D=-10\text{A}$	---	11.5	15	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-8\text{A}$	---	16	22	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = -250\mu\text{A}$	-1.0	-1.6	-2.5	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$ , $I_D=-10\text{A}$	---	13	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{\text{DS}}=-32\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_D=-10\text{A}$	---	22.2	40	nC
$Q_{\text{gs}}$	Gate-Source Charge <sup>3, 4</sup>		---	8.2	16	
$Q_{\text{gd}}$	Gate-Drain Charge <sup>3, 4</sup>		---	8.8	16	
$T_{\text{d(on)}}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{\text{DD}}=-20\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_G=6\Omega$	---	23	40	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	10	20	
$T_{\text{d(off)}}$	Turn-Off Delay Time <sup>3, 4</sup>		---	135	250	
$T_f$	Fall Time <sup>3, 4</sup>		---	46	90	
$C_{\text{iss}}$	Input Capacitance	$V_{\text{DS}}=-25\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $F=1\text{MHz}$	---	2757	4000	pF
$C_{\text{oss}}$	Output Capacitance		---	240	360	
$C_{\text{rss}}$	Reverse Transfer Capacitance		---	137	200	

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	-45	A
$I_{\text{SM}}$	Pulsed Source Current		---	---	-90	A
$V_{\text{SD}}$	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{\text{DD}}=25\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{\text{AS}}=51\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed, pulse width  $\leq 300\text{us}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

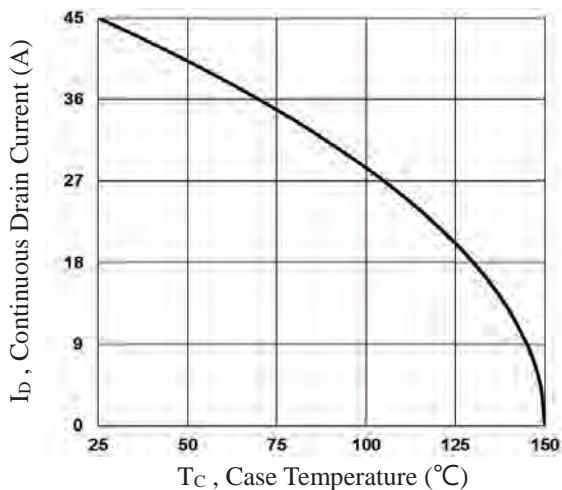
**ELECTRICAL CHARACTERISTICS CURVES**


Fig.1 Continuous Drain Current vs.  $T_c$

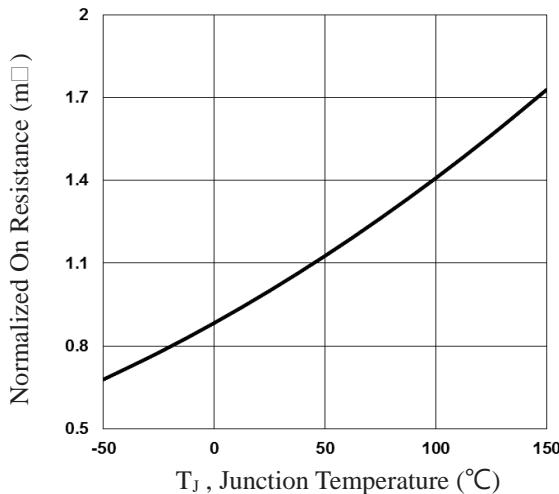


Fig.2 Normalized RDS(on) vs.  $T_j$

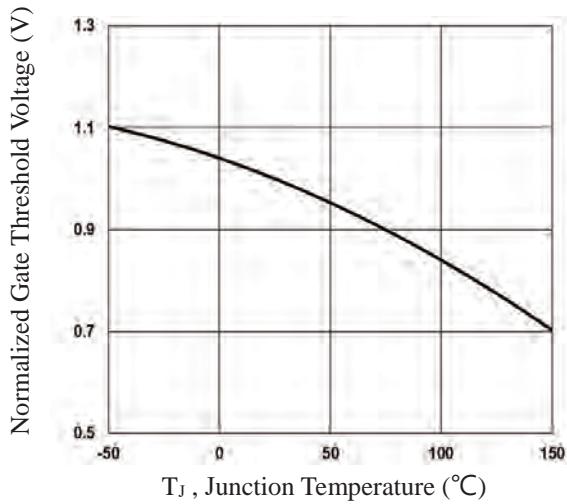


Fig.3 Normalized  $V_{th}$  vs.  $T_j$

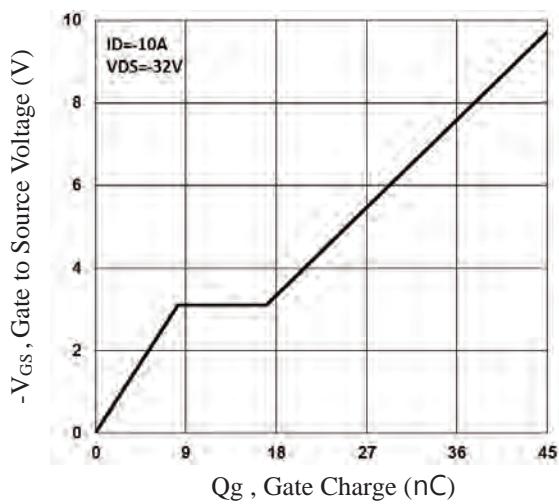


Fig.4 Gate Charge Waveform

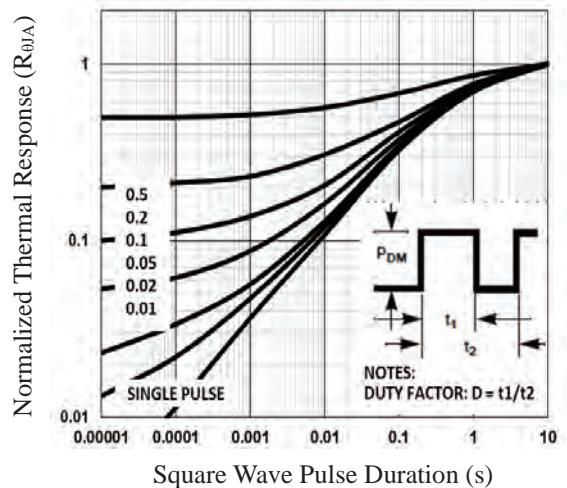


Fig.5 Normalized Transient Impedance

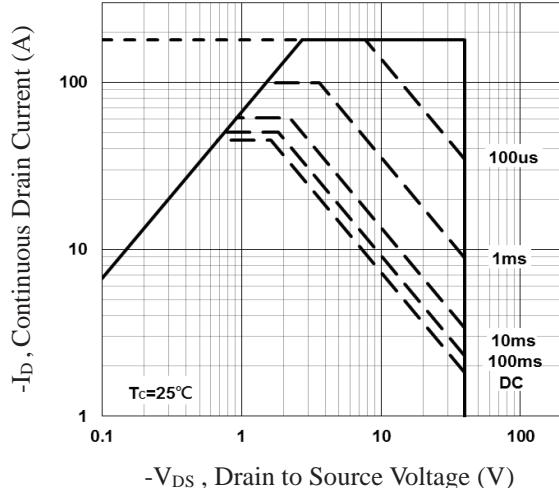


Fig.6 Maximum Safe Operation Area

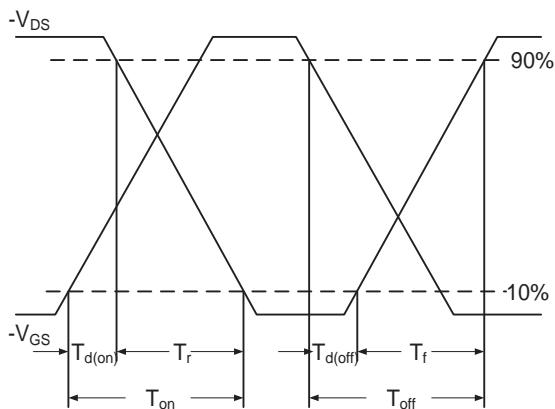
**ELECTRICAL CHARACTERISTICS CURVES (Con.)**


Fig.7 Switching Time Waveform

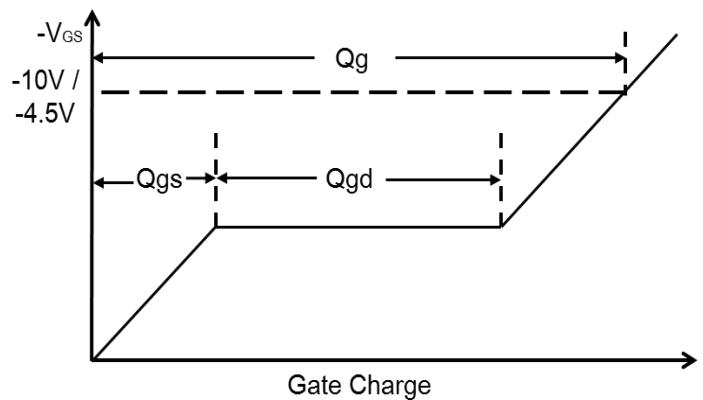
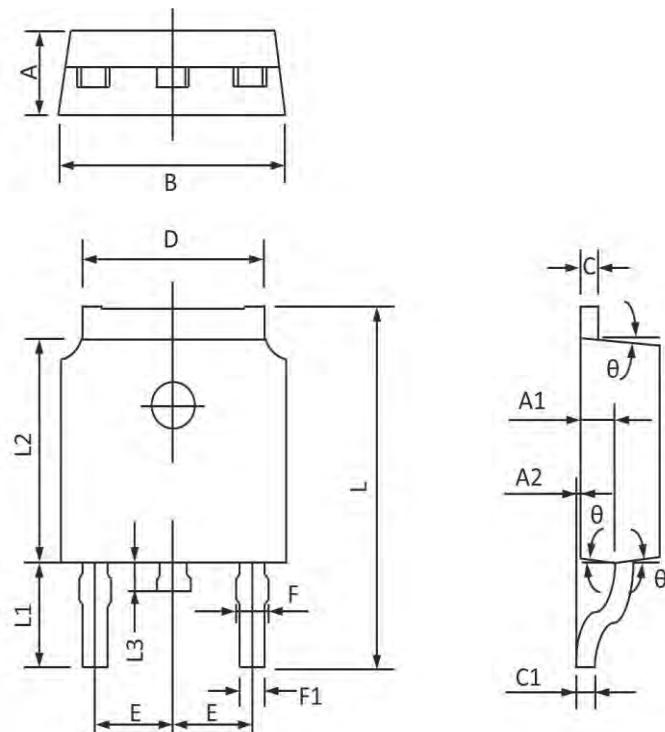


Fig.8 Gate Charge Waveform

## TO-252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
A2	0.150	0.000	0.006	0.000
B	6.800	6.400	0.268	0.252
C	0.580	0.450	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.500	5.100	0.217	0.201
E	2.386	2.186	0.094	0.086
F	1.140	0.600	0.045	0.024
F1	0.880	0.500	0.035	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.223	5.400	0.245	0.213
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°