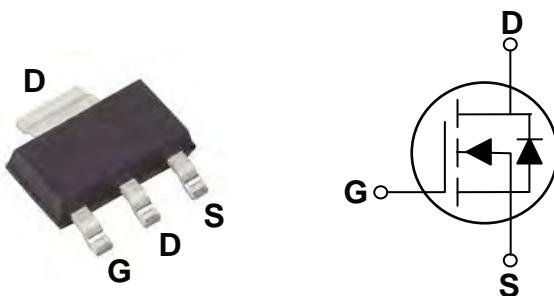


## 60V N-Channel MOSFETs

### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### SOT223 Pin Configuration



BVDSS	RDS(ON)	ID
60V	75mΩ	5A

### Features

- 60V,5A, RDS(ON) =75mΩ @VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available
- AEC-Q101 qualified

### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ\text{C}$ )	5	A
	Drain Current – Continuous ( $T_c=100^\circ\text{C}$ )	3.2	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	20	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	25	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	7	A
$P_D$	Power Dissipation ( $T_c=25^\circ\text{C}$ )	1.79	W
	Power Dissipation – Derate above 25°C	0.014	W/°C
$T_{STG}$	Storage Temperature Range	-50 to 150	°C
$T_J$	Operating Junction Temperature Range	-50 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	70	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	30	°C/W



# FTK6912T

## 60V N-Channel MOSFETs

Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.05	---	$\text{V}/^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=60\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=48\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$

### On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$ , $I_D=5\text{A}$	---	60	75	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=3\text{A}$	---	70	90	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=250\mu\text{A}$	1.2	1.8	2.5	V
			---	-5	---	$\text{mV}/^\circ\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=3\text{A}$	---	7	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{DS}=48\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=5\text{A}$	---	9.3	14	nC
$Q_{gs}$	Gate-Source Charge <sup>2,3</sup>		---	2.1	4	
$Q_{gd}$	Gate-Drain Charge <sup>2,3</sup>		---	1.8	4	
$T_{d(on)}$	Turn-On Delay Time <sup>2,3</sup>	$V_{DD}=30\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=3.3\Omega$ $I_D=1\text{A}$	---	2.9	6	ns
$T_r$	Rise Time <sup>2,3</sup>		---	9.5	18	
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>		---	18.4	35	
$T_f$	Fall Time <sup>2,3</sup>		---	5.3	10	
$C_{iss}$	Input Capacitance	$V_{DS}=15\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	500	725	pF
$C_{oss}$	Output Capacitance		---	45	65	
$C_{rss}$	Reverse Transfer Capacitance		---	16	30	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	2	4	$\Omega$

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	5	A
			---	---	20	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_S=1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	1	V
$t_{rr}$	Reverse Recovery Time <sup>2</sup>	$V_{GS}=30\text{V}$ , $I_S=1\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$	---	23.2	---	ns
$Q_{rr}$	Reverse Recovery Charge <sup>2</sup>	$T_J=25^\circ\text{C}$	---	14.3	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=1\text{mH}$ ,  $I_{AS}=7\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$
3. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

## 60V N-Channel MOSFETs

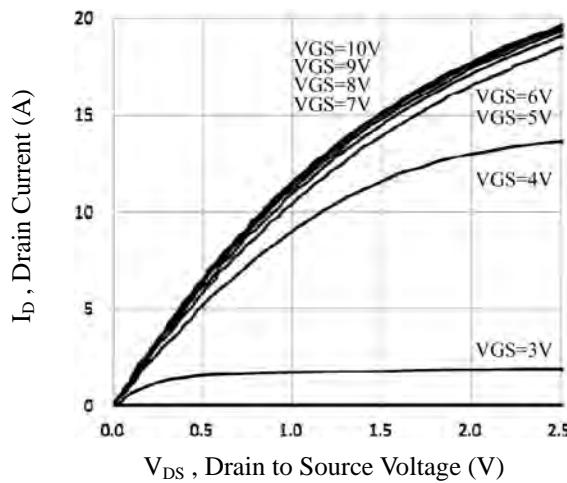


Fig.1 Typical Output Characteristics

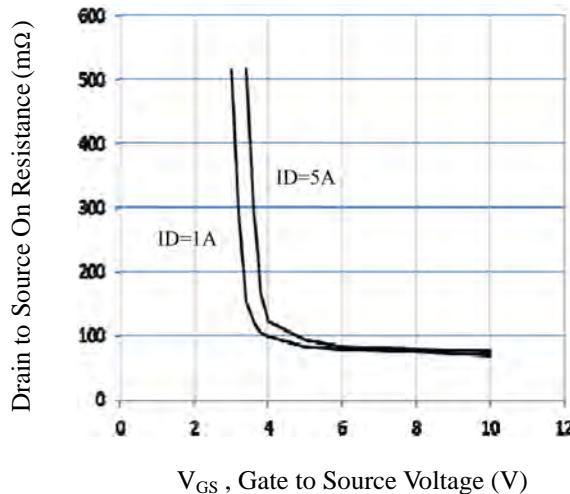


Fig.2 RDSON vs. Gate Voltage

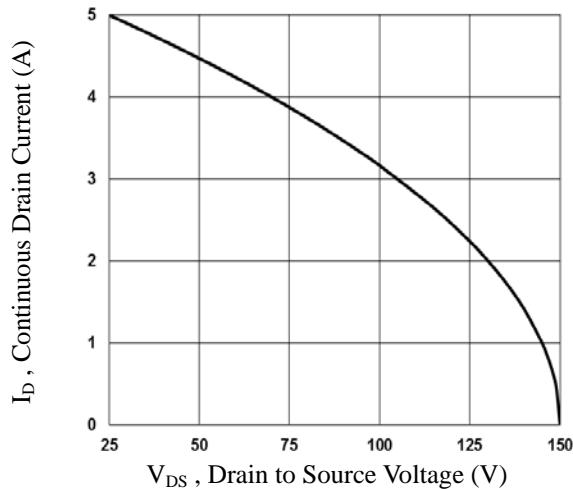


Fig.3 Output Characteristics

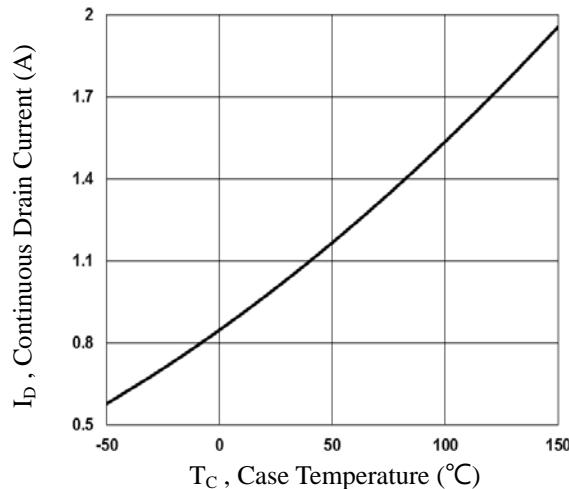


Fig.4 Continuous Drain Current vs.  $T_C$

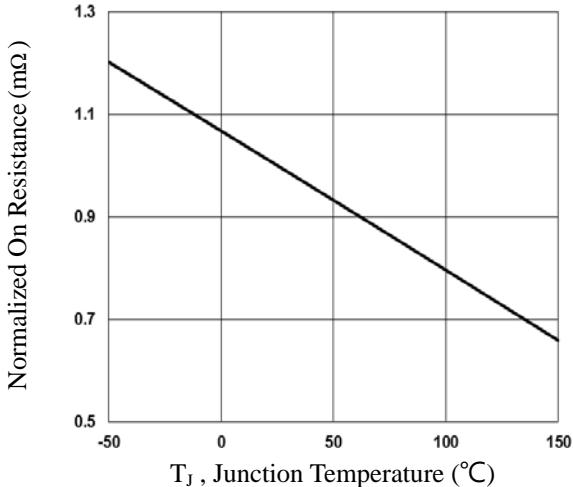


Fig.5 Normalized RDSON vs.  $T_J$

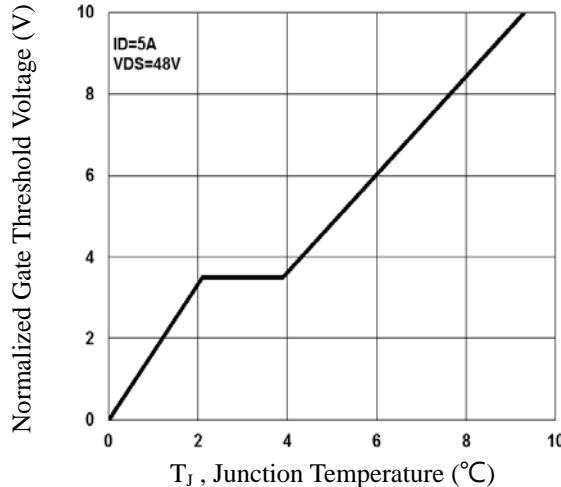


Fig.6 Normalized  $V_{th}$  vs.  $T_J$

## 60V N-Channel MOSFETs

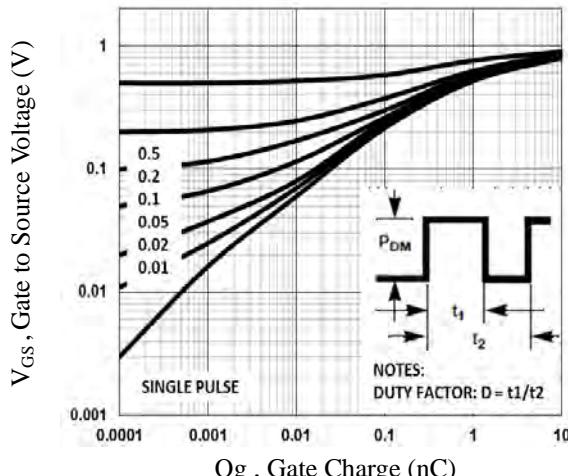


Fig.7 Gate Charge Waveform

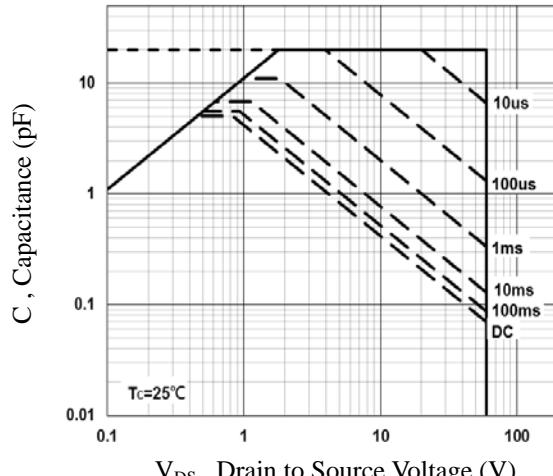


Fig.8 Capacitance Characteristics

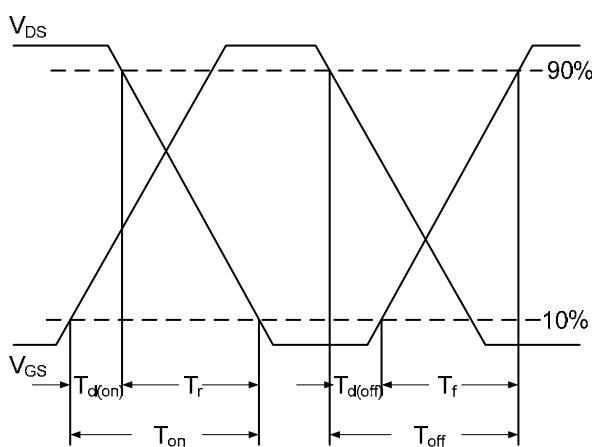


Fig.9 Switching Time Waveform

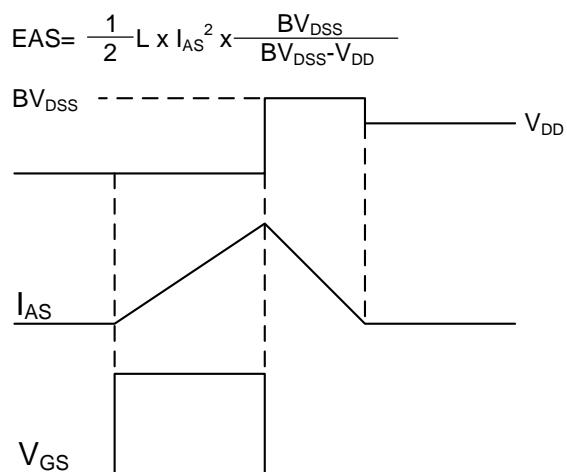
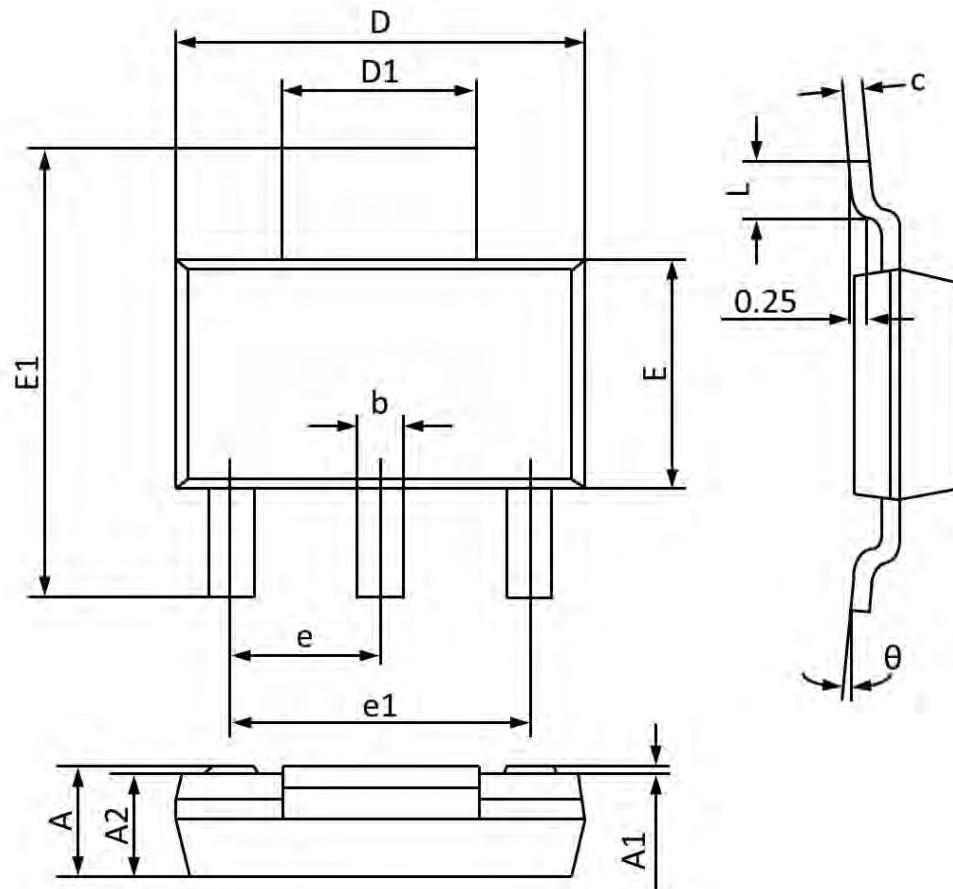


Fig.10 EAS Waveform

## 60V N-Channel MOSFETs

## SOT223 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.520	1.800	0.060	0.071
A1	0.000	0.100	0.000	0.004
A2	1.500	1.700	0.059	0.067
b	0.660	0.820	0.026	0.032
c	0.250	0.350	0.010	0.014
D	6.200	6.400	0.244	0.252
D1	2.900	3.100	0.114	0.122
E	3.300	3.700	0.130	0.146
E1	6.830	7.070	0.269	0.278
e	2.300 (BSC)		0.091 (BSC)	
e1	4.500	4.700	0.177	0.185
L	0.900	1.150	0.035	0.045
θ	0°	10°	0°	10°