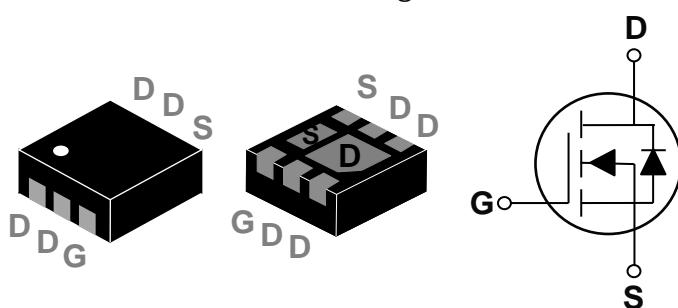


30V N-Channel MOSFETs

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

DFN2x2-6L 2EP Pin Configuration



BVDSS	RDS(ON)	ID
30V	17mΩ	8.5A

Features

- 30V, 8.5A, RDS(ON) = 17mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{Gs}	Gate-Source Voltage	± 20	V
I _D	Drain Current – Continuous (T _A =25 °C)	8.5	A
	Drain Current – Continuous (T _A =70 °C)	6.8	A
I _{DM}	Drain Current – Pulsed ¹	34	A
EAS	Single Pulse Avalanche Energy ²	13	mJ
IAS	Single Pulse Avalanche Current ²	16	A
P _D	Power Dissipation (T _c =25 °C)	2.01	W
	Power Dissipation – Derate above 25 °C	0.016	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W



FTK3910DFN22

30V N-Channel MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.04	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance ³	$V_{GS}=10\text{V}$, $I_D=6\text{A}$	---	13.5	17	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=4\text{A}$	---	17	22	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.2	1.8	2.5	V
			---	-4	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=10\text{V}$, $I_D=3\text{A}$	---	6	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=15\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=5\text{A}$	---	7.4	12	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	2.3	5	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	3	6	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=15\text{V}$, $V_{GS}=10\text{V}$, $R_G=6\Omega$ $I_D=1\text{A}$	---	3.8	7	ns
T_r	Rise Time ^{3, 4}		---	10	19	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	22	42	
T_f	Fall Time ^{3, 4}		---	6.6	13	
C_{iss}	Input Capacitance	$V_{DS}=25\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$	---	620	900	pF
C_{oss}	Output Capacitance		---	85	125	
C_{rss}	Reverse Transfer Capacitance		---	60	90	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $f=1\text{MHz}$	---	2.8	5.6	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	8.5	A
			---	---	17	A
V_{SD}	Diode Forward Voltage ³	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{DD}=25\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=16\text{A}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
- The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

30V N-Channel MOSFETs

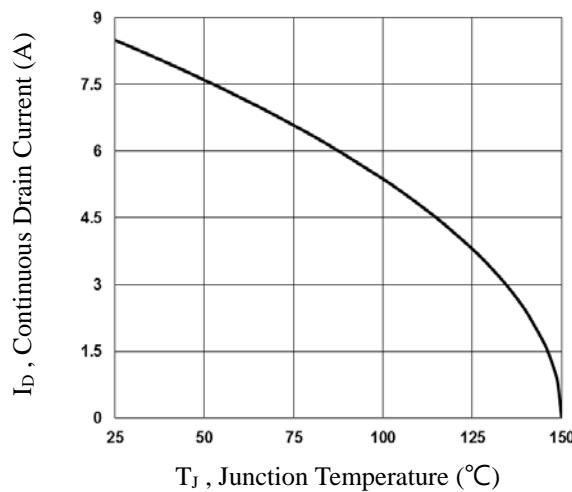


Fig.1 Continuous Drain Current vs. T_J

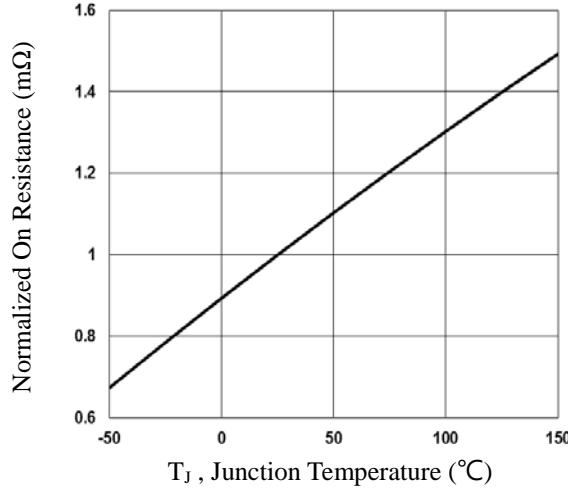


Fig.2 Normalized RDSON vs. T_J

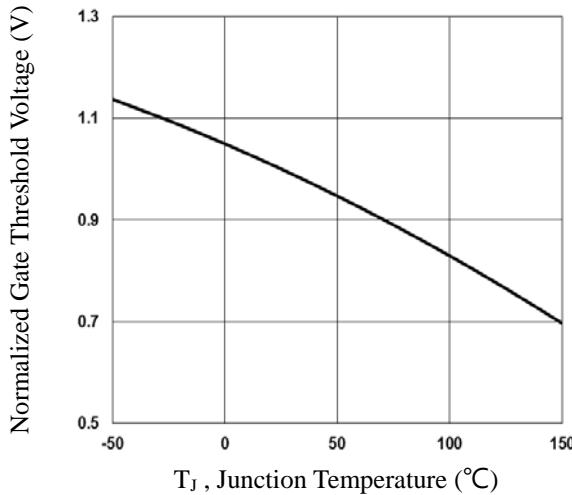


Fig.3 Normalized V_{th} vs. T_J

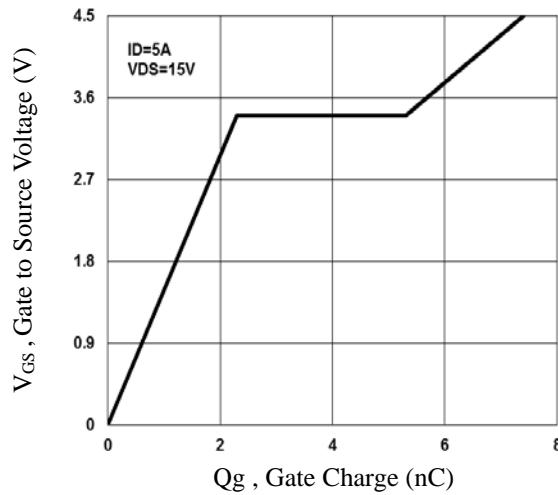


Fig.4 Gate Charge Waveform

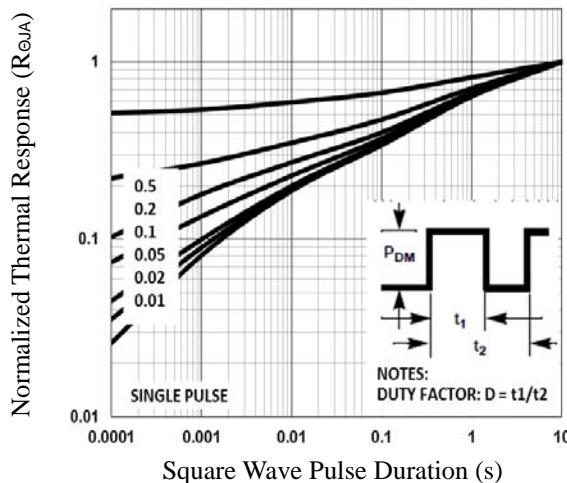


Fig.5 Normalized Transient Response

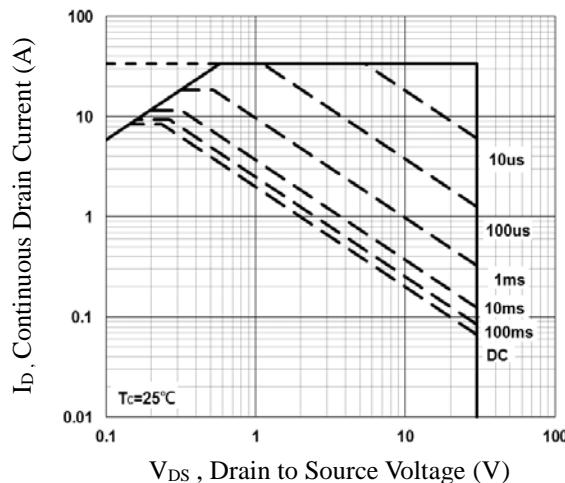


Fig.6 Maximum Safe Operation Area

30V N-Channel MOSFETs

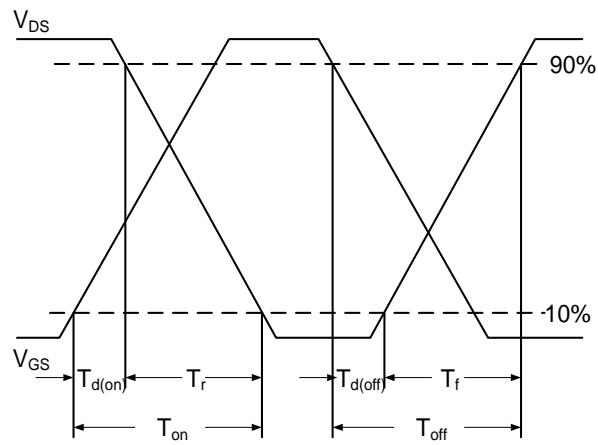


Fig.7 Switching Time Waveform

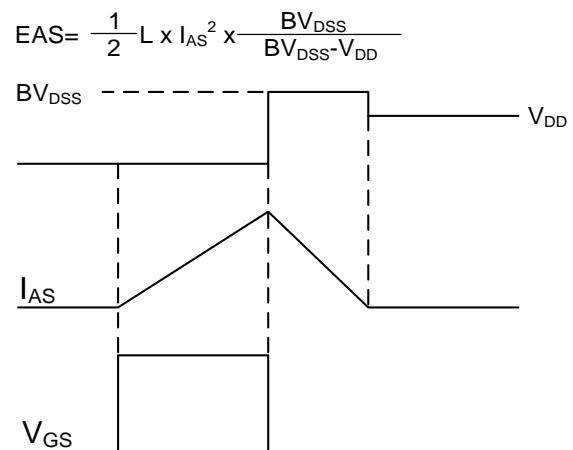
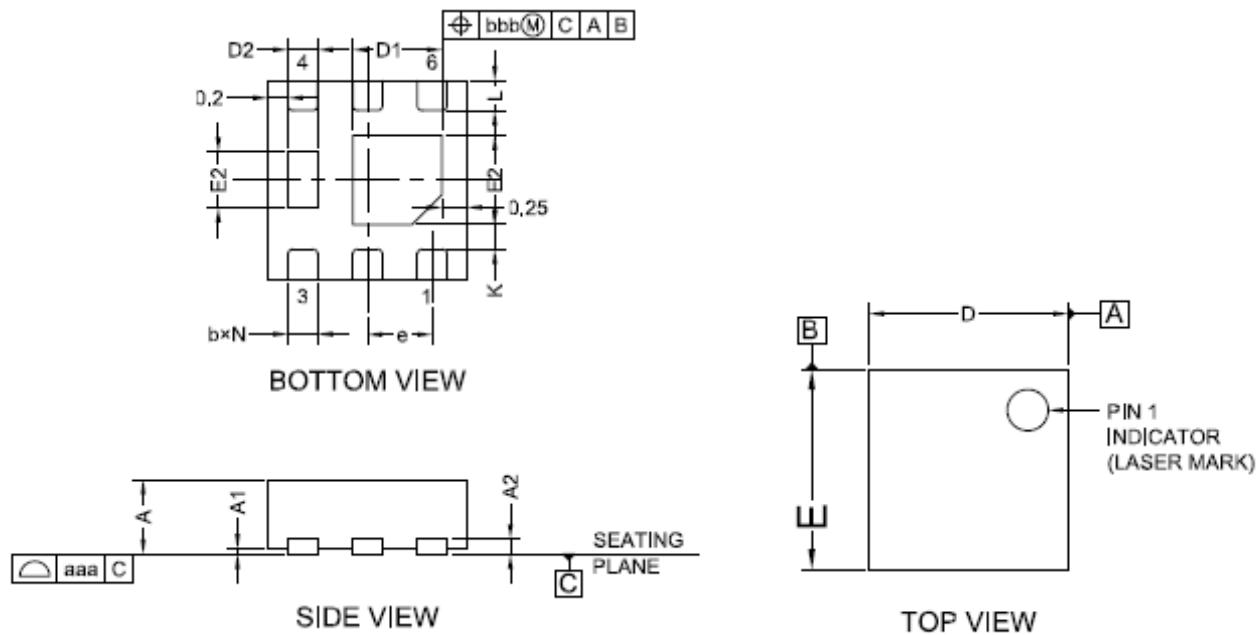


Fig.8 EAS Waveform

30V N-Channel MOSFETs

DFN2X2-6L 2EP PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		
	Min	Typ	Max
A	0.50	0.55	0.60
A1	0.00	0.02	0.05
A2	0.152REF		
b	0.25	0.30	0.35
D	1.95	2.00	2.05
D1	0.80	0.90	1.00
D2	0.25	0.30	0.35
E	1.95	2.00	2.05
E1	0.80	0.90	1.00
E2	0.46	0.56	0.66
e	0.65BSC		
L	0.25	0.30	0.35
J	0.40BSC		
K	0.20MIN		
N	6.00		
aaa	0.08		
bbb	0.10		