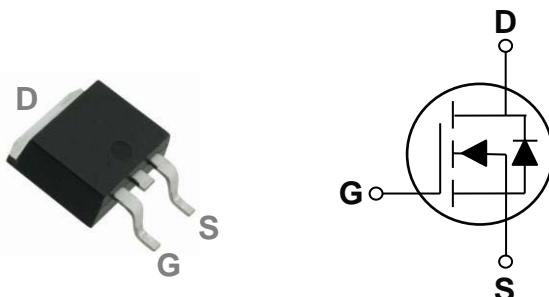


40V N-Channel MOSFETs

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO-252 Pin Configuration



BVDSS	RDS(ON)	ID
40V	30mΩ	23A

Features

- 40V,23A, RDS(ON) =30mΩ @VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- 100% PB free and Green Device Available

Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	40	V
V _{Gs}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _c =25°C)	23	A
	Drain Current – Continuous (T _c =100°C)	14.5	A
I _{DM}	Drain Current – Pulsed ¹	92	A
P _D	Power Dissipation (T _c =25°C)	25	W
	Power Dissipation – Derate above 25°C	0.2	W/°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
T _J	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	5	°C/W



FTK4912D

40V N-Channel MOSFETs

Electrical Characteristics ($T_J=25\text{ }^{\circ}\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	100	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=40\text{V}$, $V_{GS}=0\text{V}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	μA
		$V_{DS}=32\text{V}$, $V_{GS}=0\text{V}$, $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=10\text{V}$, $I_D=12\text{A}$	---	25	30	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$, $I_D=10\text{A}$	---	32	40	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=250\mu\text{A}$	1.2	1.6	2.5	V
$`g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$, $I_D=3\text{A}$	---	9	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2, 3}	$V_{DS}=20\text{V}$, $V_{GS}=4.5\text{V}$, $I_D=5\text{A}$	---	4.5	10	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	1.5	2.4	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	1.9	5	
$T_{d(on)}$	Turn-On Delay Time ^{2, 3}	$V_{DD}=20\text{V}$, $V_{GS}=4.5\text{V}$, $R_G=25\Omega$	---	3.2	6	ns
T_r	Rise Time ^{2, 3}		---	8.6	16	
$T_{d(off)}$	Turn-Off Delay Time ^{2, 3}		---	18	36	
T_f	Fall Time ^{2, 3}		---	6	12	
C_{iss}	Input Capacitance	$V_{DS}=25\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	420	800	pF
C_{oss}	Output Capacitance		---	65	120	
C_{rss}	Reverse Transfer Capacitance		---	40	80	
R_g	Gate resistance	$V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$	---	1.2	2.4	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	23	A
			---	---	46	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=1\text{A}$, $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

40V N-Channel MOSFETs

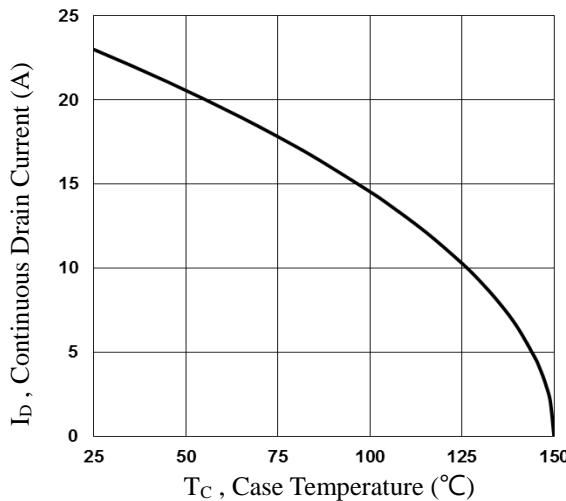


Fig.1 Continuous Drain Current vs. TC

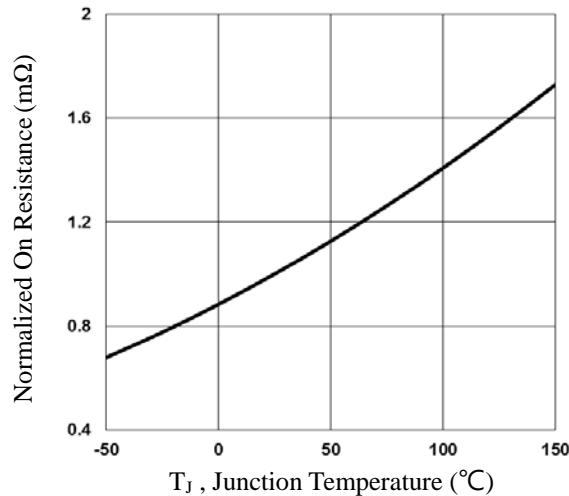


Fig.2 Normalized RDSON vs. TJ

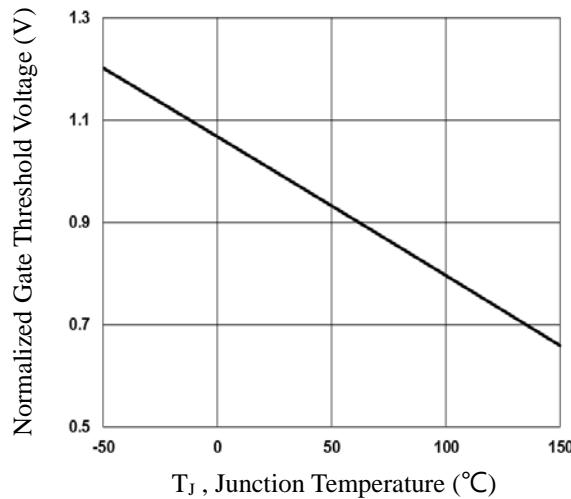


Fig.3 Normalized V_{th} vs. TJ

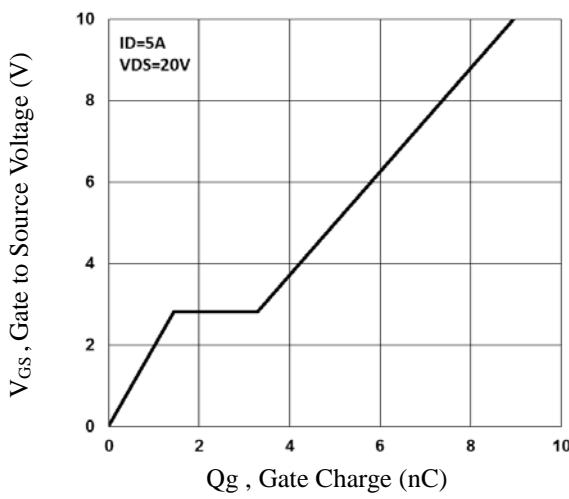


Fig.4 Gate Charge Characteristics

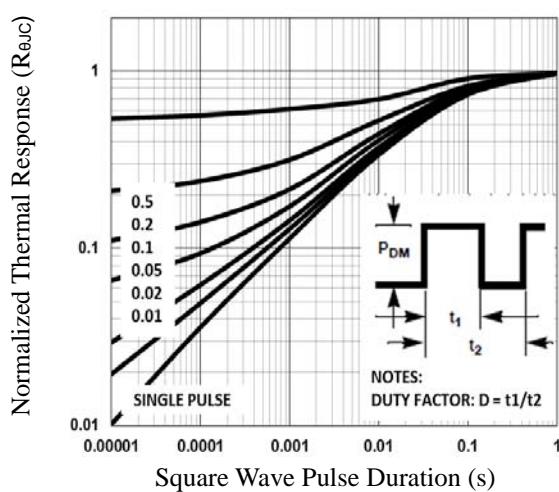


Fig.5 Normalized Transient Impedance

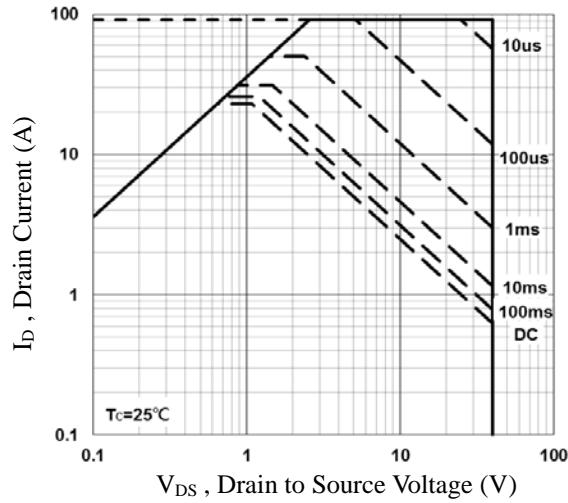


Fig.6 Maximum Safe Operation Area

40V N-Channel MOSFETs

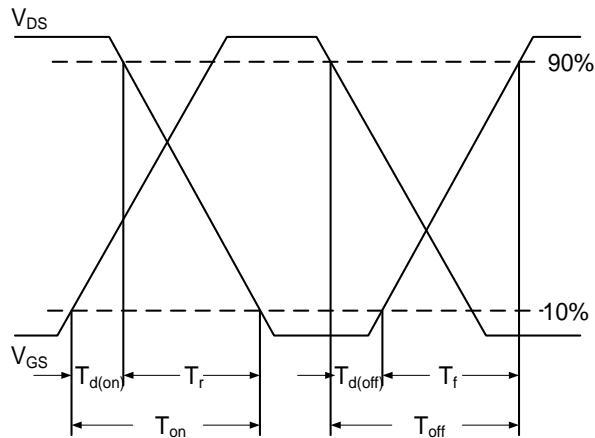


Fig.7 Switching Time Waveform

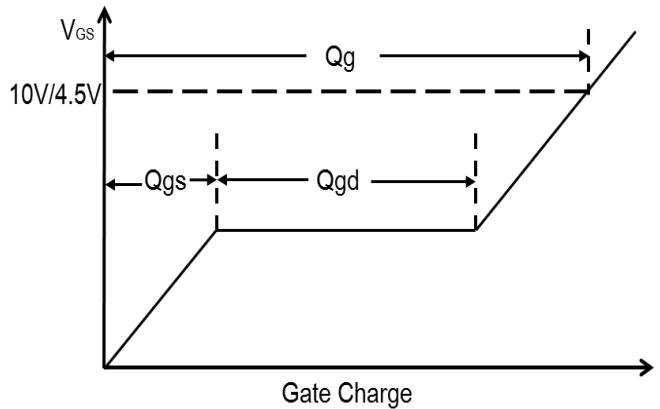
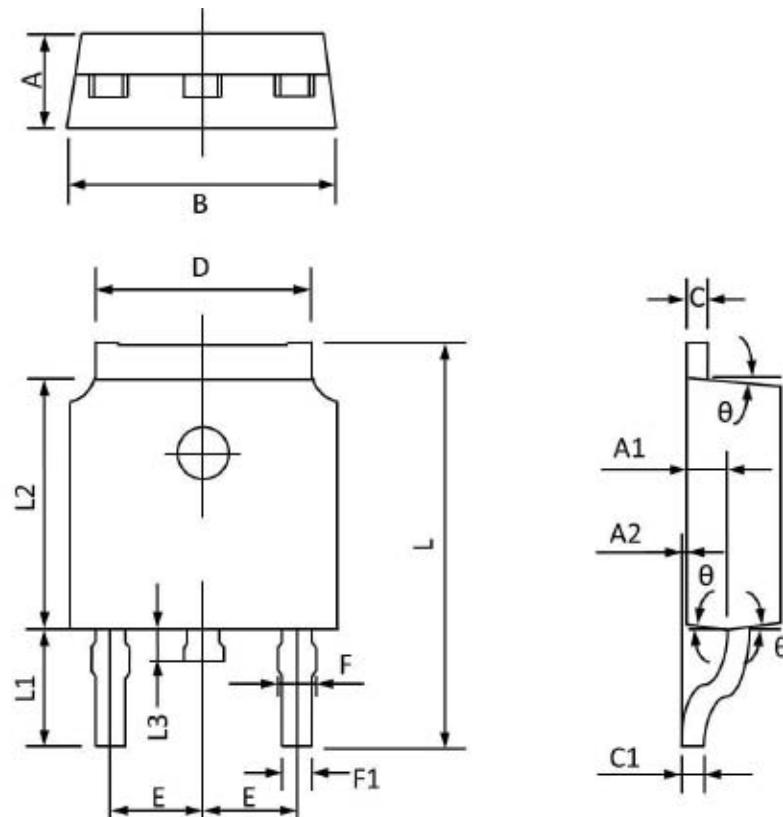


Fig.8 Gate Charge Waveform

40V N-Channel MOSFETs

TO-252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.20	2.40	0.087	0.094
A1	0.91	1.11	0.036	0.044
A2	0.00	0.15	0.000	0.006
B	6.50	6.70	0.256	0.264
C	0.46	0.580	0.018	0.230
C1	0.46	0.580	0.018	0.030
D	5.10	5.46	0.201	0.215
E	2.186	2.386	0.086	0.094
F	0.74	0.94	0.029	0.037
F1	0.660	0.860	0.026	0.034
L	9.80	10.40	0.386	0.409
L1	2.9REF		0.114REF	
L2	6.00	6.20	0.236	0.244
L3	0.60	1.00	0.024	0.039
θ	3°	9°	3°	9°