

60V P-Channel MOSFETs

General Description

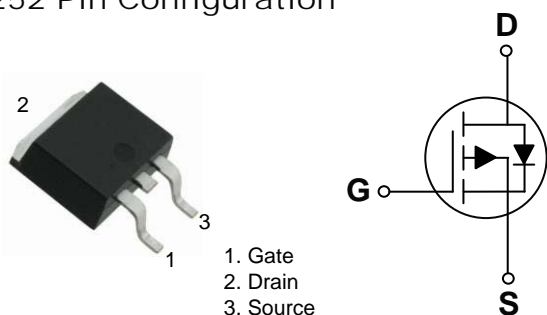
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
-60V	105mΩ	-10A

Features

- -60V, -10A, R_{DS(ON)} = 105mΩ @ V_{GS} = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO-252 Pin Configuration



Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings (T_C=25 °C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-Source Voltage	± 20	V
I _D	Drain Current – Continuous (T _C =25 °C)	-10	A
	Drain Current – Continuous (T _C =100 °C)	-6.3	A
I _{DM}	Drain Current – Pulsed ¹	-40	A
EAS	Single Pulse Avalanche Energy ²	25	mJ
IAS	Single Pulse Avalanche Current ²	-18	A
P _D	Power Dissipation (T _C =25 °C)	32	W
	Power Dissipation – Derate above 25 °C	0.25	W/°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
T _J	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	3.84	°C/W



FTK6909D

60V P-Channel MOSFETs

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$, $I_D=-1mA$	---	-0.05	---	V/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V, T_J=25\text{ }^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-48V, V_{GS}=0V, T_J=125\text{ }^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-6A$	---	87	105	m Ω
		$V_{GS}=-4.5V, I_D=-3A$	---	120	145	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.0	-1.6	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	3	---	mV/ $^\circ\text{C}$
gfs	Forward Transconductance	$V_{DS}=-10V, I_D=-6A$	---	5.5	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}	$V_{DS}=-30V, V_{GS}=-10V, I_D=-4A$	---	10	15	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	1.6	3.2	
Q_{gd}	Gate-Drain Charge ^{3,4}		---	3	6	
$T_{d(on)}$	Turn-On Delay Time ^{3,4}	$V_{DD}=-30V, V_{GS}=-10V, R_G=6\Omega, I_D=-1A$	---	8	16	ns
T_r	Rise Time ^{3,4}		---	15.4	30	
$T_{d(off)}$	Turn-Off Delay Time ^{3,4}		---	42.8	80	
T_f	Fall Time ^{3,4}		---	8.4	16	
C_{iss}	Input Capacitance	$V_{DS}=-30V, V_{GS}=0V, F=1MHz$	---	785	1300	pF
C_{oss}	Output Capacitance		---	175	300	
C_{rss}	Reverse Transfer Capacitance		---	112	220	
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	36	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	-10	A
I_{SM}	Pulsed Source Current		---	---	-20	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A, T_J=25\text{ }^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-18A, R_G=25\Omega$, Starting $T_J=25\text{ }^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

60V P-Channel MOSFETs

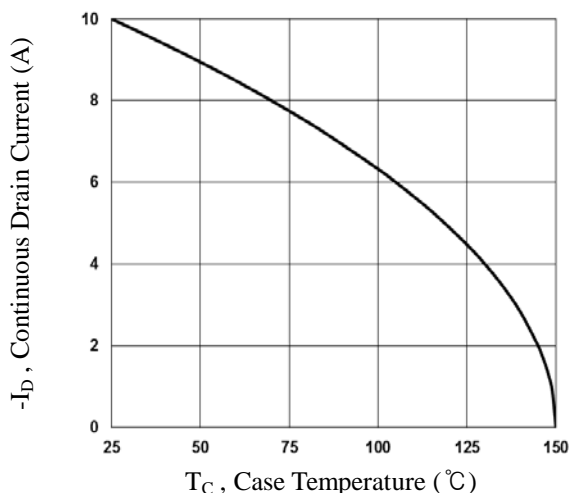


Fig.1 Continuous Drain Current vs. T_c

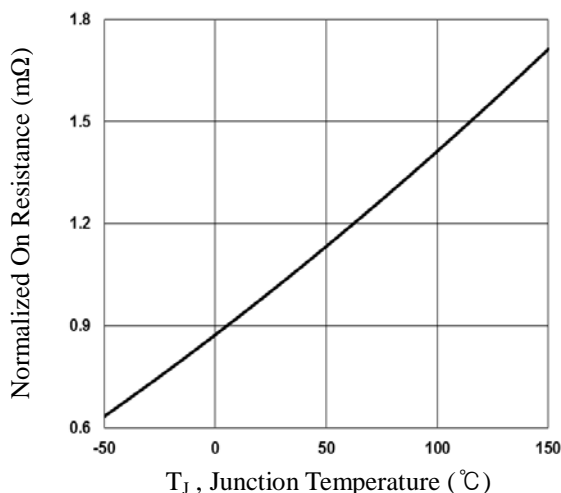


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

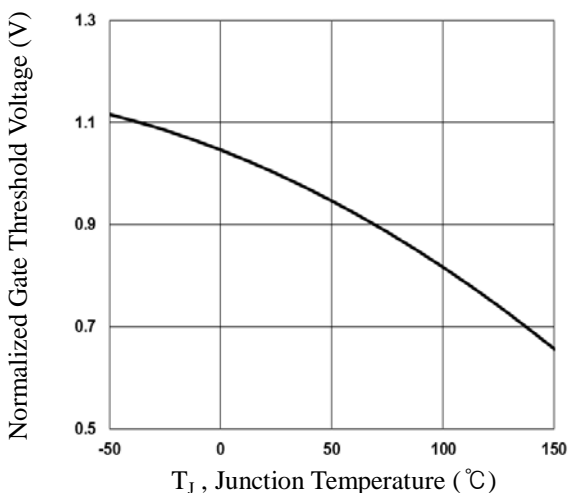


Fig.3 Normalized V_{th} vs. T_j

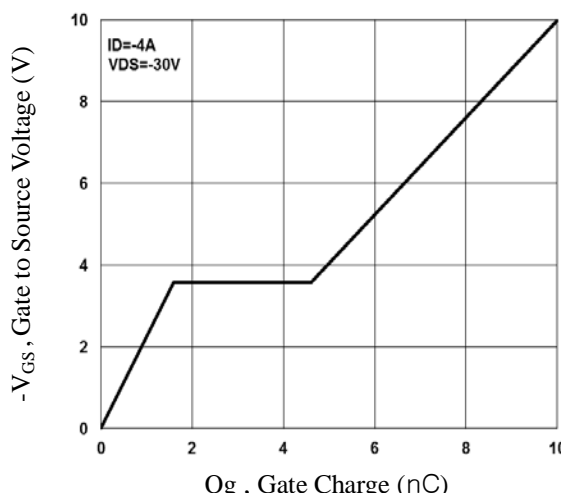


Fig.4 Gate Charge Waveform

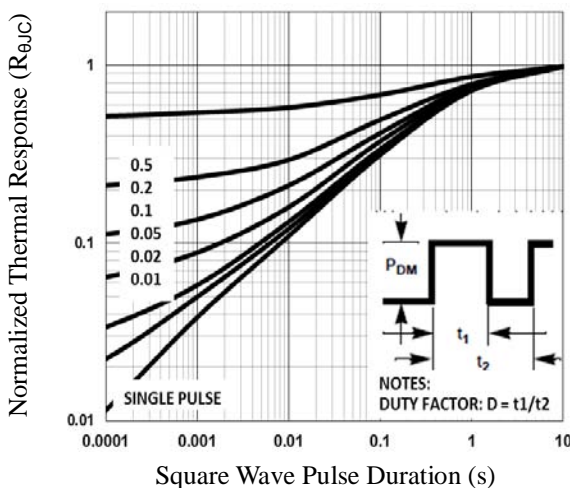


Fig.5 Normalized Transient Impedance

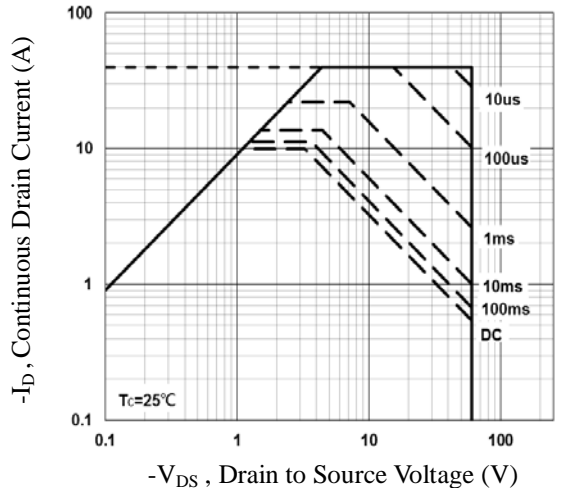


Fig.6 Maximum Safe Operation Area

60V P-Channel MOSFETs

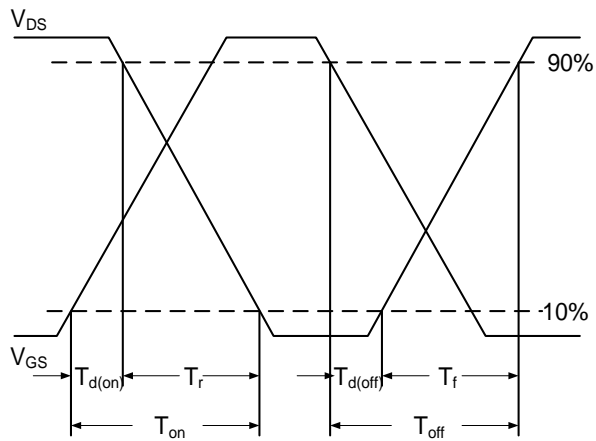


Fig.7 Switching Time Waveform

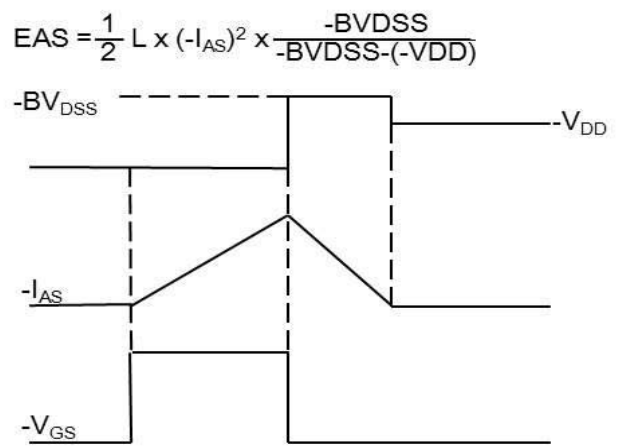
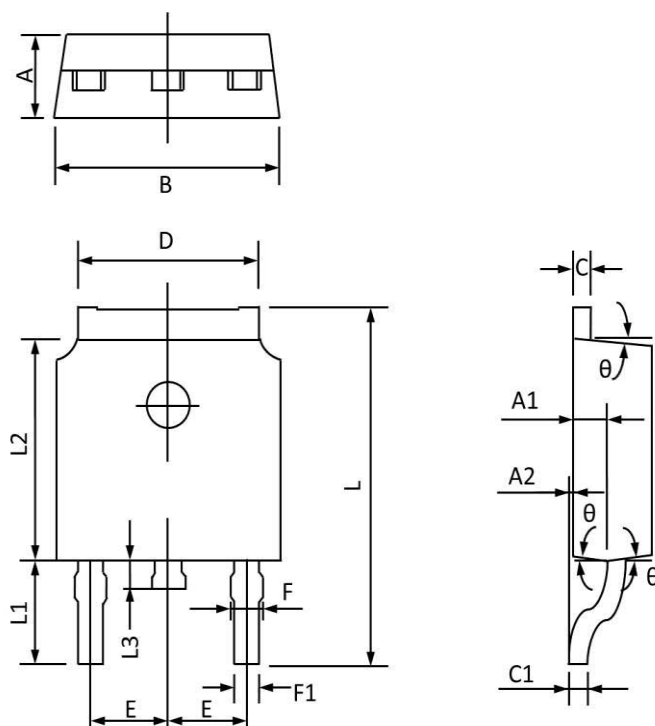


Fig.8 EAS Waveform

60V P-Channel MOSFETs

TO-252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
A2	0.150	0.000	0.006	0.000
B	6.800	6.400	0.268	0.252
C	0.580	0.450	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.500	5.100	0.217	0.201
E	2.386	2.186	0.094	0.086
F	0.940	0.600	0.037	0.024
F1	0.860	0.500	0.034	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.200	5.400	0.244	0.213
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°