

## 80V N-Channel MOSFETs

### General Description

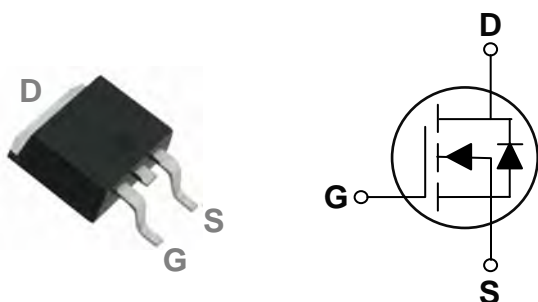
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
80V	12mΩ	60A

### Features

- 80V,60A,  $R_{DS(ON)} = 12m\Omega @ V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### TO-252 Pin Configuration



### Applications

- Networking
- Load Switch
- LED applications

### Absolute Maximum Ratings ( $T_C=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	80	V
$V_{GS}$	Gate-Source Voltage	$\pm 25$	V
$I_D$	Drain Current – Continuous ( $T_C=25^\circ C$ )	60	A
	Drain Current – Continuous ( $T_C=100^\circ C$ )	38	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	240	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	180	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	60	A
$P_D$	Power Dissipation ( $T_C=25^\circ C$ )	102	W
	Power Dissipation – Derate above $25^\circ C$	0.82	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.22	$^\circ C/W$



# FTK8966D

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Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	80	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$ , $I_D=1mA$	---	0.05	---	V/ $^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=80V, V_{GS}=0V, T_J=25\text{ }^\circ\text{C}$	---	---	1	$\mu A$
		$V_{DS}=64V, V_{GS}=0V, T_J=125\text{ }^\circ\text{C}$	---	---	10	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	---	---	$\pm 100$	nA

### On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=20A$	---	9.8	12	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	2	3	4	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-5	---	mV/ $^\circ\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=3A$	---	10	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>3,4</sup>	$V_{DS}=40V, V_{GS}=10V, I_D=10A$	---	31.2	48	nC
$Q_{gs}$	Gate-Source Charge <sup>3,4</sup>		---	9	18	
$Q_{gd}$	Gate-Drain Charge <sup>3,4</sup>		---	9.2	18	
$T_{d(on)}$	Turn-On Delay Time <sup>3,4</sup>	$V_{DD}=40V, V_{GS}=10V, R_G=6\Omega$ $I_D=1A$	---	22	44	ns
$T_r$	Rise Time <sup>3,4</sup>		---	16	32	
$T_{d(off)}$	Turn-Off Delay Time <sup>3,4</sup>		---	40	80	
$T_f$	Fall Time <sup>3,4</sup>		---	31	62	
$C_{iss}$	Input Capacitance	$V_{DS}=30V, V_{GS}=0V, F=1MHz$	---	1810	2700	pF
$C_{oss}$	Output Capacitance		---	252	380	
$C_{rss}$	Reverse Transfer Capacitance		---	100	150	
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	1.45	2.9	$\Omega$

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	60	A
$I_{SM}$	Pulsed Source Current		---	---	120	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25\text{ }^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=60A$ , Starting  $T_J=25\text{ }^\circ\text{C}$
3. The data tested by pulsed , pulse width  $\leq 300\mu s$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

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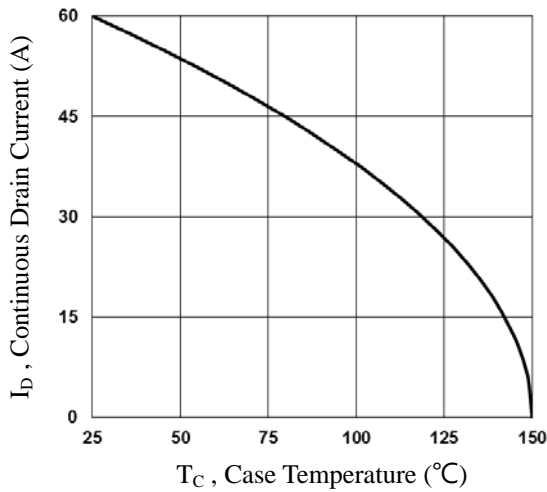


Fig.1 Continuous Drain Current vs. T<sub>c</sub>

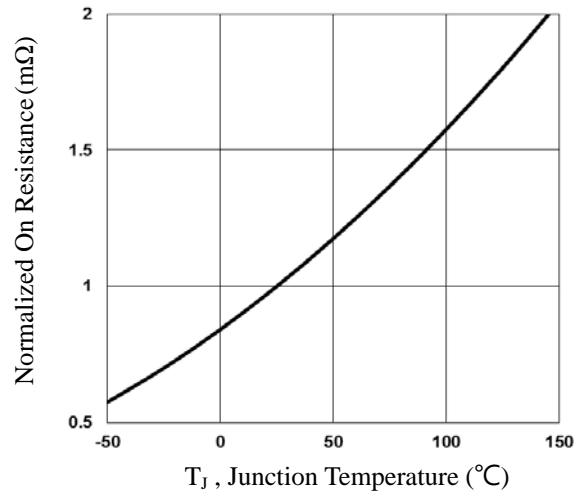


Fig.2 Normalized R<sub>DS(on)</sub> vs. T<sub>j</sub>

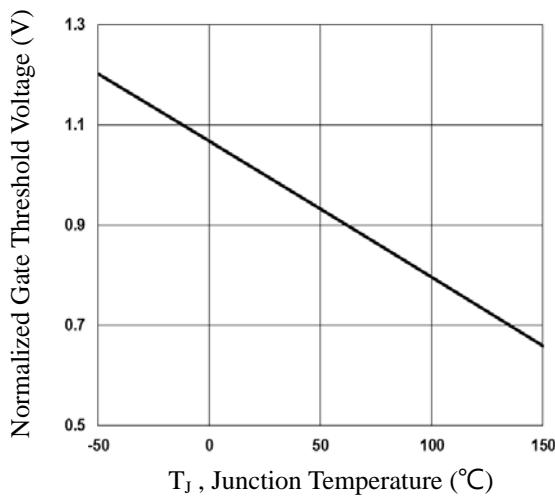


Fig.3 Normalized V<sub>th</sub> vs. T<sub>j</sub>

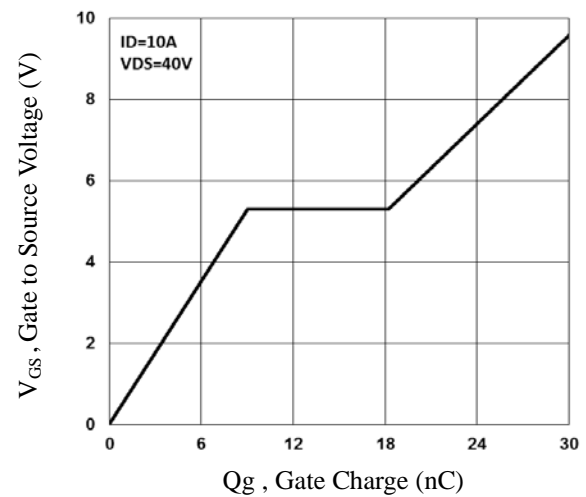


Fig.4 Gate Charge Characteristics

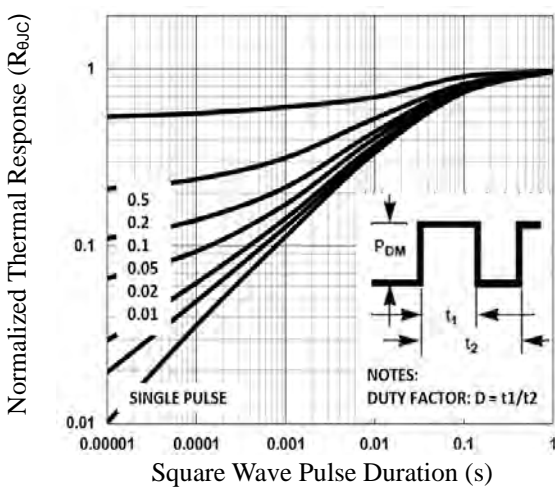


Fig.5 Normalized Transient Impedance

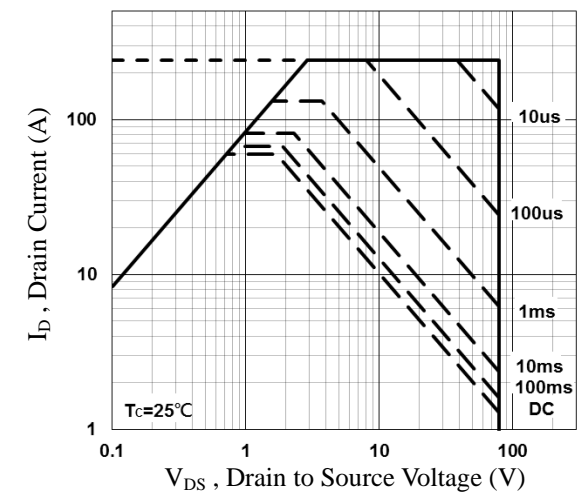


Fig.6 Maximum Safe Operation Area

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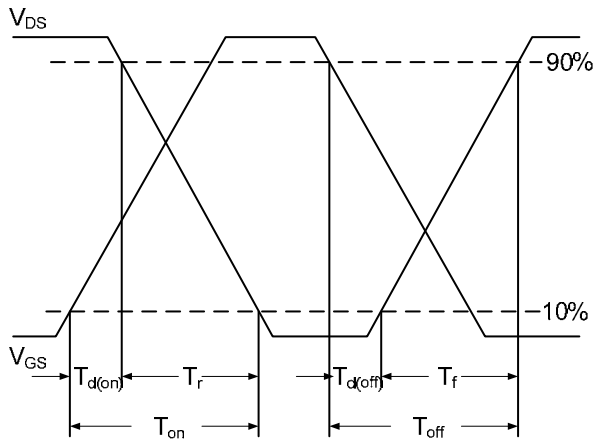


Fig.7 Switching Time Waveform

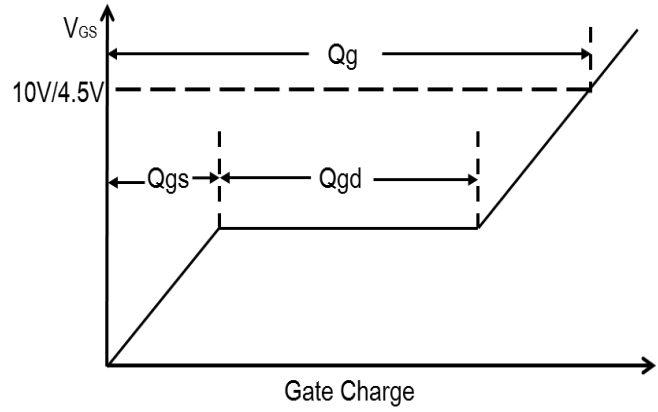
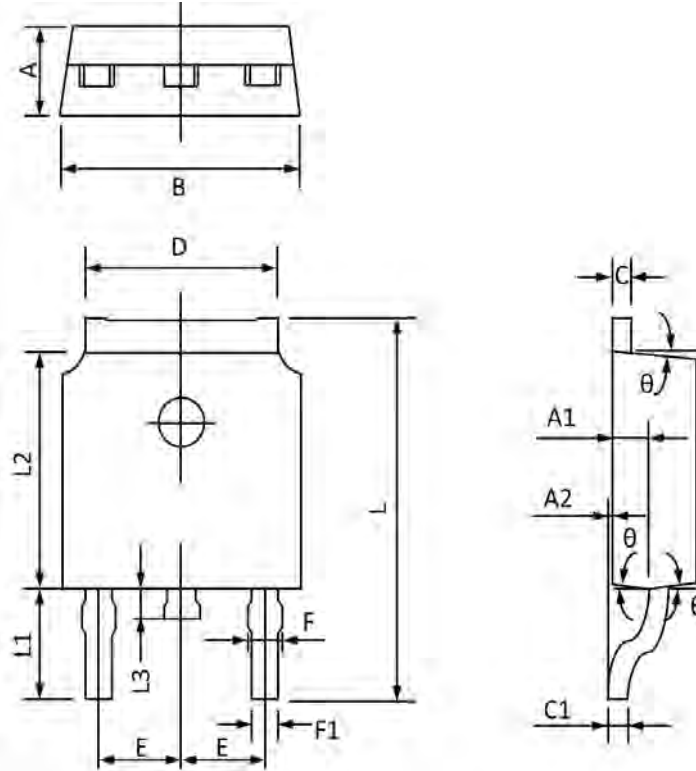


Fig.8 Gate Charge Waveform

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### TO-252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.400	2.200	0.094	0.087
A1	1.110	0.910	0.044	0.036
A2	0.150	0.000	0.006	0.000
B	6.800	6.400	0.268	0.252
C	0.580	0.450	0.023	0.018
C1	0.580	0.460	0.023	0.018
D	5.500	5.100	0.217	0.201
E	2.386	2.186	0.094	0.086
F	0.940	0.600	0.037	0.024
F1	0.860	0.500	0.034	0.020
L	10.400	9.400	0.409	0.370
L1	3.000	2.400	0.118	0.094
L2	6.200	5.400	0.244	0.213
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°