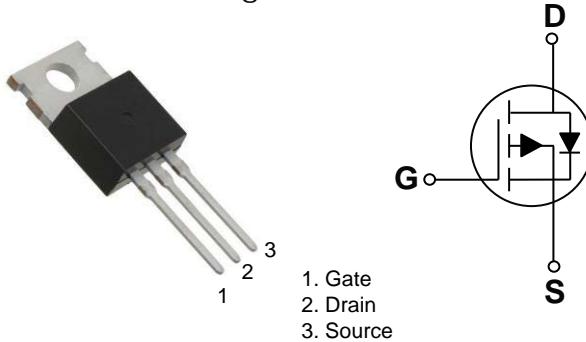


60V P-Channel MOSFETs

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO-220 Pin Configuration



BVDSS	RDS(ON)	ID
-60V	9.5mΩ	-75A

Features

- -60V,-75A, RDS(ON) 9.5mΩ @VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

Applications

- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-Source Voltage	± 20	V
I _D	Drain Current – Continuous (T _c =25°C)	-75	A
	Drain Current – Continuous (T _c =100°C)	-47.5	A
I _{DM}	Drain Current – Pulsed ¹	-300	A
EAS	Single Pulse Avalanche Energy ²	320	mJ
IAS	Single Pulse Avalanche Current ²	80	A
P _D	Power Dissipation (T _c =25°C)	183	W
	Power Dissipation – Derate above 25°C	1.47	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJC}	Thermal Resistance Junction to Case	---	0.68	°C/W
R _{θJA}	Thermal Resistance Junction to Ambient	---	62	°C/W

60V P-Channel MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$	-60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.036	---	$\text{V}/^\circ\text{C}$
I_{DS}	Drain-Source Leakage Current	$V_{DS}=-60\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-48\text{V}$, $V_{GS}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GS}	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance	$V_{GS}=-10\text{V}$, $I_D=-20\text{A}$	---	7.8	9.5	$\text{m}\Omega$
		$V_{GS}=-4.5\text{V}$, $I_D=-10\text{A}$	---	9.5	12.5	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$, $I_D=-250\mu\text{A}$	-1.2	-1.6	-2.5	V
			---	-6.3	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=-10\text{V}$, $I_D=-3\text{A}$	---	18	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3, 4}	$V_{DS}=-48\text{V}$, $V_{GS}=-10\text{V}$, $I_D=-5\text{A}$	---	141	210	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	17	25.5	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	28.6	43	
$T_{d(on)}$	Turn-On Delay Time ^{3, 4}	$V_{DD}=-48\text{V}$, $V_{GS}=-10\text{V}$, $R_G=6\Omega$ $I_D=-1\text{A}$	---	70	140	ns
T_r	Rise Time ^{3, 4}		---	205	410	
$T_{d(off)}$	Turn-Off Delay Time ^{3, 4}		---	402	804	
T_f	Fall Time ^{3, 4}		---	197	394	
C_{iss}	Input Capacitance	$V_{DS}=-25\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$	---	8620	12930	pF
C_{oss}	Output Capacitance		---	486	730	
C_{rss}	Reverse Transfer Capacitance		---	288	430	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-75	A
I_{SM}	Pulsed Source Current		---	---	-150	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{DD}=50\text{V}$, $V_{GS}=10\text{V}$, $L=0.1\text{mH}$, $I_{AS}=80\text{A}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
- The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

60V P-Channel MOSFETs

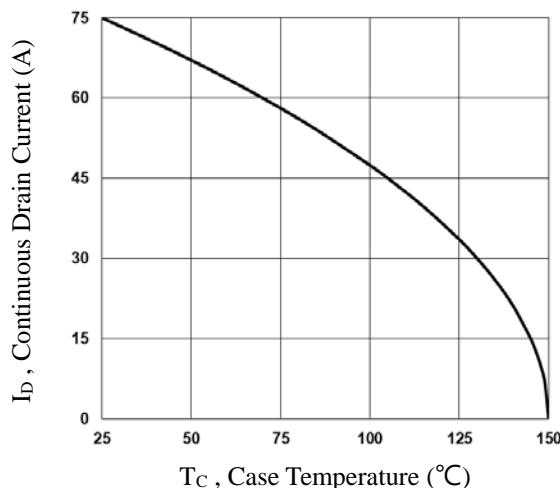


Fig.1 Continuous Drain Current vs. T_c

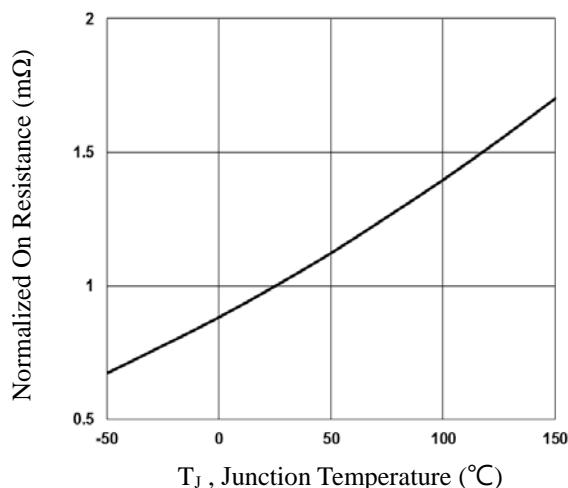


Fig.2 Normalized RDS(on) vs. T_j

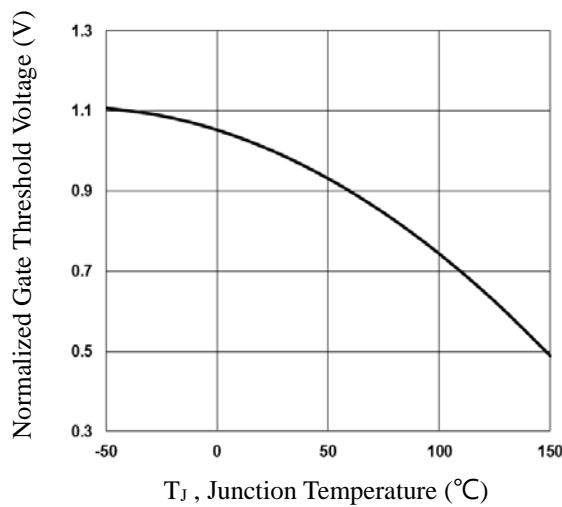


Fig.3 Normalized V_{th} vs. T_j

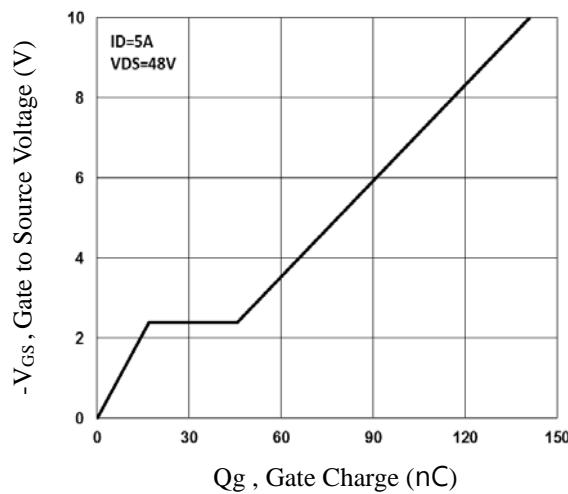


Fig.4 Gate Charge Waveform

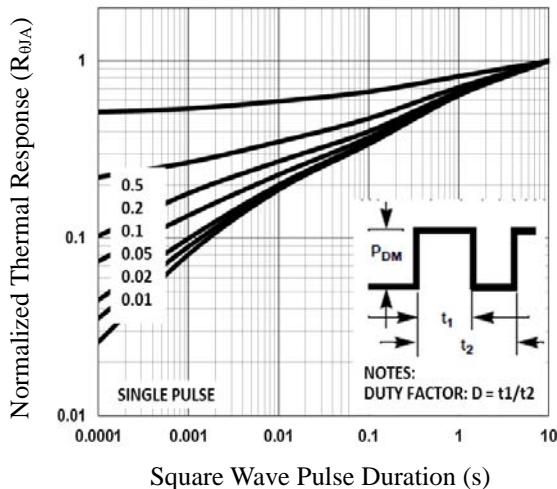


Fig.5 Normalized Transient Impedance

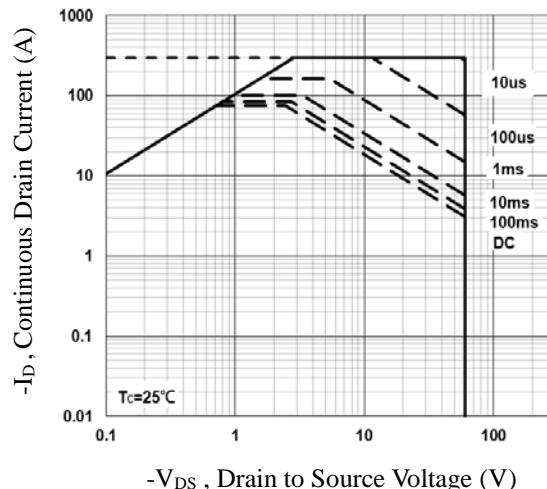


Fig.6 Maximum Safe Operation Area

60V P-Channel MOSFETs

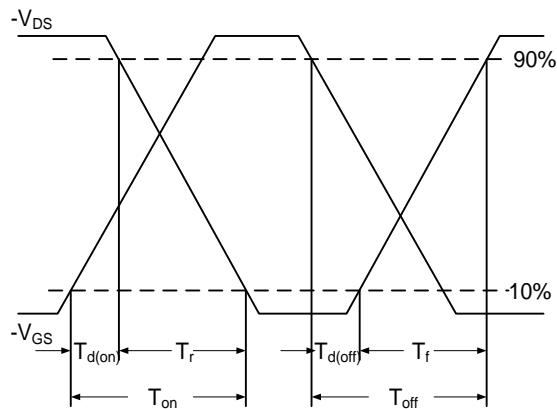


Fig.7 Switching Time Waveform

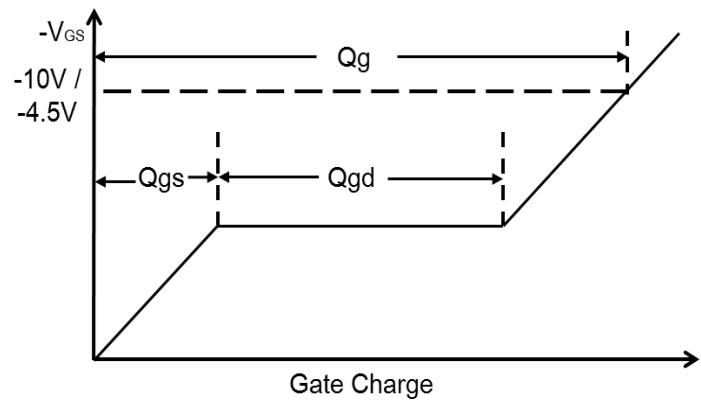
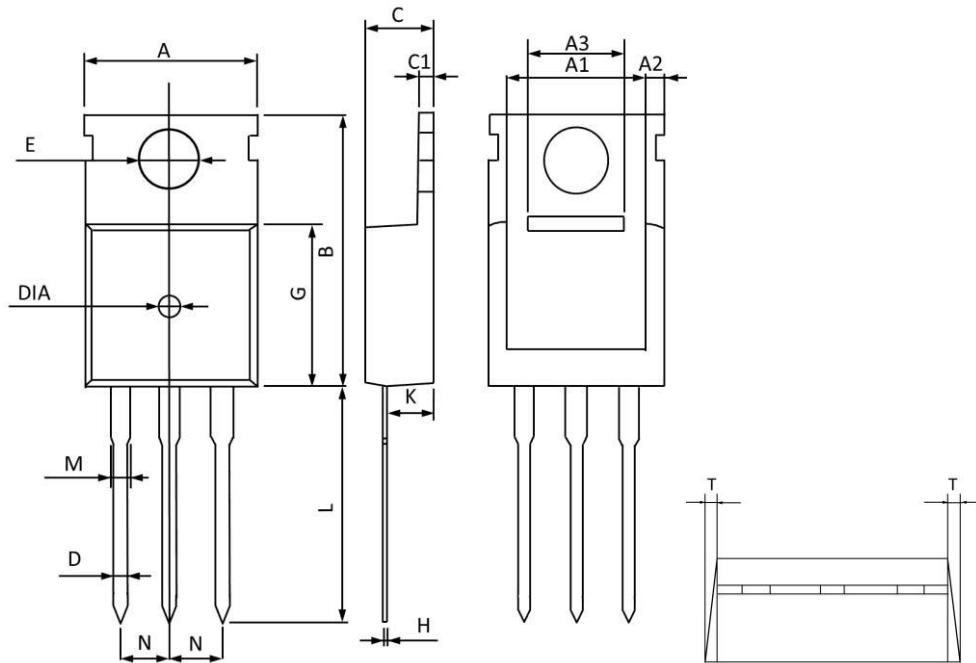


Fig.8 Gate Charge Waveform

60V P-Channel MOSFETs

TO-220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.300	9.700	0.406	0.382
A1	8.840	8.440	0.348	0.332
A2	1.250	1.050	0.049	0.041
A3	5.300	5.100	0.209	0.201
B	16.200	15.400	0.638	0.606
C	4.680	4.280	0.184	0.169
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	3.800	3.400	0.150	0.134
G	9.300	8.700	0.366	0.343
H	0.600	0.400	0.024	0.016
K	2.700	2.100	0.106	0.083
L	13.600	12.800	0.535	0.504
M	1.500	1.100	0.059	0.043
N	2.590	2.490	0.102	0.098
T	W0.35		W0.014	
DIA	Φ1.5 TYP.	deep0.2 TYP.	Φ0.059 TYP.	deep0.008 TYP.