

60V P-Channel MOSFETs

General Description

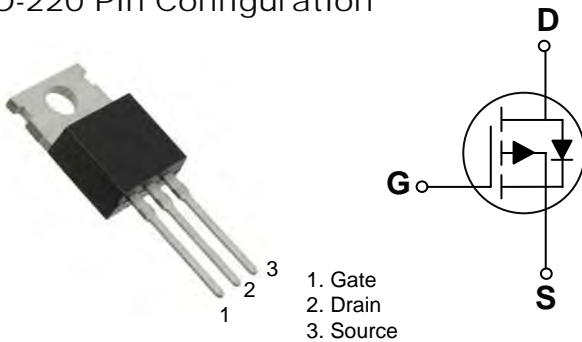
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
-60V	48mΩ	-20A

Features

- -60V, -20A, R_{DS(ON)} = 48mΩ @ V_{GS} = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO-220 Pin Configuration



Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-Source Voltage	± 20	V
I _D	Drain Current – Continuous (T _C =25°C)	-20	A
	Drain Current – Continuous (T _C =100°C)	-13	A
I _{DM}	Drain Current – Pulsed ¹	-80	A
EAS	Single Pulse Avalanche Energy ²	51	mJ
IAS	Single Pulse Avalanche Current ²	-32	A
P _D	Power Dissipation (T _C =25°C)	46	W
	Power Dissipation – Derate above 25°C	0.37	W/°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
T _J	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	2.7	°C/W

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Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$, $I_D=-1mA$	---	-0.05	---	V/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V, T_J=25\text{ }^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-48V, V_{GS}=0V, T_J=125\text{ }^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-8A$	---	39	48	m Ω
		$V_{GS}=-4.5V, I_D=-4A$	---	53	65	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	-1.6	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	5	---	mV/ $^\circ\text{C}$
gfs	Forward Transconductance	$V_{DS}=-10V, I_D=-6A$	---	11	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}	$V_{DS}=-30V, V_{GS}=-10V, I_D=-8A$	---	22.4	31	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	4.1	6	
Q_{gd}	Gate-Drain Charge ^{3,4}		---	5.2	8	
$T_{d(on)}$	Turn-On Delay Time ^{3,4}	$V_{DD}=-30V, V_{GS}=-10V, R_G=6\Omega$ $I_D=-1A$	---	13	25	ns
T_r	Rise Time ^{3,4}		---	42.4	81	
$T_{d(off)}$	Turn-Off Delay Time ^{3,4}		---	64.6	123	
T_f	Fall Time ^{3,4}		---	16.4	31	
C_{iss}	Input Capacitance	$V_{DS}=-30V, V_{GS}=0V, F=1MHz$	---	1250	1810	pF
C_{oss}	Output Capacitance		---	85	125	
C_{riss}	Reverse Transfer Capacitance		---	65	95	
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	15	30	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	-20	A
I_{SM}	Pulsed Source Current		---	---	-80	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A, T_J=25\text{ }^\circ\text{C}$	---	---	-1	V
t_{rr}	Reverse Recovery Time ³	$V_{GS}=0V, I_S=-1A, di/dt=100A/\mu s$ $T_J=25\text{ }^\circ\text{C}$	---	---	---	ns
Q_{rr}	Reverse Recovery Charge ³		---	---	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-32A, R_G=25\Omega$, Starting $T_J=25\text{ }^\circ\text{C}$
3. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

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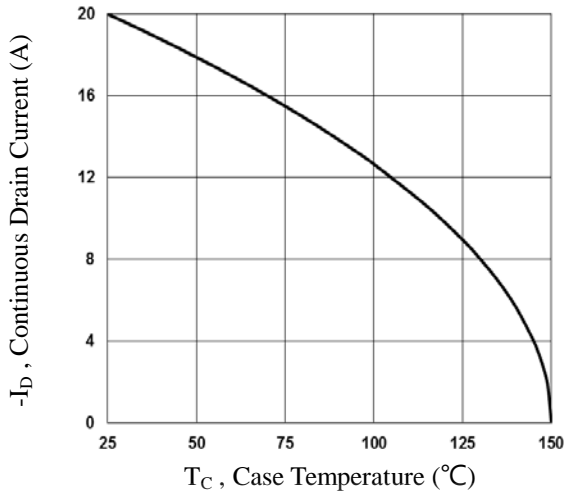


Fig.1 Continuous Drain Current vs. T_c

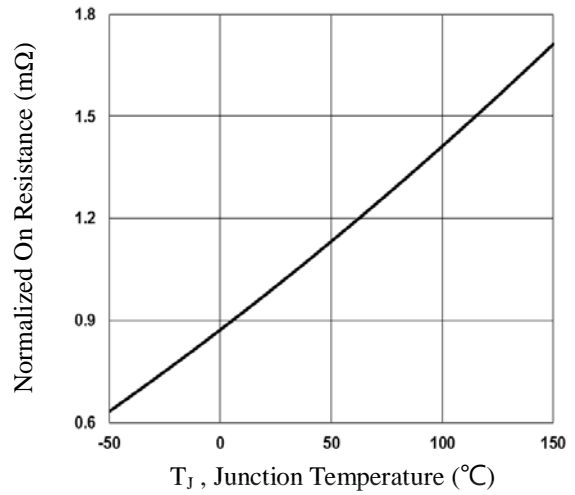


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

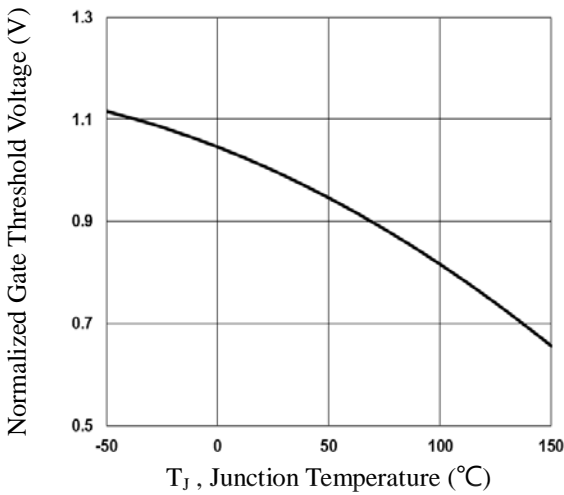


Fig.3 Normalized V_{th} vs. T_j

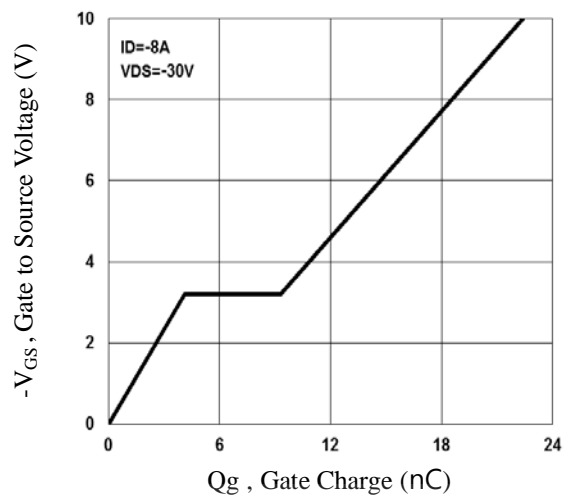


Fig.4 Gate Charge Waveform

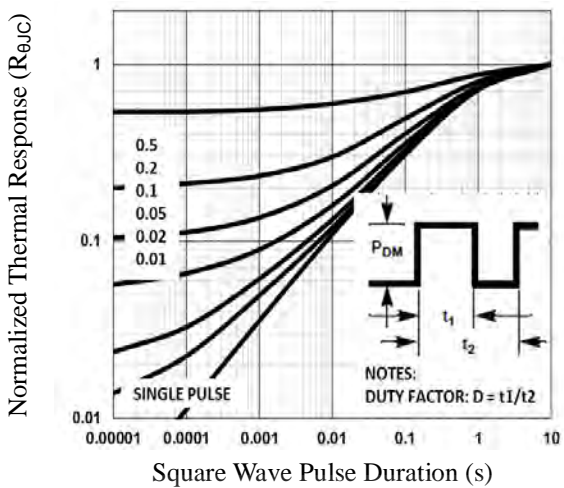


Fig.5 Normalized Transient Impedance

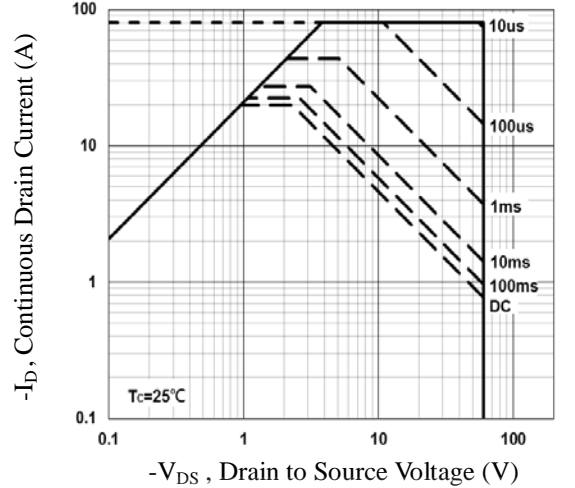


Fig.6 Maximum Safe Operation Area

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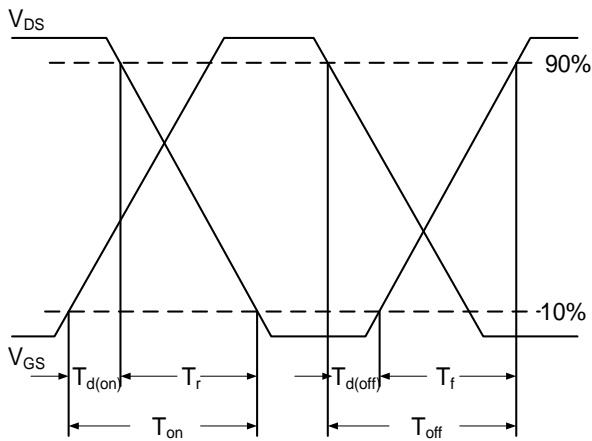


Fig.7 Switching Time Waveform

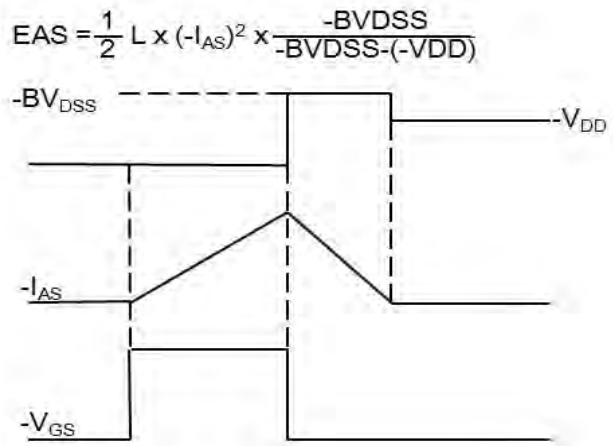
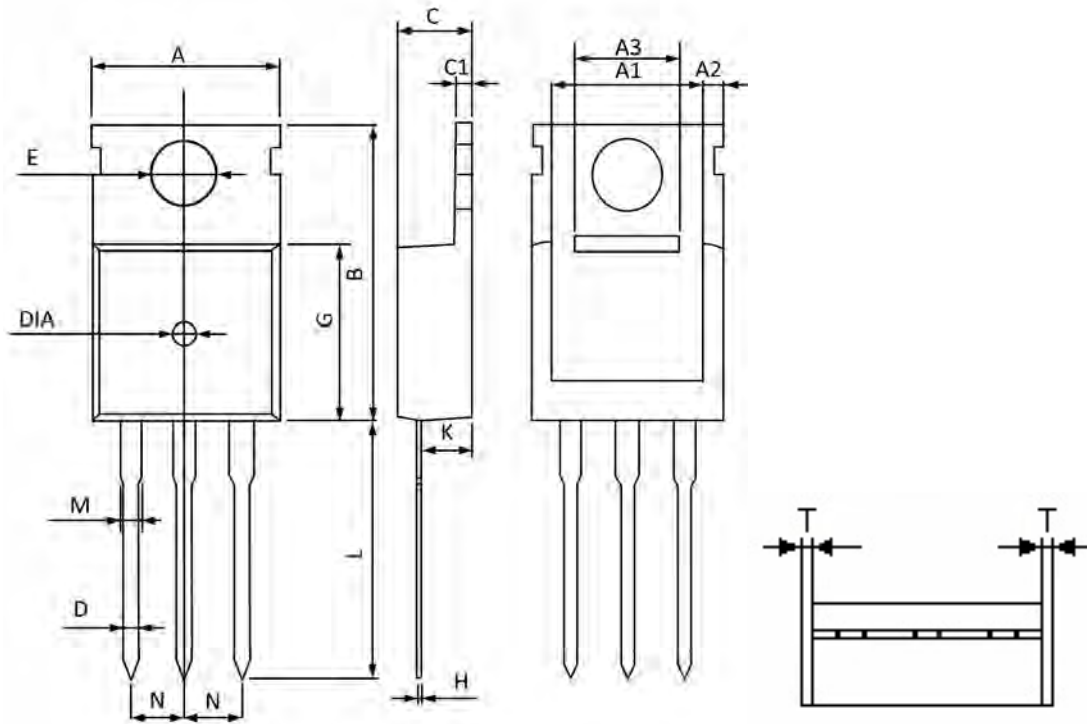


Fig.8 EAS Waveform

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TO-220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.70	10.30	0.382	0.405
A1	8.44	8.84	0.333	0.348
A2	1.05	1.25	0.042	0.049
A3	5.10	5.30	0.201	0.208
B	15.40	16.20	0.607	0.637
C	4.28	4.68	0.169	0.184
C1	1.10	1.50	0.044	0.059
D	0.60	1.00	0.024	0.039
E	3.40	3.80	0.134	0.149
G	8.70	9.30	0.343	0.366
H	0.40	0.60	0.016	0.023
K	2.10	2.70	0.083	0.106
L	12.80	13.60	0.504	0.535
M	1.10	1.50	0.044	0.059
N	2.49	2.59	0.099	0.101
T	0.345	0.355	0.014	0.014
DIA	1.45	1.55	0.058	0.061