

60V P-Channel MOSFETs

General Description

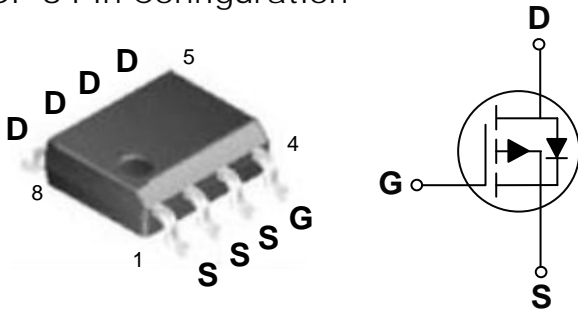
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
-60V	68mΩ	-3.3A

Features

- -60V,-3.3A, R_{DS(ON)} =68mΩ@V_{GS} = -10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

SOP-8 Pin Configuration



Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{GS}	Gate-Source Voltage	± 25	V
I _D	Drain Current – Continuous (T _c =25°C)	-3.3	A
	Drain Current – Continuous (T _c =100°C)	-2.6	A
I _{DM}	Drain Current – Pulsed ¹	-13.2	A
EAS	Single Pulse Avalanche Energy ²	31	mJ
IAS	Single Pulse Avalanche Current ²	-25	A
P _D	Power Dissipation (T _c =25°C)	1.47	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
T _J	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	85	°C/W



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Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$, $I_D=-1mA$	---	-0.05	---	V/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V, T_J=25\text{ }^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-48V, V_{GS}=0V, T_J=125\text{ }^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-3A$	---	60	72	m Ω
		$V_{GS}=-4.5V, I_D=-2A$	---	75	90	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	-1.6	-2.2	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	5	---	mV/ $^\circ\text{C}$
gfs	Forward Transconductance	$V_{DS}=-10V, I_D=-3A$	---	8.5	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}	$V_{DS}=-30V, V_{GS}=-10V, I_D=-3A$	---	16.4	23	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	2.8	4	
Q_{gd}	Gate-Drain Charge ^{3,4}		---	3.6	6	
$T_{d(on)}$	Turn-On Delay Time ^{3,4}	$V_{DD}=-30V, V_{GS}=-10V, R_G=6\Omega$ $I_D=-1A$	---	8.3	16	ns
T_r	Rise Time ^{3,4}		---	29.6	56	
$T_{d(off)}$	Turn-Off Delay Time ^{3,4}		---	51.7	98	
T_f	Fall Time ^{3,4}		---	15.6	30	
C_{iss}	Input Capacitance	$V_{DS}=-30V, V_{GS}=0V, F=1MHz$	---	870	1260	pF
C_{oss}	Output Capacitance		---	70	100	
C_{rss}	Reverse Transfer Capacitance		---	42	60	
R_g	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	16	32	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	-3.3	A
I_{SM}	Pulsed Source Current		---	---	-6.6	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=-1A, T_J=25\text{ }^\circ\text{C}$	---	---	-1	V
t_{rr}	Reverse Recovery Time ³	$V_{GS}=0V, I_S=-1A, di/dt=100A/\mu s$	---	---	---	ns
Q_{rr}	Reverse Recovery Charge ³	$T_J=25\text{ }^\circ\text{C}$	---	---	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=-25A, R_G=25\Omega$, Starting $T_J=25\text{ }^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

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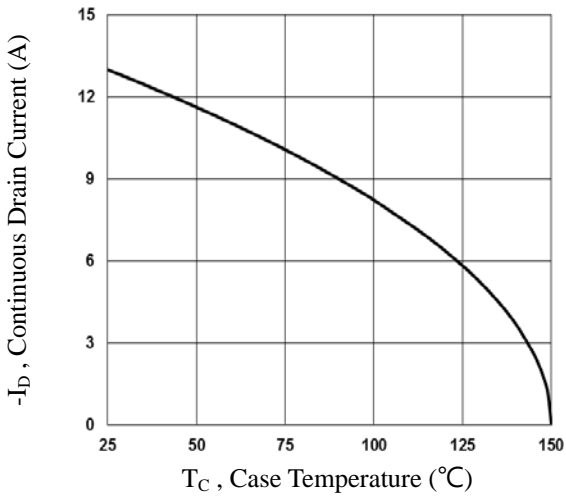


Fig.1 Continuous Drain Current vs. T_C

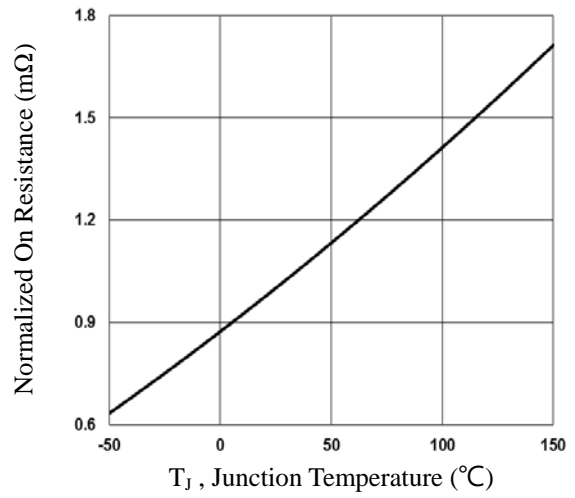


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

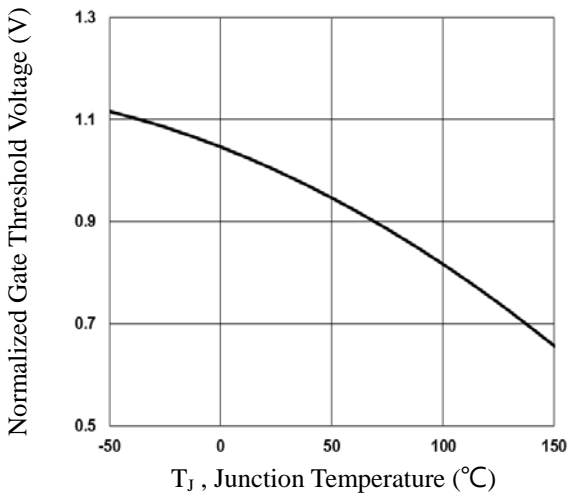


Fig.3 Normalized V_{th} vs. T_J

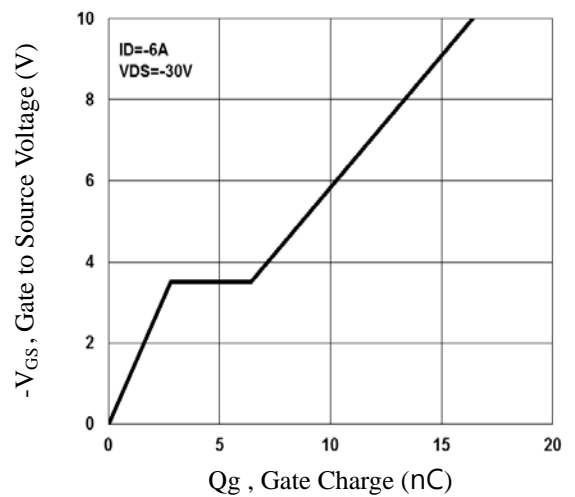


Fig.4 Gate Charge Waveform

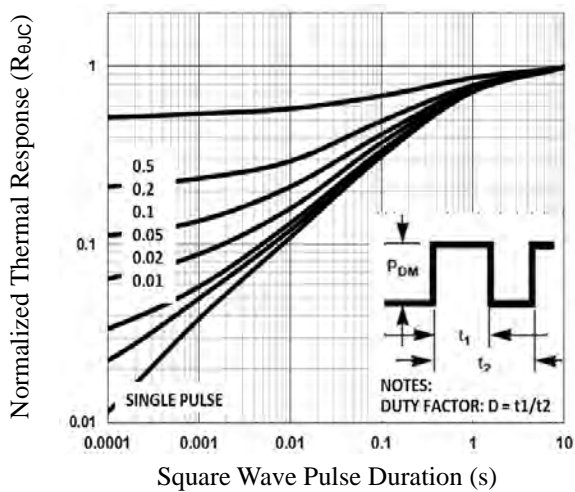


Fig.5 Normalized Transient Impedance

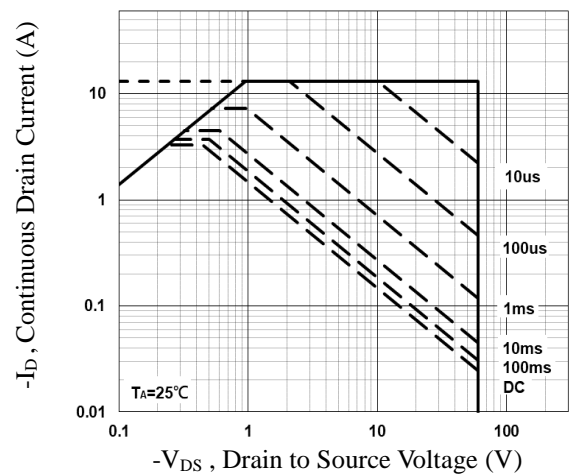


Fig.6 Maximum Safe Operation Area

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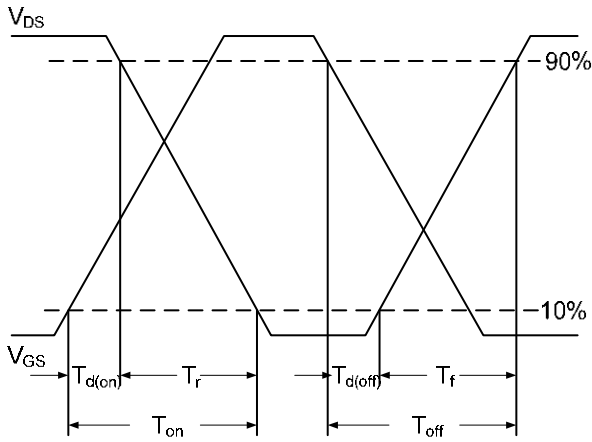


Fig.7 Switching Time Waveform

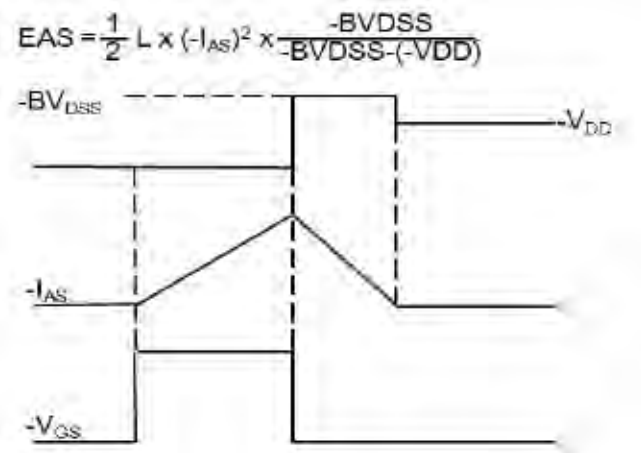
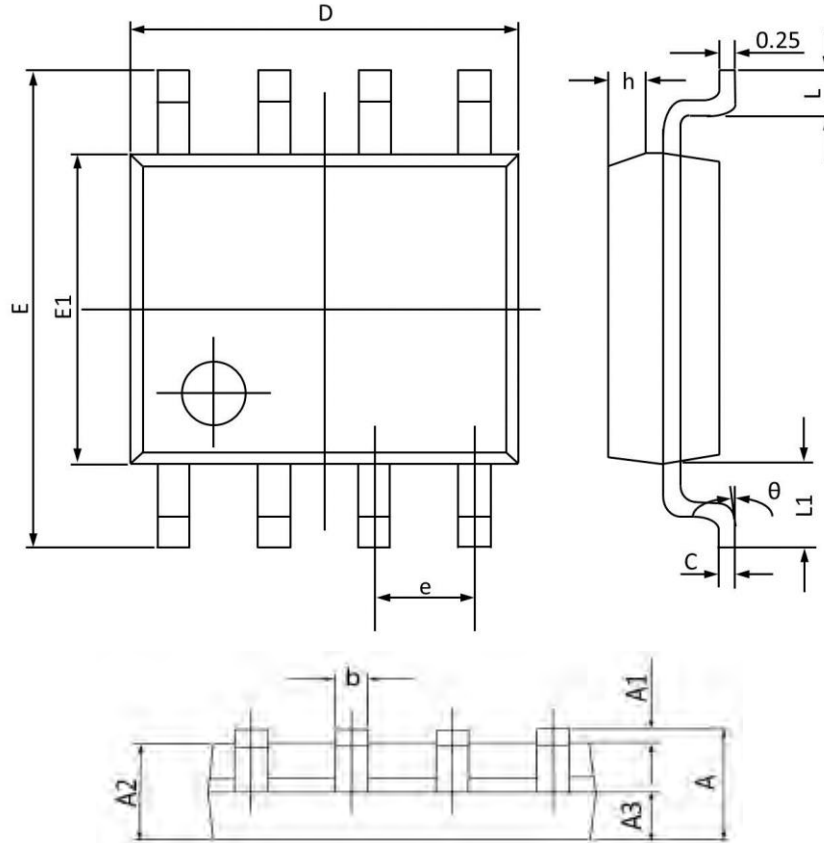


Fig.8 EAS Waveform

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SOP-8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050(BSC)		0.041(BSC)	
θ	0°	8°	0°	8°