

## 60V N-Channel MOSFETs

### General Description

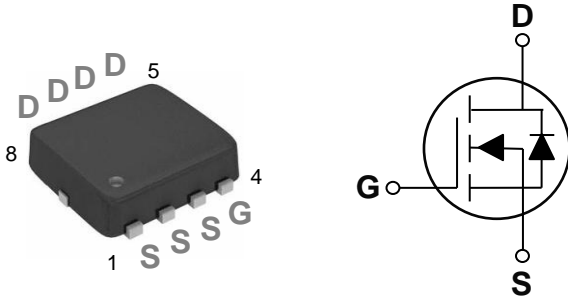
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
60V	12mΩ	42A

### Features

- 60V,42A,  $R_{DS(ON)} = 12m\Omega$  @  $V_{GS} = 10V$
- Improved  $dv/dt$  capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### PPAK3X3 Pin Configuration



### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings (Tc=25 °C unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{GS}$	Gate-Source Voltage	±20	V
$I_D$	Drain Current – Continuous (Tc=25°C)	42	A
	Drain Current – Continuous (Tc=100°C)	26	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	168	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	61	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	35	A
$P_D$	Power Dissipation (Tc=25°C)	52	W
	Power Dissipation – Derate above 25°C	0.42	W/°C
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	°C/W
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2.4	°C/W



# FTK6904Z

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Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1mA$	---	0.03	---	$V/^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=60V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	$\mu A$
		$V_{DS}=48V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	10	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA

### On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10V, I_D=10A$	---	10	12	m $\Omega$
		$V_{GS}=4.5V, I_D=8A$	---	12	15	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.6	2.2	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4	---	$mV/^\circ\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10V, I_D=6A$	---	11.7	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=30V, V_{GS}=10V, I_D=10A$	---	39.2	59	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	5.9	9	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	8.8	14	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=15V, V_{GS}=10V, R_G=6\Omega$ $I_D=1A$	---	9.6	18	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	28.2	54	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	45.3	86	
$T_f$	Fall Time <sup>3, 4</sup>		---	10.9	21	
$C_{iss}$	Input Capacitance	$V_{DS}=25V, V_{GS}=0V, F=1MHz$	---	2100	3050	pF
$C_{oss}$	Output Capacitance		---	165	240	
$C_{rss}$	Reverse Transfer Capacitance		---	80	120	
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	1.6	3.2	$\Omega$

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	42	A
$I_{SM}$	Pulsed Source Current <sup>3</sup>		---	---	84	A
$V_{SD}$	Diode Forward Voltage <sup>3</sup>	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=35A, R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

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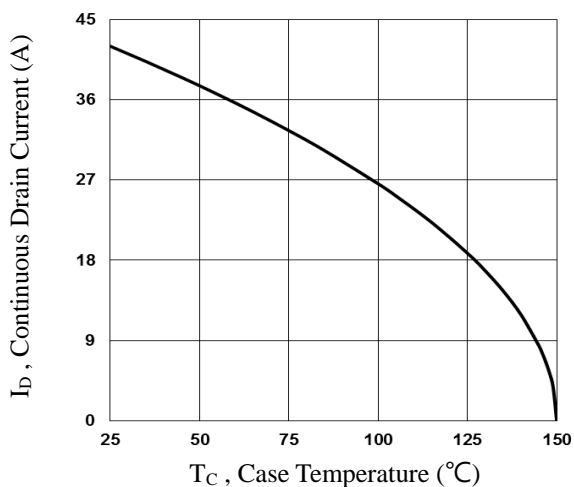


Fig.1 Continuous Drain Current vs.  $T_C$

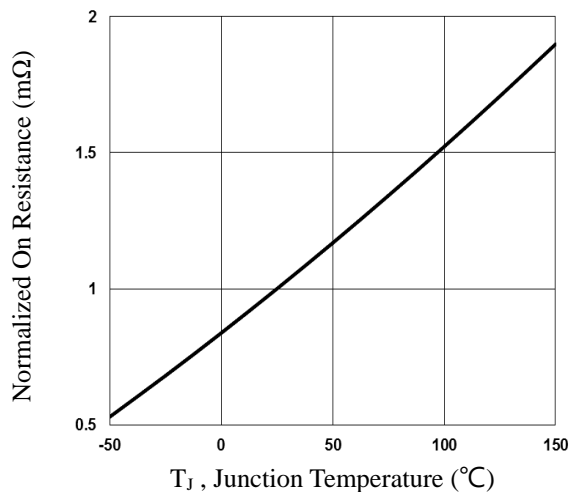


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$

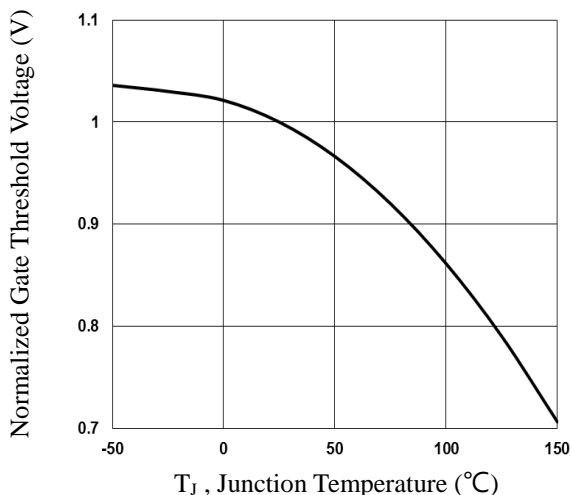


Fig.3 Normalized  $V_{th}$  vs.  $T_J$

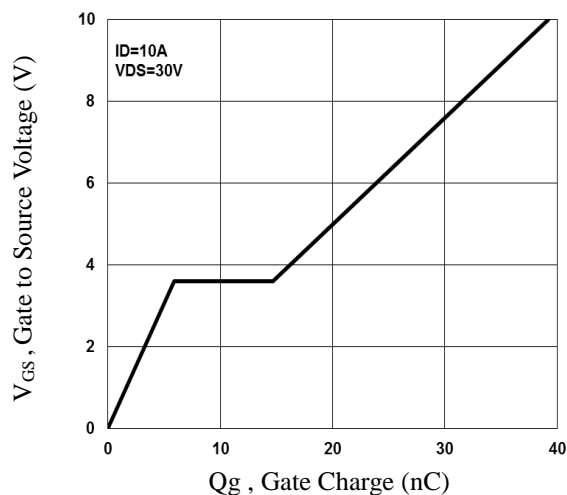


Fig.4 Gate Charge Waveform

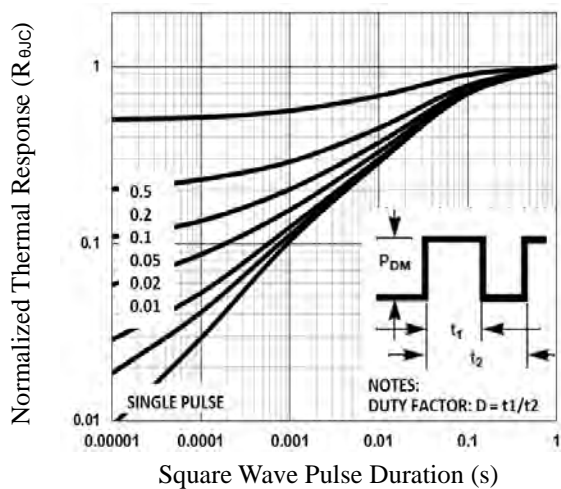


Fig.5 Normalized Transient Response

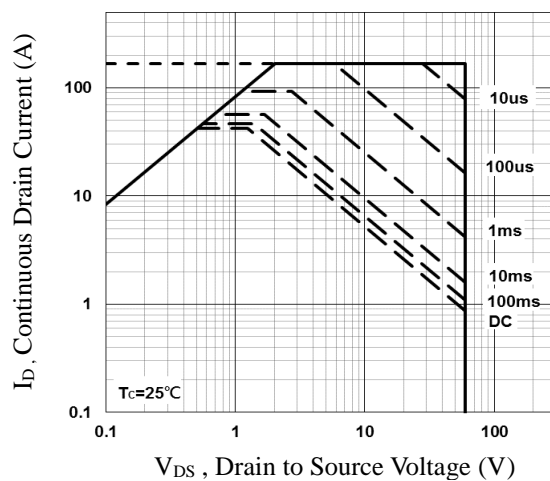


Fig.6 Maximum Safe Operation Area

## 60V N-Channel MOSFETs

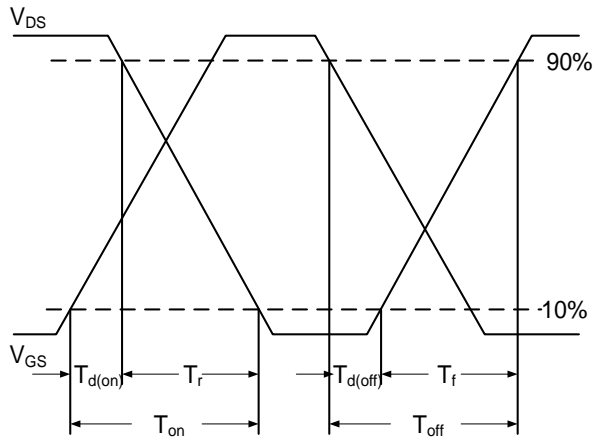


Fig.7 Switching Time Waveform

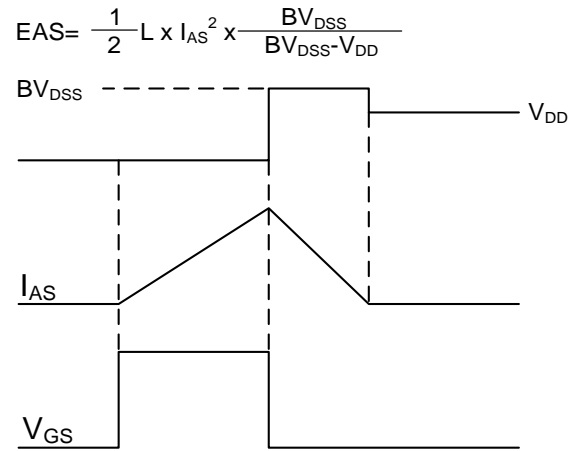
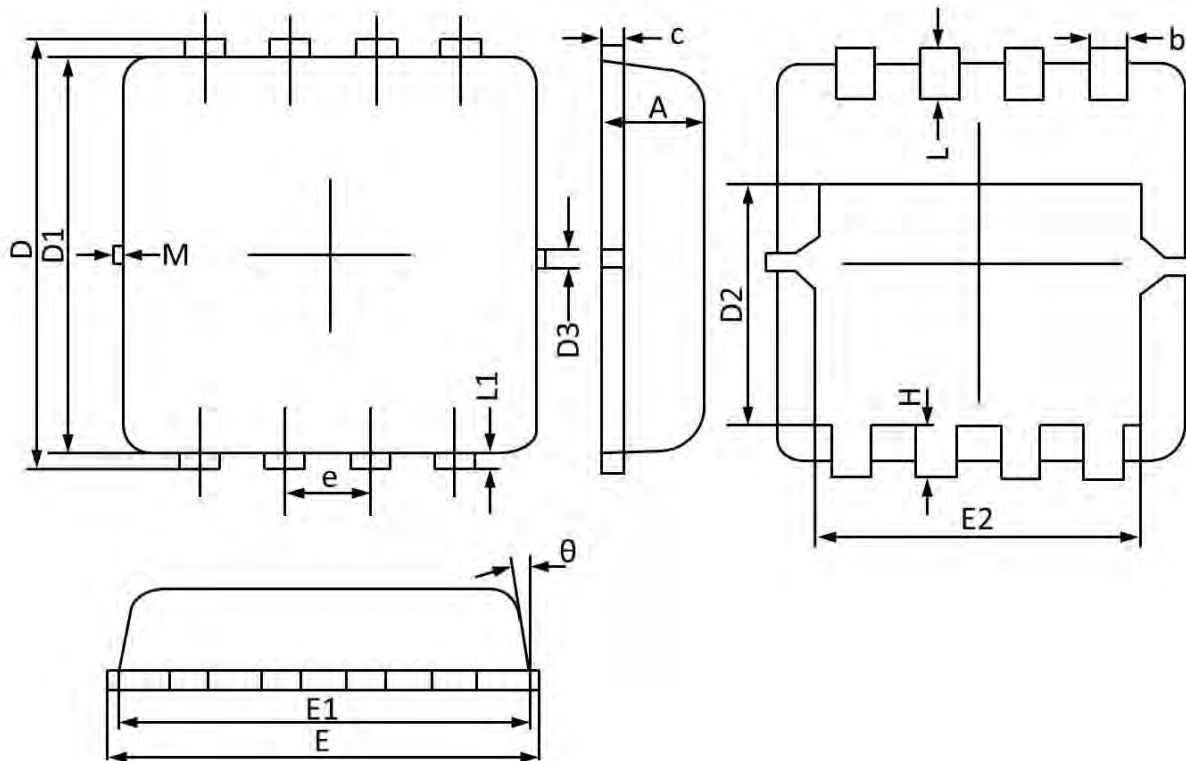


Fig.8 EAS Waveform

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### PPAK3x3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
b	0.250	0.350	0.010	0.013
c	0.100	0.250	0.004	0.009
D	3.250	3.450	0.128	0.135
D1	3.000	3.200	0.119	0.125
D2	1.780	1.980	0.070	0.077
D3	0.130 REF		0.005 REF	
E	3.200	3.400	0.126	0.133
E1	3.000	3.200	0.119	0.125
E2	2.390	2.590	0.094	0.102
e	0.650 BSC		0.026 BSC	
H	0.300	0.500	0.011	0.019
L	0.300	0.500	0.011	0.019
L1	0.130 REF		0.005 REF	
θ	0°	12°	0°	12°
M	0.150 REF		0.006 REF	