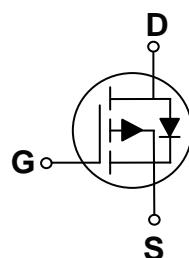
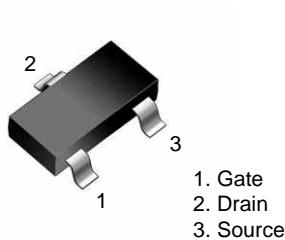


20V P-Channel MOSFETs

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

SOT-23 Pin Configuration



BVDSS	RDS(ON)	ID
-20V	26mΩ	-6.5A

Features

- -20V, -6.5A, RDS(ON) = 26mΩ @ VGS = -4.5V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- Suit for -1.8V Gate Drive Applications

Applications

- Notebook
- Load Switch
- Networking

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-20	V
V _{GS}	Gate-Source Voltage	± 10	V
I _D	Drain Current – Continuous (Tc=25°C)	-6.5	A
	Drain Current – Continuous (Tc=100°C)	-4.1	A
I _{DM}	Drain Current – Pulsed ¹	-26	A
P _D	Power Dissipation (Tc=25°C)	1.56	W
	Power Dissipation – Derate above 25°C	0.012	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	80	°C/W

20V P-Channel MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-20	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=-1\text{mA}$	---	-0.01	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-20\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}}=-16\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 10\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-4.5\text{V}$, $I_D=-5\text{A}$	---	20	26	$\text{m}\Omega$
		$V_{\text{GS}}=-2.5\text{V}$, $I_D=-4\text{A}$	---	25	32	
		$V_{\text{GS}}=-1.8\text{V}$, $I_D=-3\text{A}$	---	31	40	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-0.3	-0.6	-1.0	V
$\Delta V_{\text{GS(th)}}$	$V_{\text{GS(th)}}$ Temperature Coefficient		---	3	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_S=-5\text{A}$	---	15	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=-10\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $I_D=-5\text{A}$	---	19.5	29	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	2	4	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	3.6	7	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=-10\text{V}$, $V_{\text{GS}}=-4.5\text{V}$, $R_G=25\Omega$ $I_D=-1\text{A}$	---	10.4	20	nS
T_r	Rise Time ^{2,3}		---	37.5	71	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	89.1	129	
T_f	Fall Time ^{2,3}		---	24.6	47	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	1670	2430	pF
C_{oss}	Output Capacitance		---	220	320	
C_{rss}	Reverse Transfer Capacitance		---	120	180	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-6.5	A
I_{SM}	Pulsed Source Current		---	---	-26	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

20V P-Channel MOSFETs

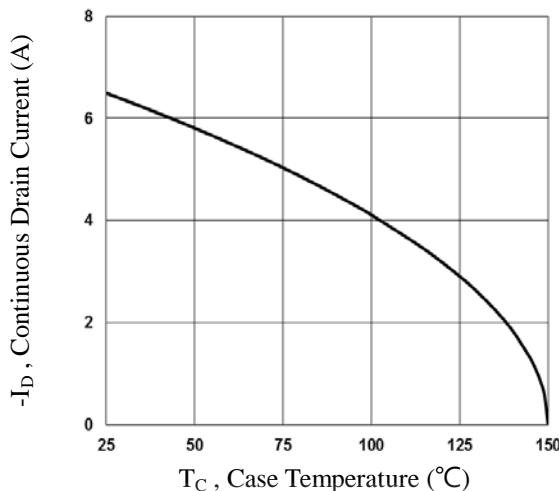


Fig.1 Continuous Drain Current vs. T_C

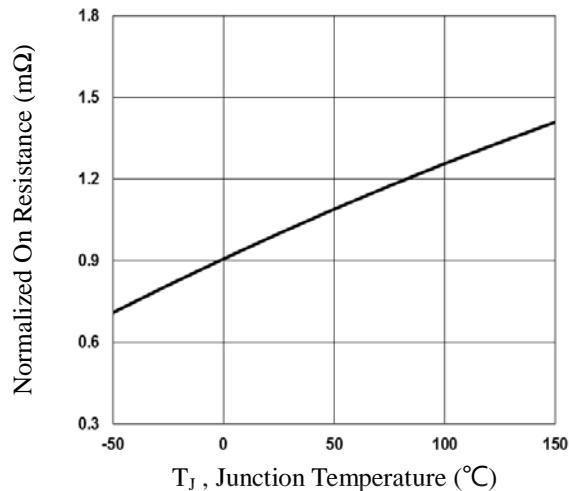


Fig.2 Normalized RDS(on) vs. T_J

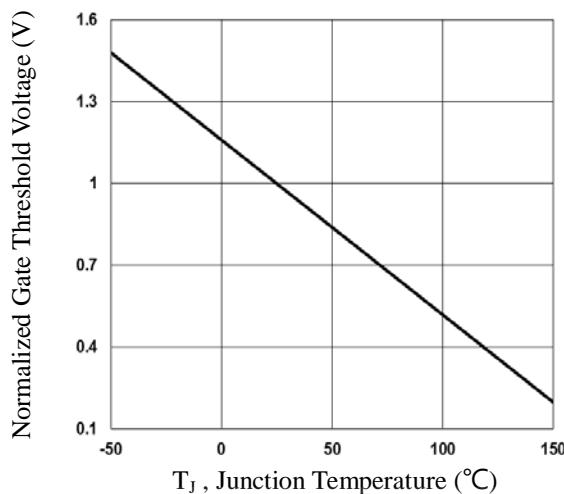


Fig.3 Normalized V_{th} vs. T_J

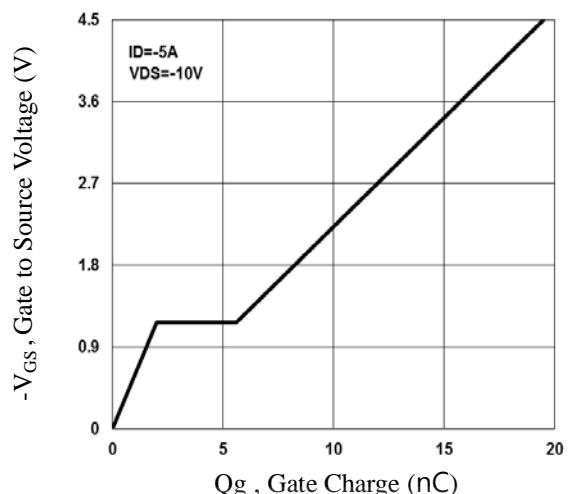


Fig.4 Gate Charge Waveform

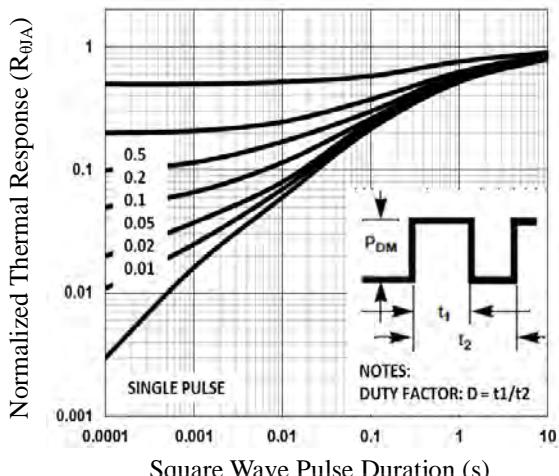


Fig.5 Normalized Transient Impedance

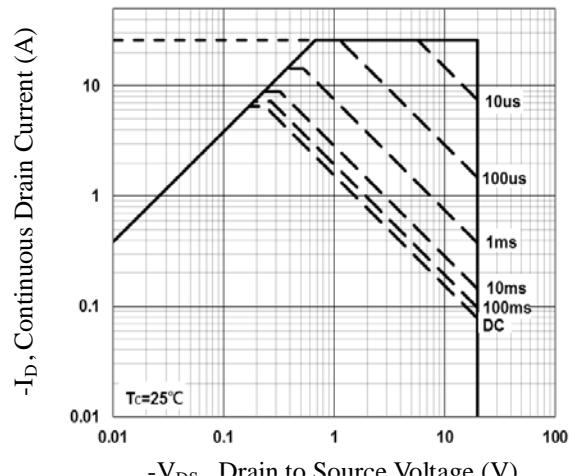


Fig.6 Maximum Safe Operation Area

20V P-Channel MOSFETs

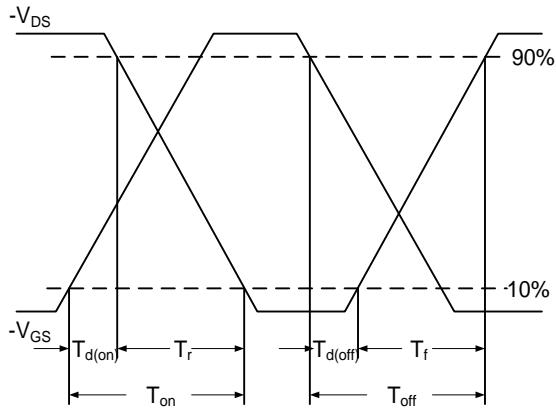


Fig.7 Switching Time Waveform

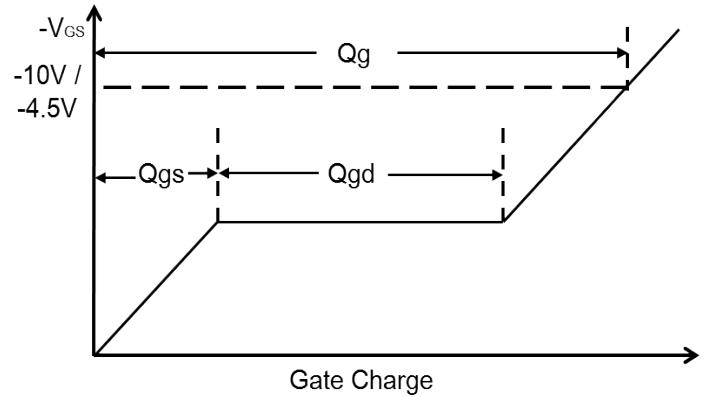
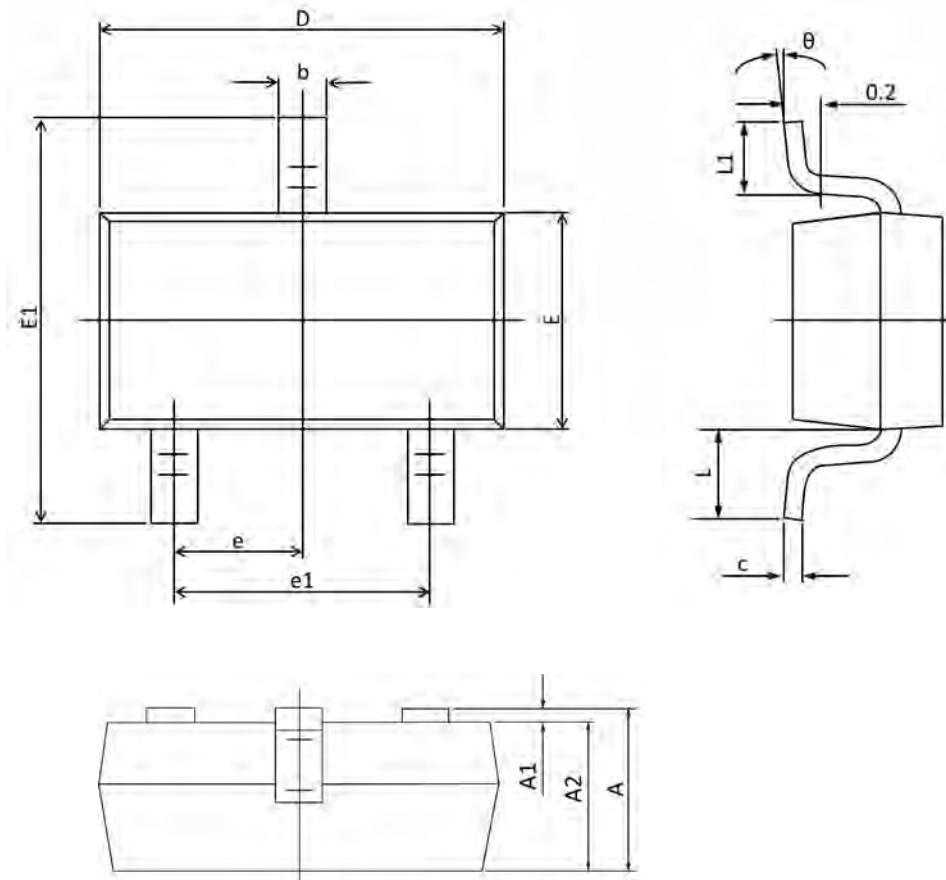


Fig.8 Gate Charge Waveform

20V P-Channel MOSFETs
SOT-23 PACKAGE INFORMATION


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.400	0.012	0.016
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.700 REF.		0.028 REF.	
L1	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°