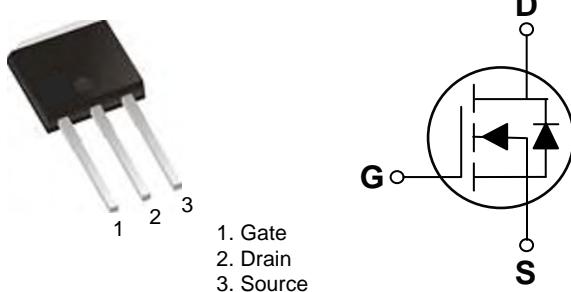


60V N-Channel MOSFETs

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO-251 Pin Configuration



BVDSS	RDS(ON)	ID
60V	75mΩ	11A

Features

- 60V, 11A, RDS(ON) = 75mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings (T_c=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	± 20	V
I _D	Drain Current – Continuous (T _c =25°C)	11	A
	Drain Current – Continuous (T _c =100°C)	7	A
I _{DM}	Drain Current – Pulsed ¹	44	A
EAS	Single Pulse Avalanche Energy ²	25	mJ
IAS	Single Pulse Avalanche Current ²	7	A
P _D	Power Dissipation (T _c =25°C)	25	W
	Power Dissipation – Derate above 25°C	0.2	W/°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
T _J	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	5	°C/W

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Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.05	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=6\text{A}$	---	60	75	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=3\text{A}$	---	70	90	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.2	1.8	2.5	V
$\Delta V_{\text{GS(th)}}$			---	-5	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=3\text{A}$	---	4	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=6\text{A}$	---	9.3	13	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	2.1	3	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	1.8	4	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3.3\Omega$	---	2.9	6	ns
T_r	Rise Time ^{2,3}		---	9.5	18	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	18.4	35	
T_f	Fall Time ^{2,3}		---	5.3	10	
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	500	725	pF
C_{oss}	Output Capacitance		---	45	65	
C_{rss}	Reverse Transfer Capacitance		---	16	30	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	2	4	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	11	A
I_{SM}	Pulsed Source Current		---	---	44	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V
t_{rr}	Reverse Recovery Time ²	$V_{\text{GS}}=30\text{V}$, $I_s=1\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$	---	23.2	---	ns
Q_{rr}	Reverse Recovery Charge ²		---	14.3	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=1\text{mH}$, $I_{\text{AS}}=7\text{A}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$
3. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

60V N-Channel MOSFETs

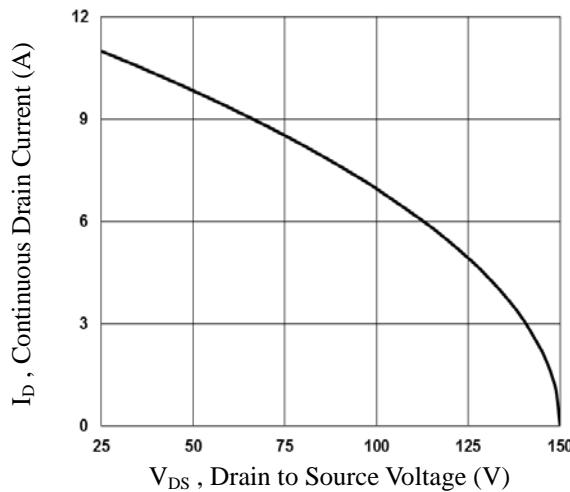


Fig.1 Output Characteristics

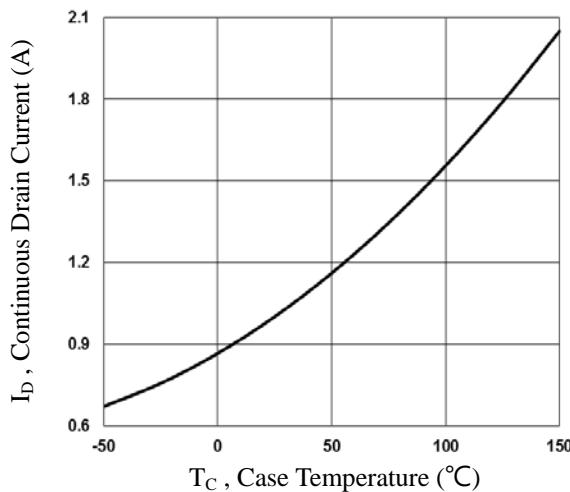


Fig.2 Continuous Drain Current vs. T_c

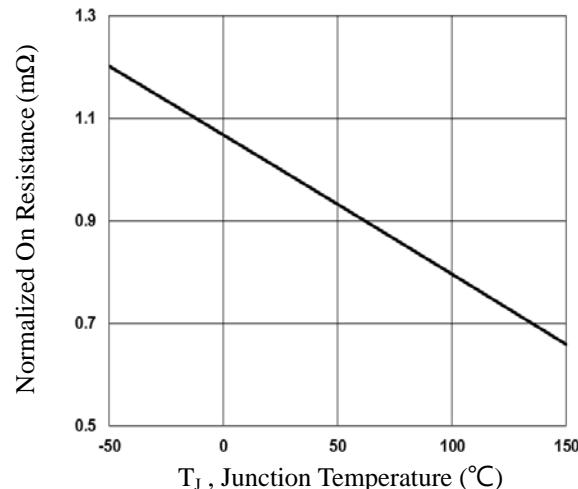


Fig.3 Normalized RDS(ON) vs. T_J

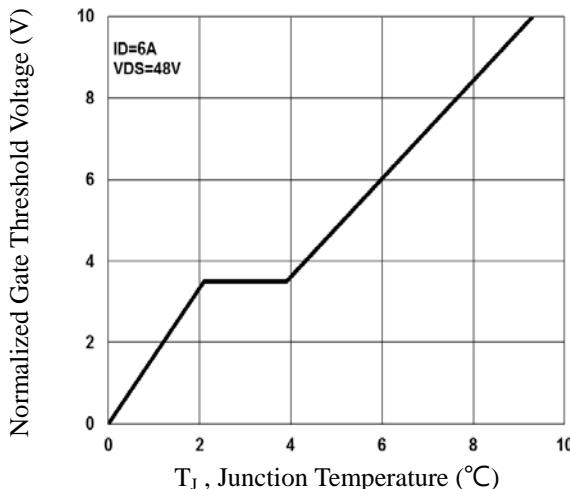


Fig.4 Normalized V_{th} vs. T_J

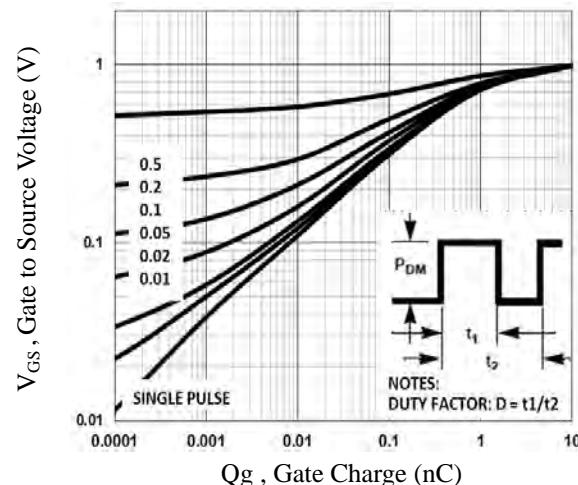


Fig.5 Gate Charge Waveform

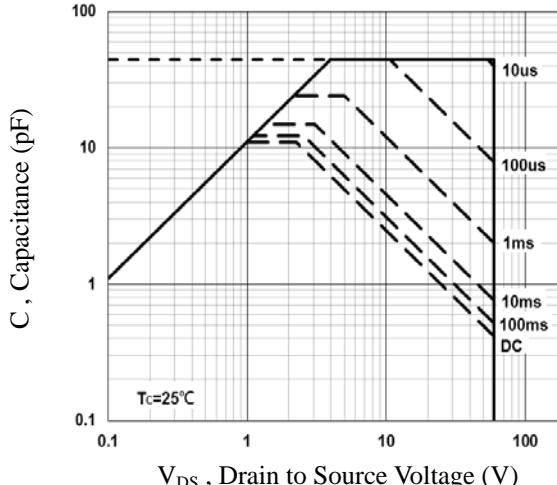


Fig.6 Capacitance Characteristics

60V N-Channel MOSFETs

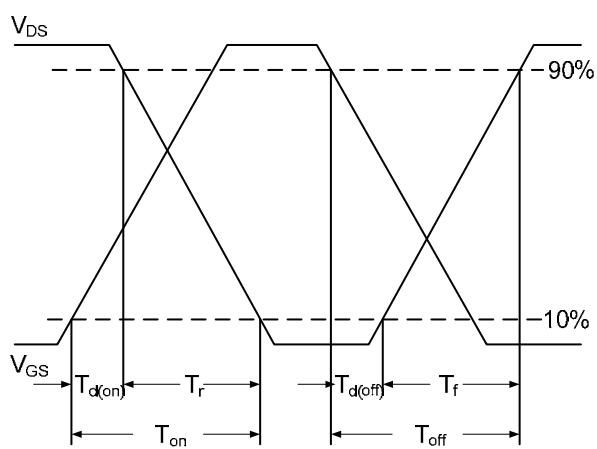


Fig.7 Switching Time Waveform

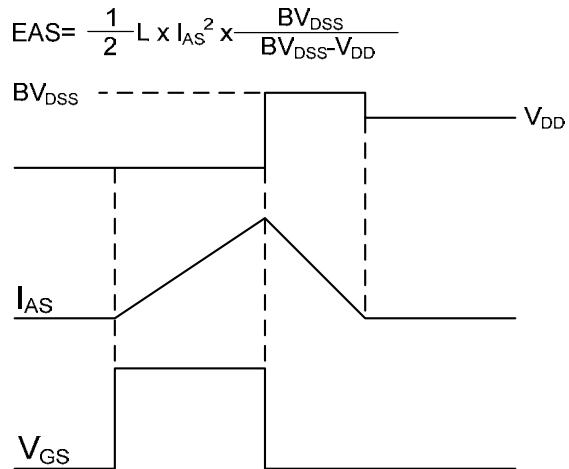
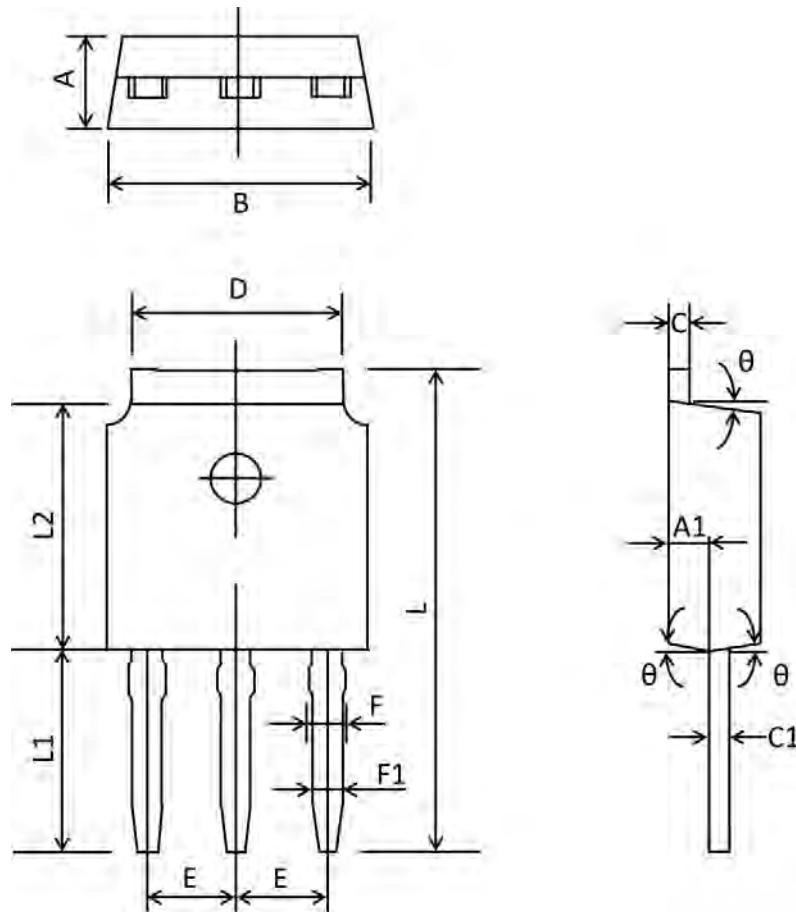


Fig.8 EAS Waveform

60V N-Channel MOSFETs
TO-251 PACKAGE INFORMATION


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.20	2.40	0.087	0.094
A1	0.91	1.11	0.036	0.044
B	6.50	6.70	0.256	0.264
C	0.46	0.580	0.018	0.230
C1	0.46	0.580	0.018	0.030
D	5.10	5.46	0.201	0.215
E	2.186	2.386	0.086	0.094
F	0.74	0.94	0.029	0.037
F1	0.660	0.860	0.026	0.034
L	11.70	12.30	0.461	0.484
L1	4.8	5.2	0.189	0.205
L2	6.00	6.20	0.236	0.244
θ	3°	9°	3°	9°