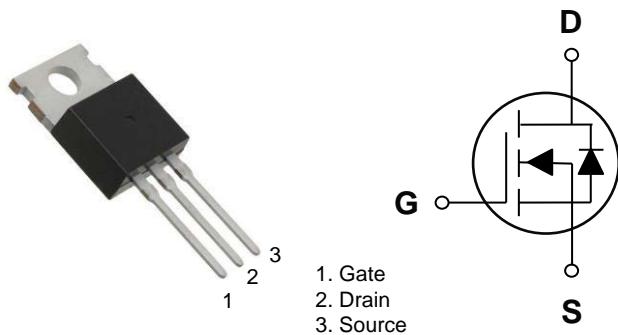


60V N-Channel MOSFETs

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO-220 Pin Configuration



BVDSS	RDS(ON)	ID
60V	2.2mΩ	170A

Features

- 60V, 170A, RDS(ON) = 2.2mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

Applications

- Motor Drive
- Power Tools
- LED Lighting
- Quick Charger

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	60	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (Tc=25°C)	170	A
	Drain Current – Continuous (Tc=100°C)	105	A
I _{DM}	Drain Current – Pulsed ¹	680	A
EAS	Single Pulse Avalanche Energy ²	1065	mJ
I _{AS}	Single Pulse Avalanche Current ²	146	A
P _D	Power Dissipation (Tc=25°C)	208	W
	Power Dissipation – Derate above 25°C	1.67	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	0.6	°C/W

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Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_D=250\mu\text{A}$	60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $\text{I}_D=1\text{mA}$	---	0.043	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}=60\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$\text{V}_{\text{DS}}=48\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}=\pm 20\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ³	$\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=20\text{A}$	---	1.4	2.2	$\text{m}\Omega$
		$\text{V}_{\text{GS}}=6\text{V}$, $\text{I}_D=15\text{A}$	---	1.8	2.6	$\text{m}\Omega$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{GS}}=\text{V}_{\text{DS}}$, $\text{I}_D=250\mu\text{A}$	1.5	2.3	3.5	V
			---	-4.7	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$\text{V}_{\text{DS}}=10\text{V}$, $\text{I}_D=5\text{A}$	---	25	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3, 4}	$\text{V}_{\text{DS}}=48\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{I}_D=10\text{A}$	---	293	450	nC
Q_{gs}	Gate-Source Charge ^{3, 4}		---	61.8	93	
Q_{gd}	Gate-Drain Charge ^{3, 4}		---	123	185	
$\text{T}_{\text{d(on)}}$	Turn-On Delay Time ^{3, 4}	$\text{V}_{\text{DD}}=15\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $\text{R}_G=3.3\Omega$ $\text{I}_D=1\text{A}$	---	45.4	90	ns
T_r	Rise Time ^{3, 4}		---	122.2	145	
$\text{T}_{\text{d(off)}}$	Turn-Off Delay Time ^{3, 4}		---	175.4	350	
T_f	Fall Time ^{3, 4}		---	36.6	72	
C_{iss}	Input Capacitance		---	16920	33000	pF
C_{oss}	Output Capacitance	$\text{V}_{\text{DS}}=25\text{V}$, $\text{V}_{\text{GS}}=0\text{V}$, $\text{F}=1\text{MHz}$	---	1361	2700	
C_{rss}	Reverse Transfer Capacitance		---	424	840	
R_g	Gate resistance	$\text{V}_{\text{GS}}=0\text{V}$, $\text{V}_{\text{DS}}=0\text{V}$, $\text{F}=1\text{MHz}$	---	1.6	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$\text{V}_G=\text{V}_D=0\text{V}$, Force Current	---	---	170	A
I_{SM}	Pulsed Source Current ³		---	---	340	A
V_{SD}	Diode Forward Voltage ³	$\text{V}_{\text{GS}}=0\text{V}$, $\text{I}_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $\text{V}_{\text{DD}}=25\text{V}$, $\text{V}_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $\text{I}_{\text{AS}}=146\text{A}$, $\text{R}_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

60V N-Channel MOSFETs

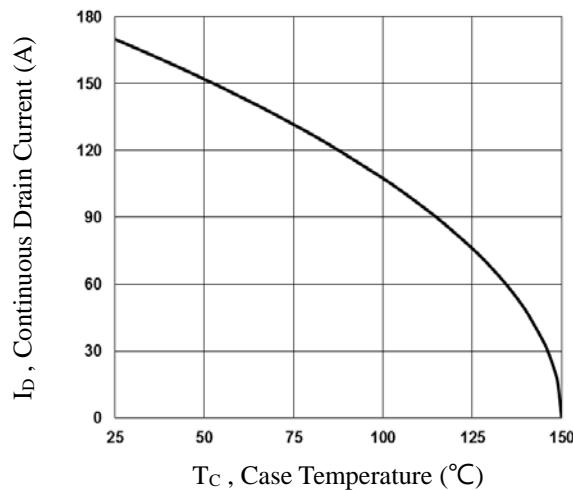


Fig.1 Continuous Drain Current vs. T_C

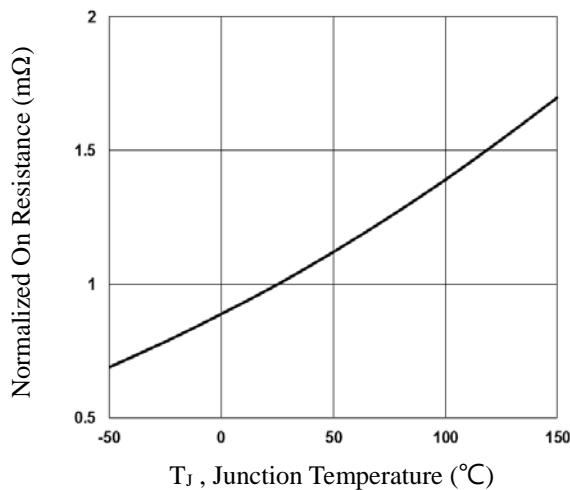


Fig.2 Normalized RDS(on) vs. T_J

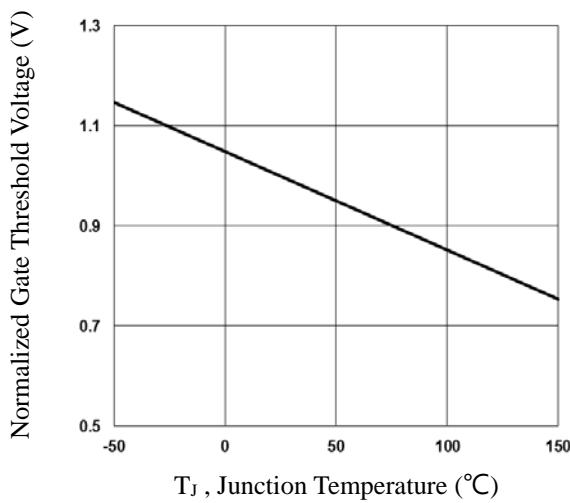


Fig.3 Normalized V_{th} vs. T_J

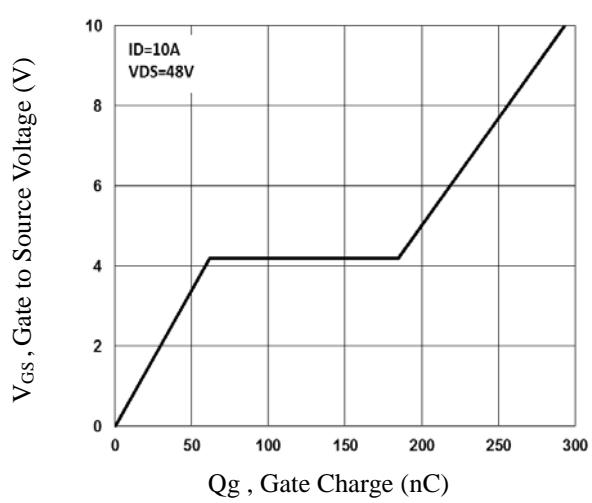


Fig.4 Gate Charge Waveform

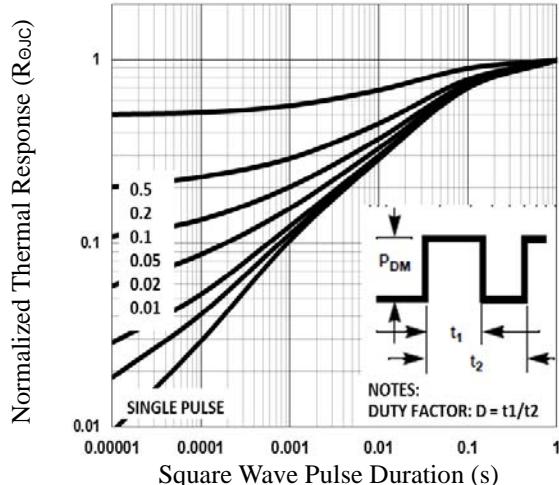


Fig.5 Normalized Transient Response

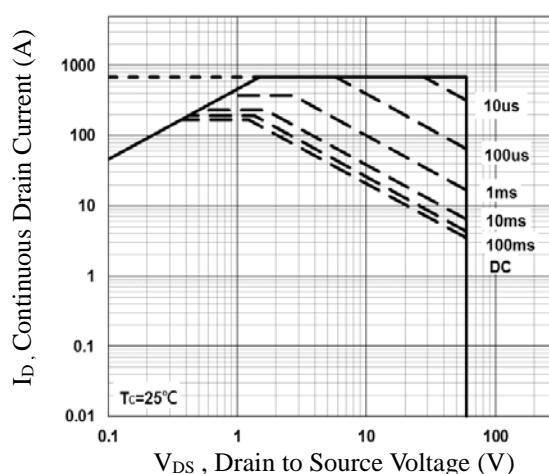


Fig.6 Maximum Safe Operation Area

60V N-Channel MOSFETs

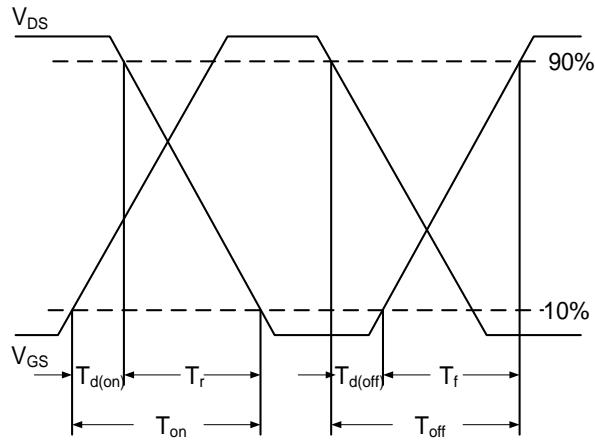


Fig.7 Switching Time Waveform

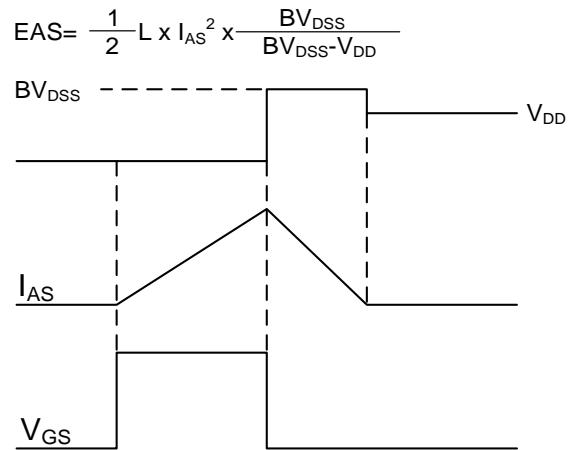
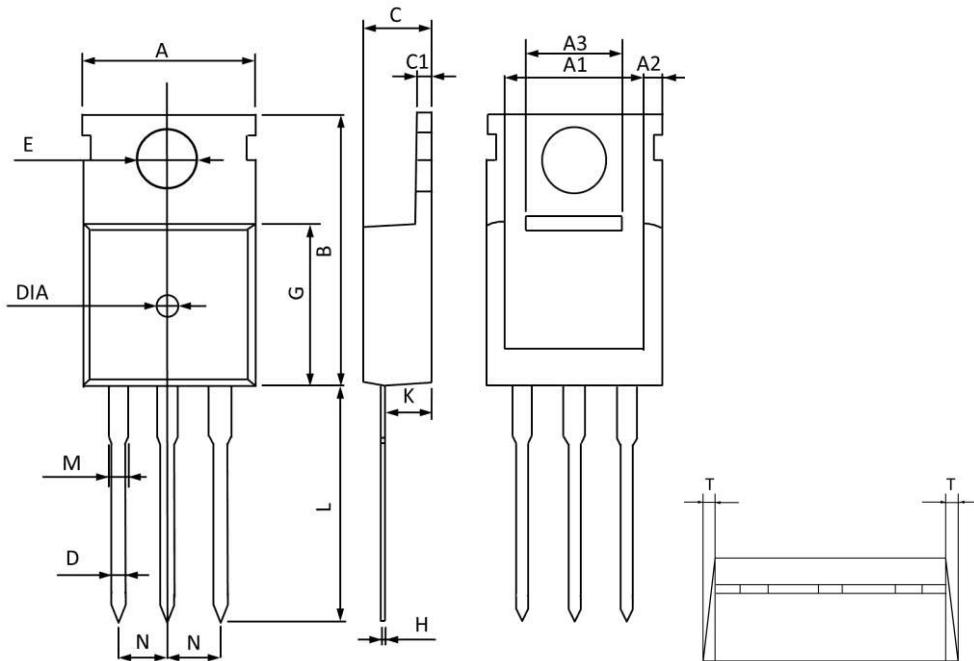


Fig.8 EAS Waveform

60V N-Channel MOSFETs

TO-220 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	10.300	9.700	0.406	0.382
A1	8.840	8.440	0.348	0.332
A2	1.250	1.050	0.049	0.041
A3	5.300	5.100	0.209	0.201
B	16.200	15.400	0.638	0.606
C	4.680	4.280	0.184	0.169
C1	1.500	1.100	0.059	0.043
D	1.000	0.600	0.039	0.024
E	3.800	3.400	0.150	0.134
G	9.300	8.700	0.366	0.343
H	0.600	0.400	0.024	0.016
K	2.700	2.100	0.106	0.083
L	13.600	12.800	0.535	0.504
M	1.500	1.100	0.059	0.043
N	2.590	2.490	0.102	0.098
T	W0.35		W0.014	
DIA	Φ1.5 TYP.	deep0.2 TYP.	Φ0.059 TYP.	deep0.008 TYP.