

## 60V N-Channel MOSFETs

### General Description

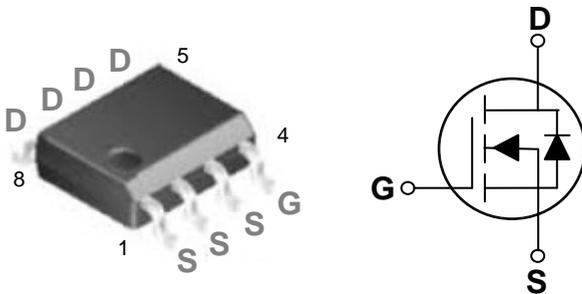
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R <sub>DS(ON)</sub>	I <sub>D</sub>
60V	50mΩ	5.5A

### Features

- 60V, 5.5A, R<sub>DS(ON)</sub> = 50mΩ @ V<sub>GS</sub> = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### SOP-8 Pin Configuration



### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings (T<sub>c</sub>=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	60	V
V <sub>GS</sub>	Gate-Source Voltage	± 20	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>c</sub> =25°C)	5.5	A
	Drain Current – Continuous (T <sub>c</sub> =100°C)	3.5	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	22	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	11	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	15	A
P <sub>D</sub>	Power Dissipation (T <sub>c</sub> =25°C)	1.66	W
	Power Dissipation – Derate above 25°C	0.0133	W/°C
T <sub>STG</sub>	Storage Temperature Range	-50 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-50 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	75	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction to Case	---	35	°C/W



# FTK6910

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Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_D=1\text{mA}$	---	0.05	---	V/ $^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=60V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=48V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA

### On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=10V, I_D=8A$	---	43	50	m $\Omega$
		$V_{GS}=4.5V, I_D=4A$	---	50	60	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=250\mu A$	1.2	1.8	2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4.2	---	mV/ $^\circ\text{C}$
gfs	Forward Transconductance	$V_{DS}=10V, I_D=4A$	---	6.5	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>2,3</sup>	$V_{DS}=30V, V_{GS}=10V, I_D=4A$	---	14	21	nC
$Q_{gs}$	Gate-Source Charge <sup>2,3</sup>		---	2.9	5	
$Q_{gd}$	Gate-Drain Charge <sup>2,3</sup>		---	2.3	4	
$T_{d(on)}$	Turn-On Delay Time <sup>2,3</sup>	$V_{DD}=30V, V_{GS}=10V, R_G=3.3\Omega$ $I_D=1A$	---	3.9	7	ns
$T_r$	Rise Time <sup>2,3</sup>		---	12.6	24	
$T_{d(off)}$	Turn-Off Delay Time <sup>2,3</sup>		---	23.1	44	
$T_f$	Fall Time <sup>2,3</sup>		---	6.7	13	
$C_{iss}$	Input Capacitance	$V_{DS}=15V, V_{GS}=0V, F=1\text{MHz}$	---	800	1160	pF
$C_{oss}$	Output Capacitance		---	380	550	
$C_{rss}$	Reverse Transfer Capacitance		---	115	170	
$R_g$	Gate resistance	$V_{GS}=0V, V_{DS}=0V, F=1\text{MHz}$	---	1.7	3.4	$\Omega$

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	5.5	A
$I_{SM}$	Pulsed Source Current		---	---	11	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=15A, R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$
3. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .

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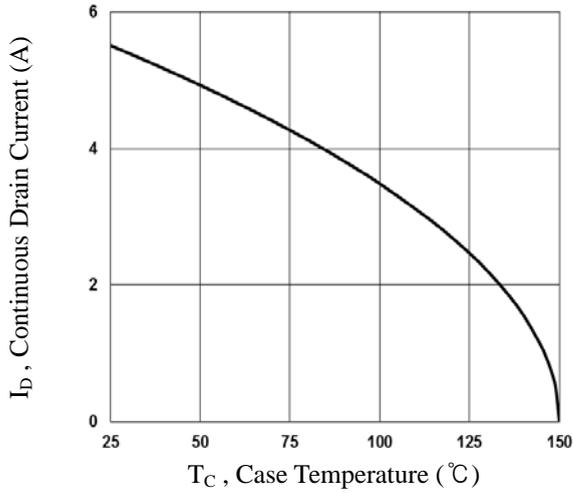


Fig.1 Continuous Drain Current vs.  $T_c$

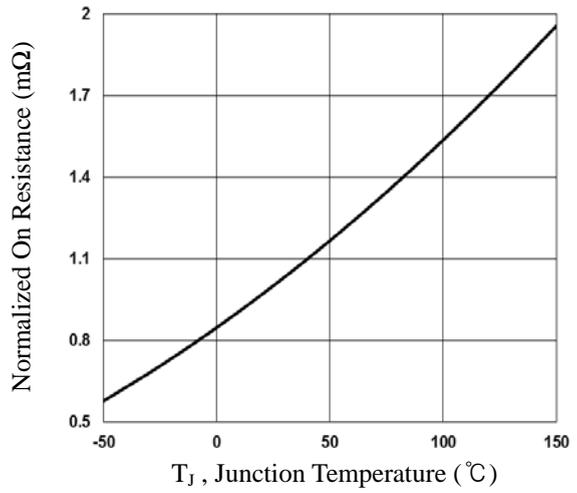


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_j$

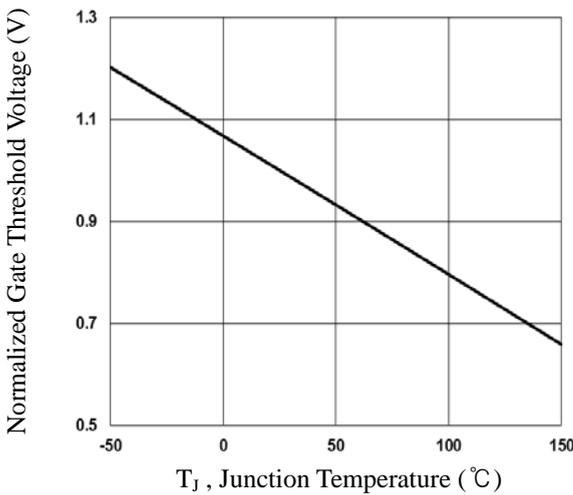


Fig.3 Normalized  $V_{th}$  vs.  $T_j$

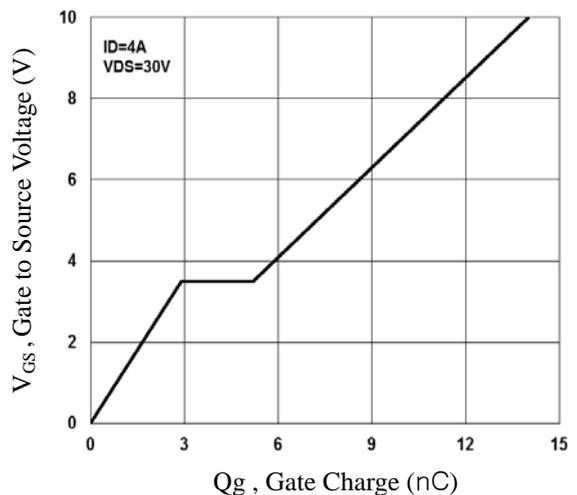


Fig.4 Gate Charge Waveform

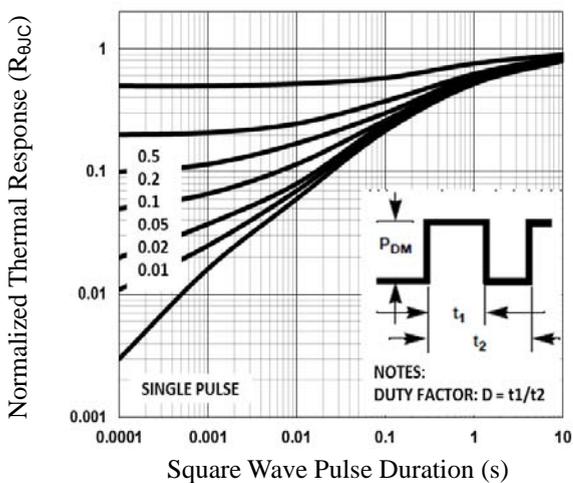


Fig.5 Normalized Transient Impedance

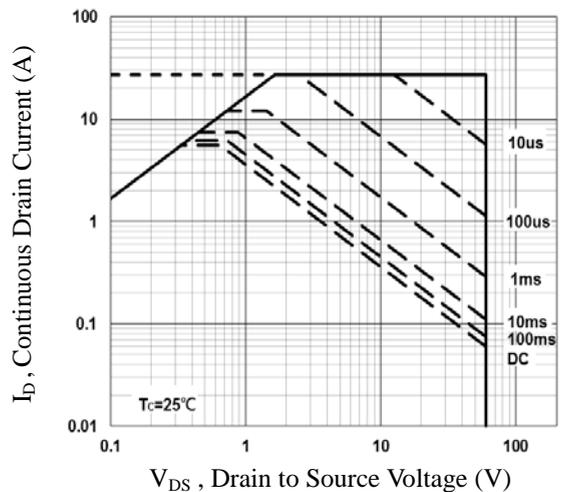


Fig.6 Maximum Safe Operation Area

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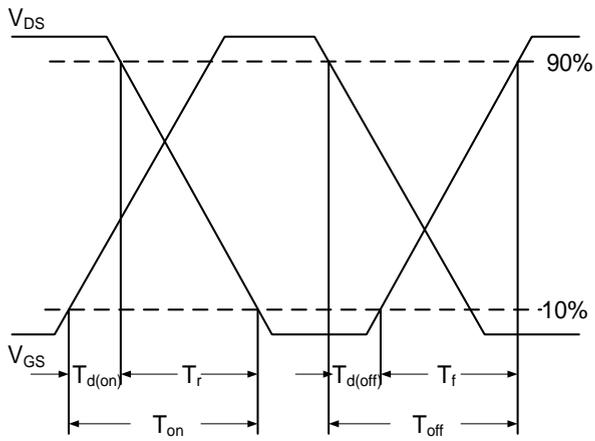


Fig.7 Switching Time Waveform

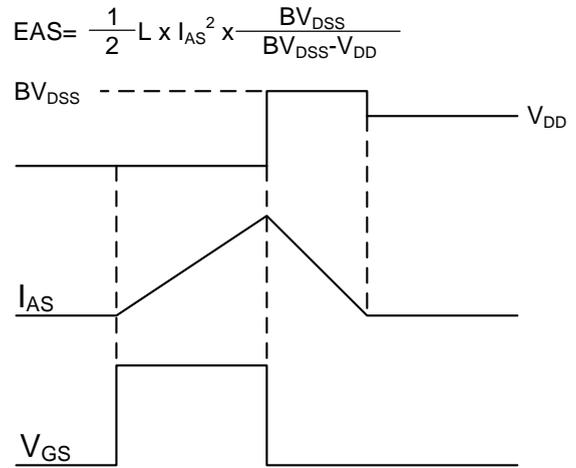
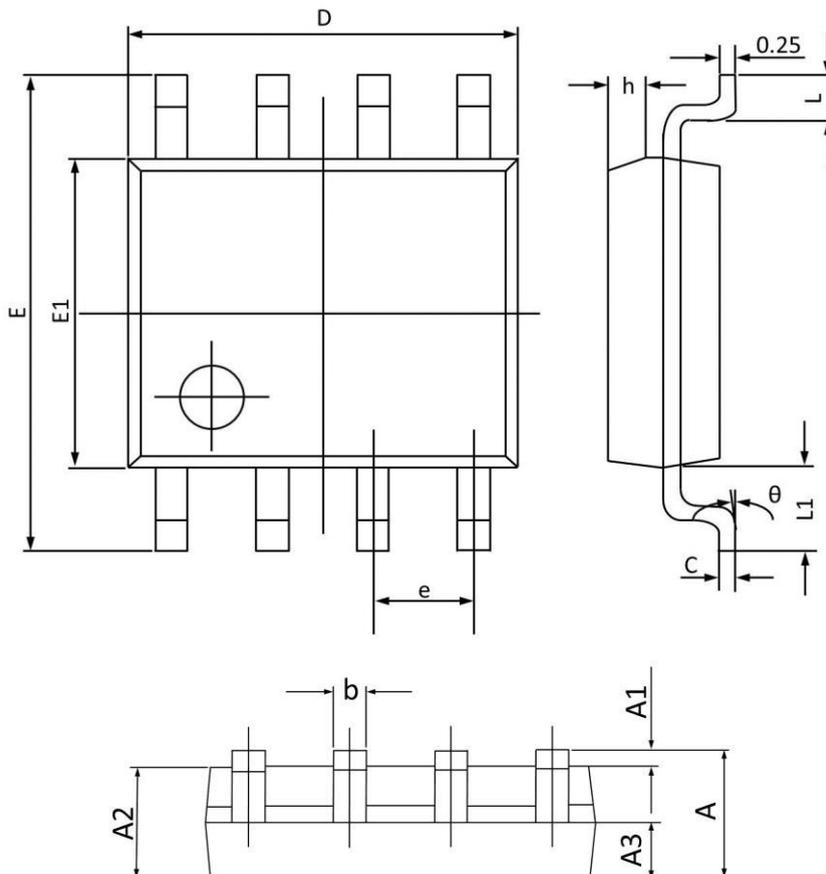


Fig.8 EAS Waveform

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### SOP-8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050(BSC)		0.041(BSC)	
$\theta$	0°	8°	0°	8°