

150V N-Channel MOSFETs

General Description

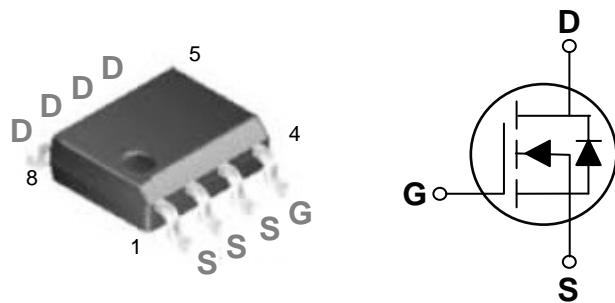
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
150V	65mΩ	4A

Features

- 150V, 4A, RDS(ON) = 65mΩ @ VGS = 10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

SOP-8 Pin Configuration



Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	150	V
V _{GS}	Gate-Source Voltage	± 25	V
I _D	Drain Current – Continuous (T _C =25 °C)	4	A
	Drain Current – Continuous (T _C =100 °C)	2.5	A
I _{DM}	Drain Current – Pulsed ¹	16	A
P _D	Power Dissipation (T _C =25 °C)	2.5	W
	Power Dissipation – Derate above 25 °C	0.02	W/°C
T _{STG}	Storage Temperature Range	-50 to 150	°C
T _J	Operating Junction Temperature Range	-50 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	50	°C/W



FTK04N15

150V N-Channel MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	150	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_D=1\text{mA}$	---	0.07	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=150\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=120\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 25\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=4\text{A}$	---	52	65	$\text{m}\Omega$
		$V_{\text{GS}}=6\text{V}$, $I_D=2\text{A}$	---	60	85	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	2	3	4	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=3\text{A}$	---	8	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge	$V_{\text{DS}}=75\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=4\text{A}$	---	30	60	nC
Q_{gs}	Gate-Source Charge		---	8.7	16	
Q_{gd}	Gate-Drain Charge		---	8	16	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=75\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=6\Omega$ $I_D=1\text{A}$	---	14.5	28	ns
T_r	Rise Time		---	19.2	18	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	33.6	60	
T_f	Fall Time		---	22.8	25	
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	1790	3000	pF
C_{oss}	Output Capacitance		---	160	300	
C_{rss}	Reverse Transfer Capacitance		---	82	150	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	1.4	2.8	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	4	A
			---	---	8	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

150V N-Channel MOSFETs

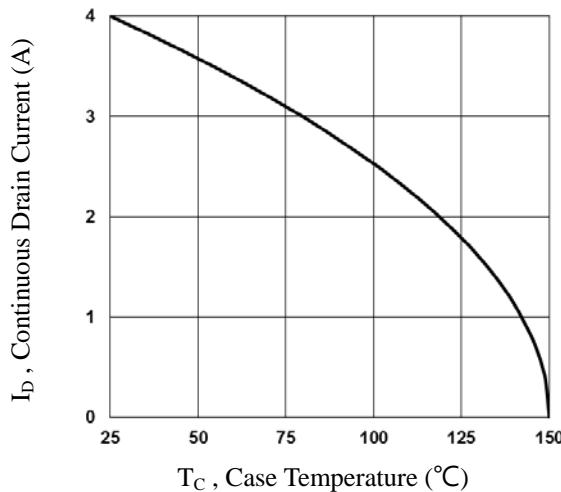


Fig.1 Continuous Drain Current vs. T_c

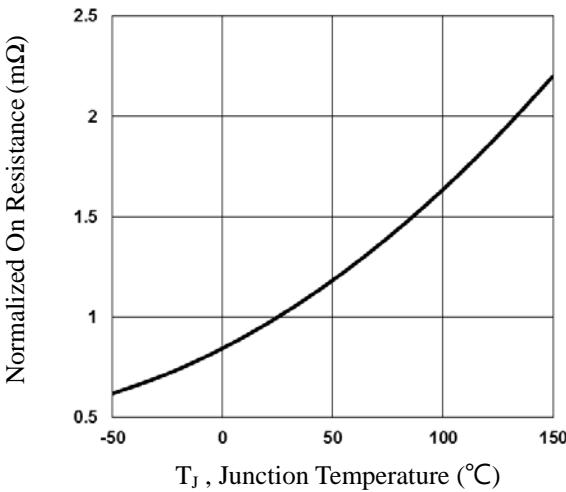


Fig.2 Normalized RD_{SON} vs. T_j

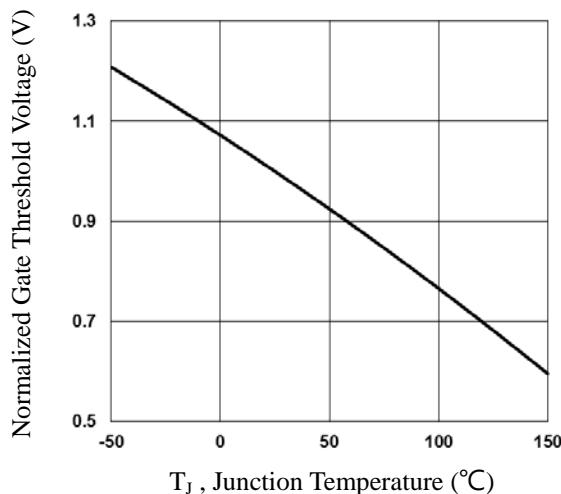


Fig.3 Normalized V_{th} vs. T_j

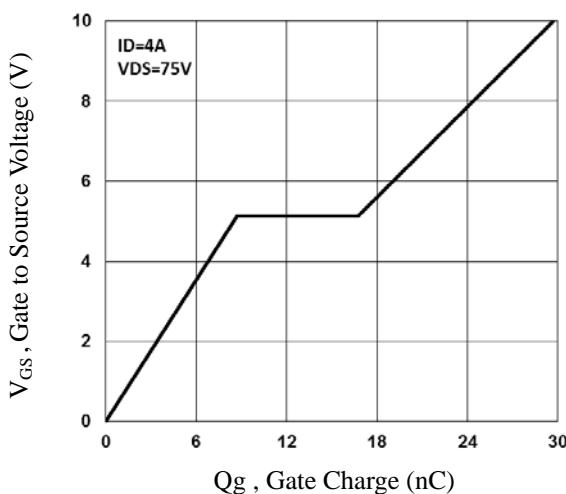


Fig.4 Gate Charge Characteristics

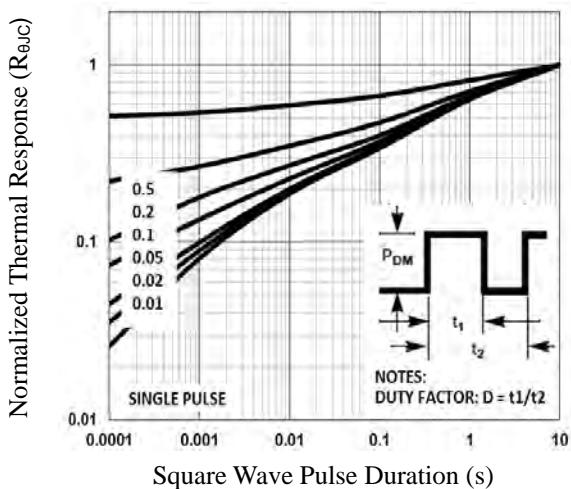


Fig.5 Normalized Transient Impedance

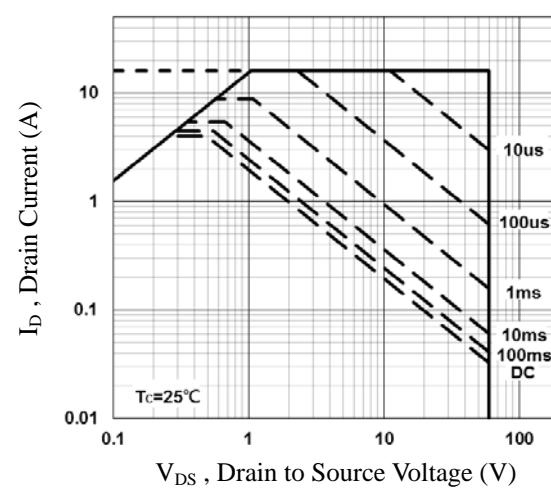


Fig.6 Maximum Safe Operation Area

150V N-Channel MOSFETs

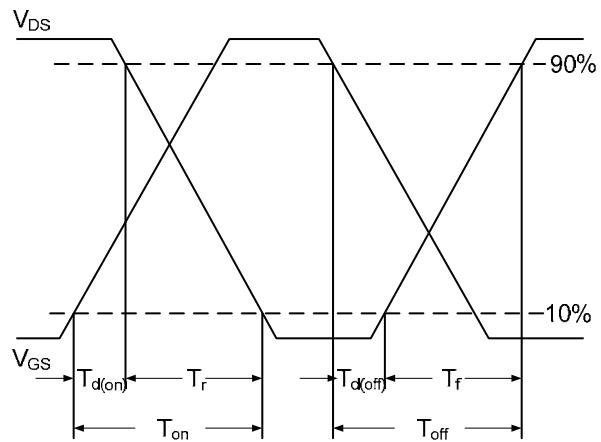


Fig.7 Switching Time Waveform

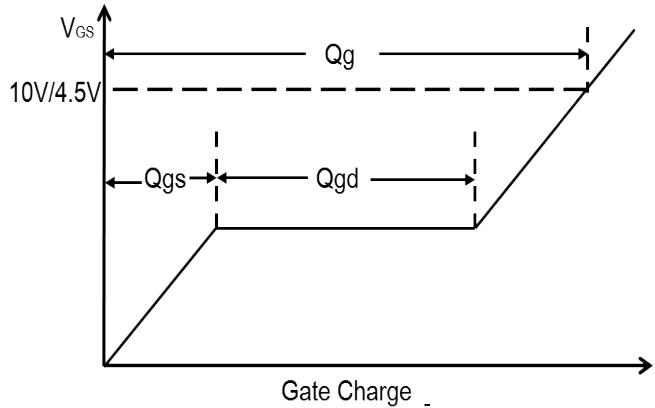
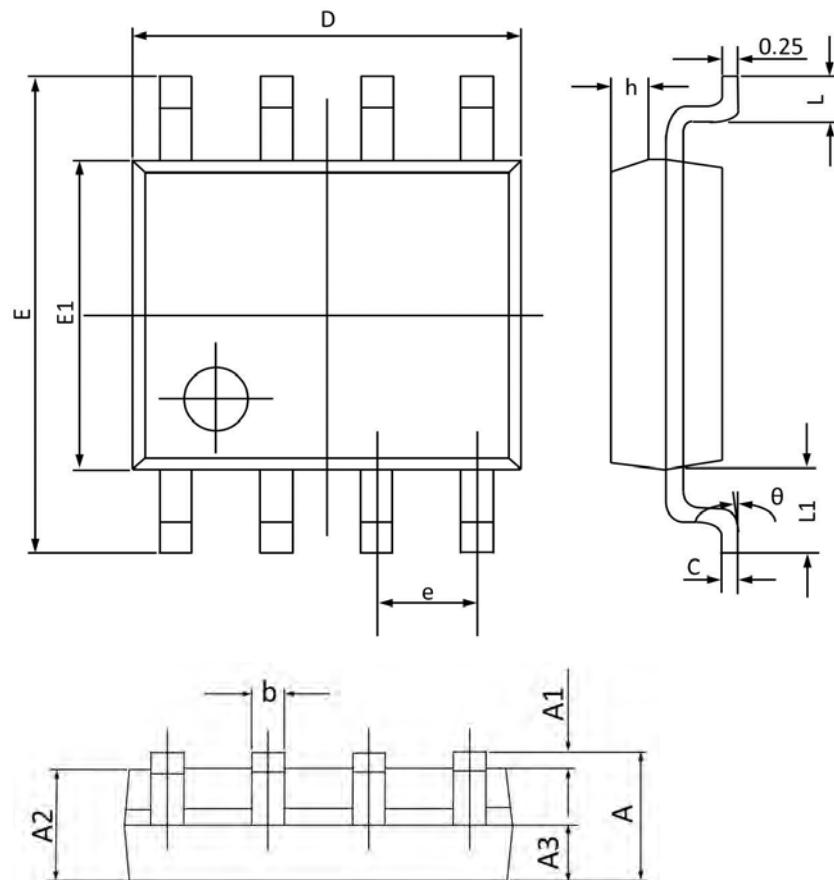


Fig.8 Gate Charge Waveform

150V N-Channel MOSFETs
SOP-8 PACKAGE INFORMATION


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.068
A1	0.100	0.250	0.004	0.009
A2	1.300	1.500	0.052	0.059
A3	0.600	0.700	0.024	0.027
b	0.390	0.480	0.016	0.018
c	0.210	0.260	0.009	0.010
D	4.700	5.100	0.186	0.200
E	5.800	6.200	0.229	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.019
L	0.500	0.800	0.019	0.031
L1	1.050(BSC)		0.041(BSC)	
θ	0°	8°	0°	8°