

60V P-Channel MOSFETs

General Description

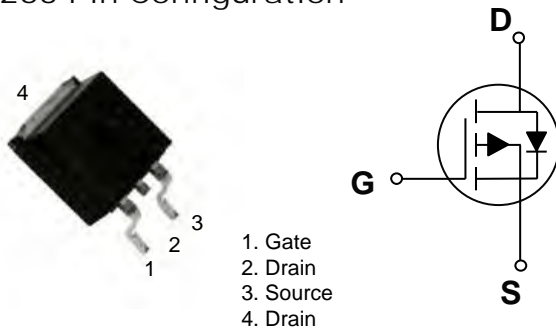
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
-60V	9.2mΩ	-75A

Features

- -60V,-75A, $R_{DS(ON)} = 9.2m\Omega @ V_{GS} = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

TO-263 Pin Configuration



Applications

- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ\text{C}$)	-75	A
	Drain Current – Continuous ($T_c=100^\circ\text{C}$)	-47.5	A
I_{DM}	Drain Current – Pulsed ¹	-300	A
EAS	Single Pulse Avalanche Energy ²	320	mJ
IAS	Single Pulse Avalanche Current ²	80	A
P_D	Power Dissipation ($T_c=25^\circ\text{C}$)	183	W
	Power Dissipation – Derate above 25°C	1.47	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	0.68	$^\circ\text{C}/\text{W}$



FTK6901DD

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Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$, $I_D=1mA$	---	0.036	---	V/ $^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V, T_J=25\text{ }^\circ\text{C}$	---	---	-1	μA
		$V_{DS}=-48V, V_{GS}=0V, T_J=125\text{ }^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA

On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-20A$	---	7.6	9.2	m Ω
		$V_{GS}=-4.5V, I_D=-10A$	---	9.3	12	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.2	-1.6	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-6.3	---	mV/ $^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{DS}=-10V, I_D=-3A$	---	18	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{3,4}	$V_{DS}=-30V, V_{GS}=-10V, I_D=-5A$	---	138	210	nC
Q_{gs}	Gate-Source Charge ^{3,4}		---	25.4	38	
Q_{gd}	Gate-Drain Charge ^{3,4}		---	12.8	19	
$T_{d(on)}$	Turn-On Delay Time ^{3,4}	$V_{DD}=-48V, V_{GS}=-10V, R_G=6\Omega$ $I_D=-1A$	---	70	140	ns
T_r	Rise Time ^{3,4}		---	205	410	
$T_{d(off)}$	Turn-Off Delay Time ^{3,4}		---	402	804	
T_f	Fall Time ^{3,4}		---	197	394	
C_{iss}	Input Capacitance	$V_{DS}=-25V, V_{GS}=0V, F=1MHz$	---	8620	12930	pF
C_{oss}	Output Capacitance		---	486	730	
C_{rss}	Reverse Transfer Capacitance		---	288	430	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current	$V_G=V_D=0V$, Force Current	---	---	-75	A
I_{SM}	Pulsed Source Current		---	---	-150	A
V_{SD}	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25\text{ }^\circ\text{C}$	---	---	-1	V
t_{rr}	Reverse Recovery Time	$V_{GS}=10V, I_S=10A, di/dt=100A/\mu s$	---	370	---	ns
Q_{rr}	Reverse Recovery Charge	$T_J=25\text{ }^\circ\text{C}$	---	1740	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. $V_{DD}=50V, V_{GS}=10V, L=0.1mH, I_{AS}=80A, R_G=25\Omega$, Starting $T_J=25\text{ }^\circ\text{C}$.
3. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

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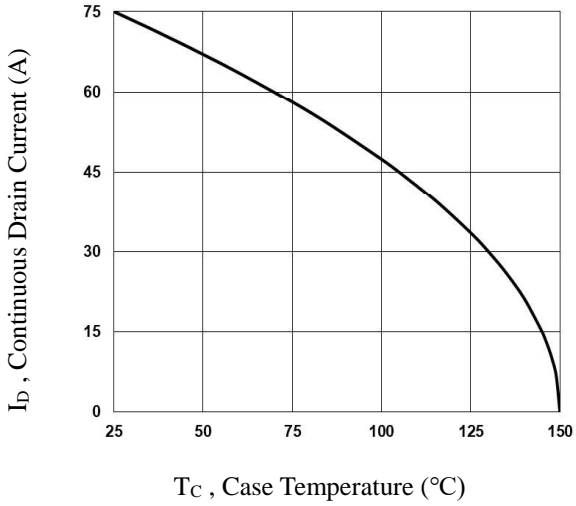


Fig.1 Continuous Drain Current vs. T_c

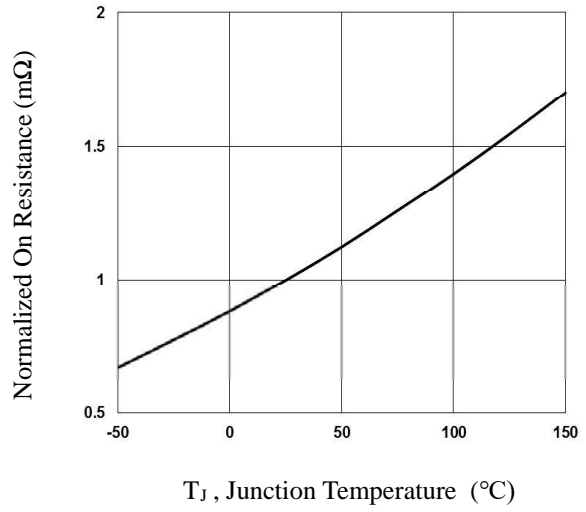


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

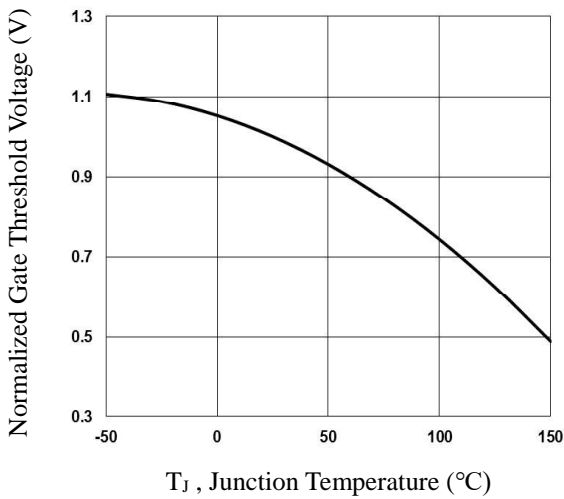


Fig.3 Normalized V_{th} vs. T_j

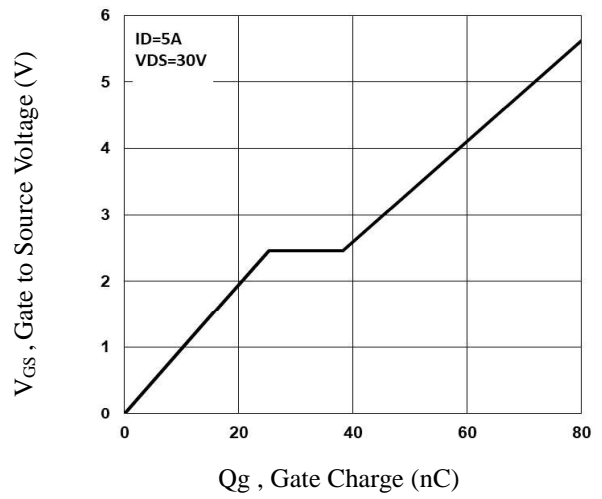


Fig.4 Gate Charge Characteristics

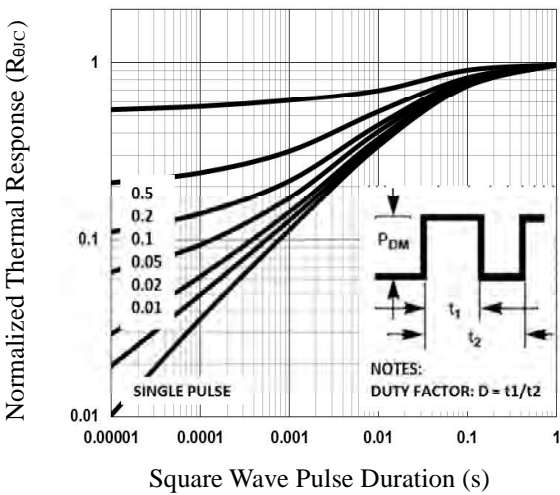


Fig.5 Normalized Transient Impedance

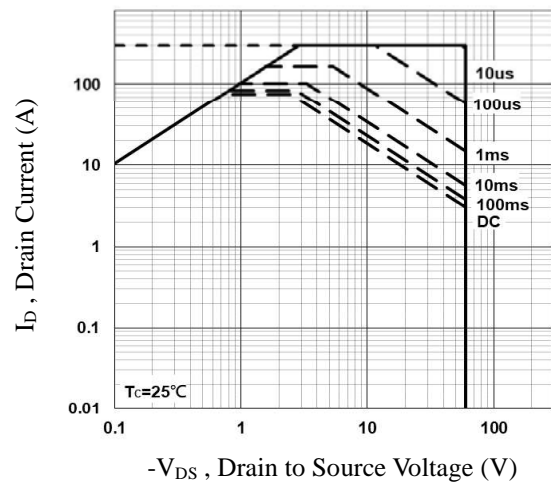


Fig.6 Maximum Safe Operation Area

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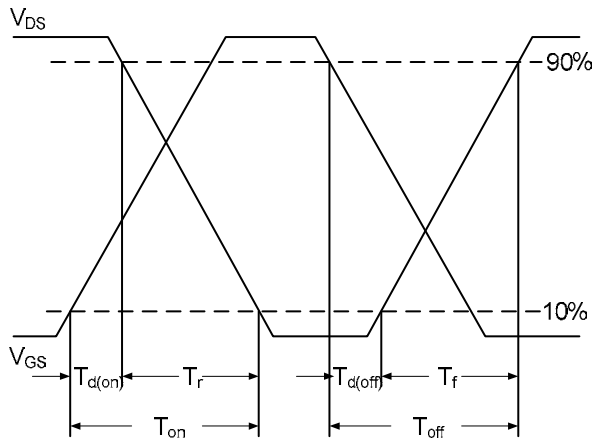


Fig.7 Switching Time Waveform

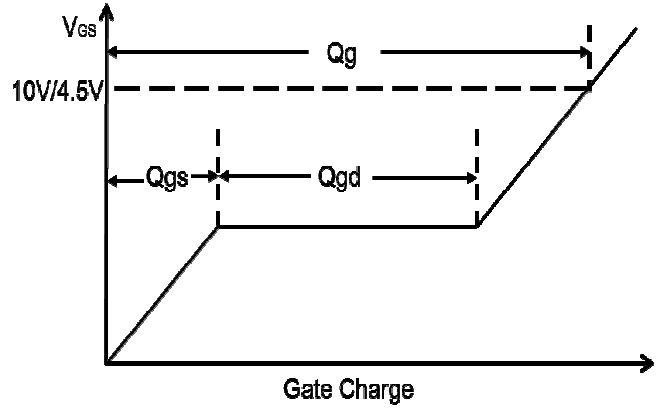


Fig.8 Gate Charge Waveform

