

## 60V P-Channel MOSFETs

### General Description

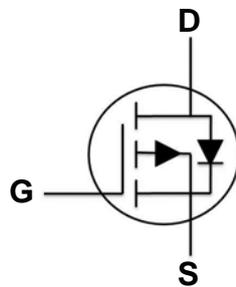
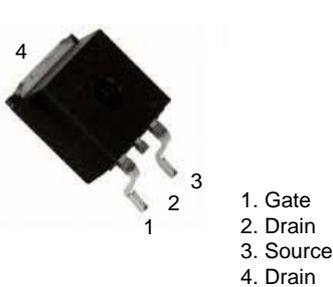
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
-60V	27mΩ	-42A

### Features

- -60V, -42A,  $R_{DS(ON)} = 27m\Omega @ V_{GS} = -10V$
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

### TO-263 Pin Configuration



### Applications

- POL Applications
- Load Switch
- LED Application

### Absolute Maximum Ratings (Tc=25 °C unless otherwise noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-60	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>C</sub> =25°C)	-42	A
	Drain Current – Continuous (T <sub>C</sub> =100°C)	-26.5	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	-168	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	105	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	46	A
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25°C)	121	W
	Power Dissipation – Derate above 25°C	0.97	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	62.5	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction to Case	---	1.03	°C/W



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Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	---	---	V
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	-1	$\mu A$
		$V_{DS}=-48V, V_{GS}=0V, T_J=125^\circ\text{C}$	---	---	-10	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	$\pm 100$	nA

### On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V, I_D=-8A$	---	22	27	m $\Omega$
		$V_{GS}=-4.5V, I_D=-6A$	---	28	36	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}, I_D=-250\mu A$	-1.0	-1.6	-2.5	V
gfs	Forward Transconductance	$V_{DS}=-10V, I_D=-3A$	---	18	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=-30V, V_{GS}=-10V, I_D=-5A$	---	43.8	88	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	4.6	9	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	8.3	17	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=-30V, V_{GS}=-10V, R_G=6\Omega$ $I_D=-1A$	---	25	50	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	13.8	28	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	148	290	
$T_f$	Fall Time <sup>3, 4</sup>		---	51	100	
$C_{iss}$	Input Capacitance	$V_{DS}=-25V, V_{GS}=0V, F=1\text{MHz}$	---	2595	3900	pF
$C_{oss}$	Output Capacitance		---	162	240	
$C_{riss}$	Reverse Transfer Capacitance		---	115	170	

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	-42	A
$I_{SM}$	Pulsed Source Current		---	---	-70	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	-1	V
$T_{rr}$	Reverse Recovery Time	$V_R=30V, I_S=10A$	---	150	---	nS
$Q_{rr}$	Reverse Recovery Charge	$di/dt=100A/\mu s, T_J=25^\circ\text{C}$	---	380	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=46A, R_G=25\Omega$ , Starting  $T_J=25^\circ\text{C}$ .
3. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.

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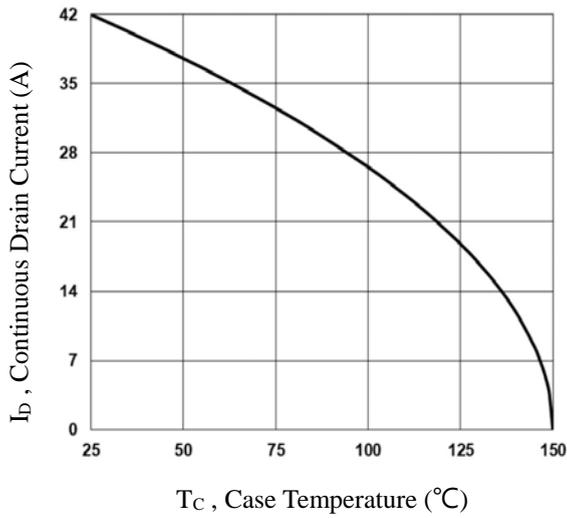


Fig.1 Continuous Drain Current vs.  $T_C$

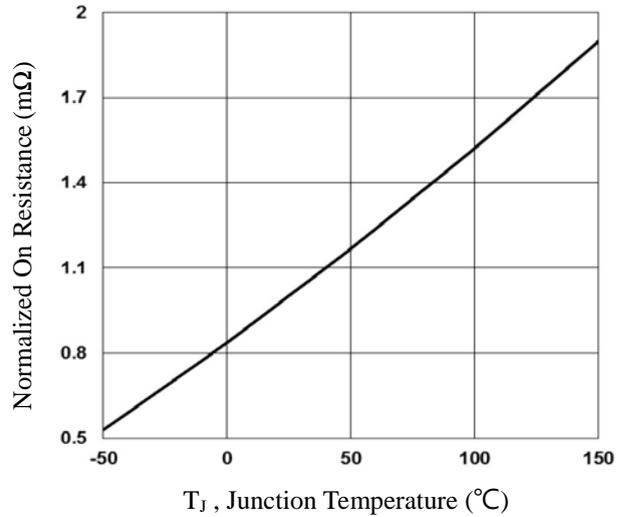


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$

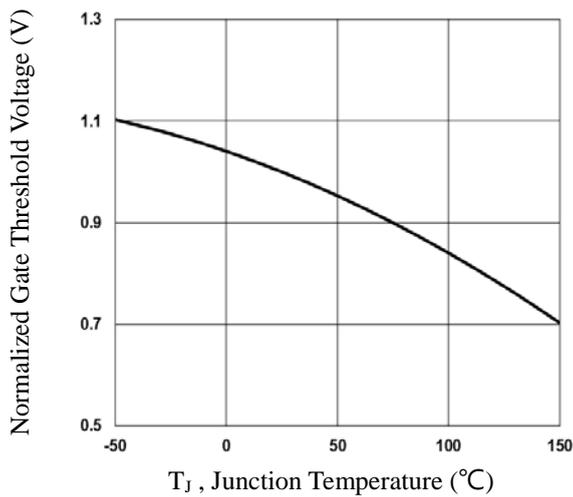


Fig.3 Normalized  $V_{th}$  vs.  $T_J$

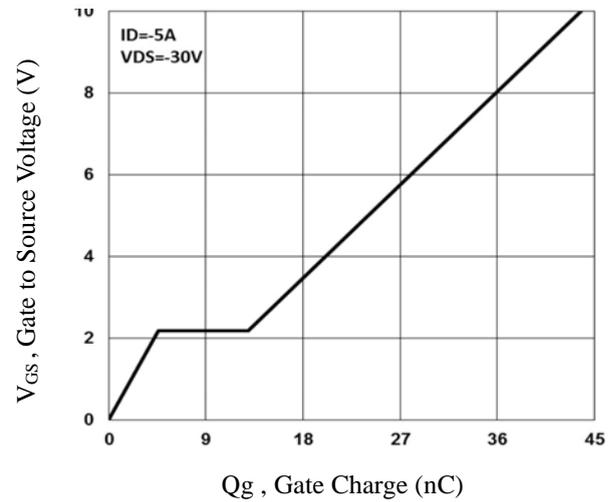


Fig.4 Gate Charge Characteristics

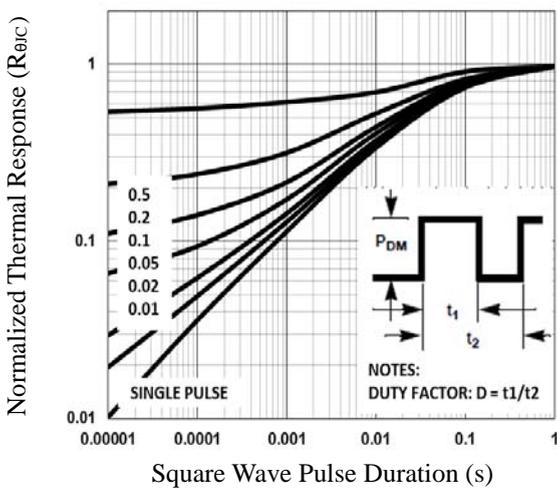


Fig.5 Normalized Transient Impedance

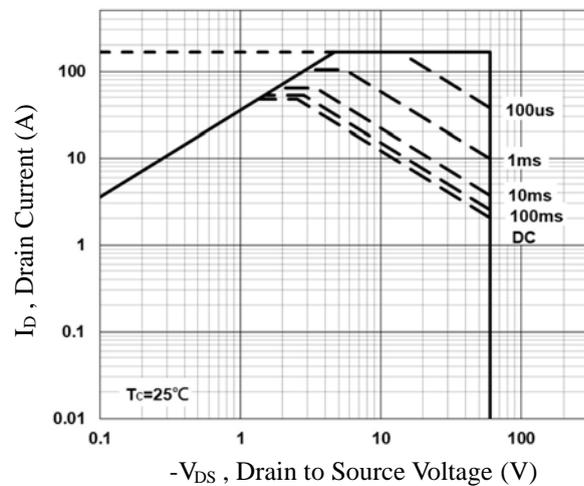


Fig.6 Maximum Safe Operation Area

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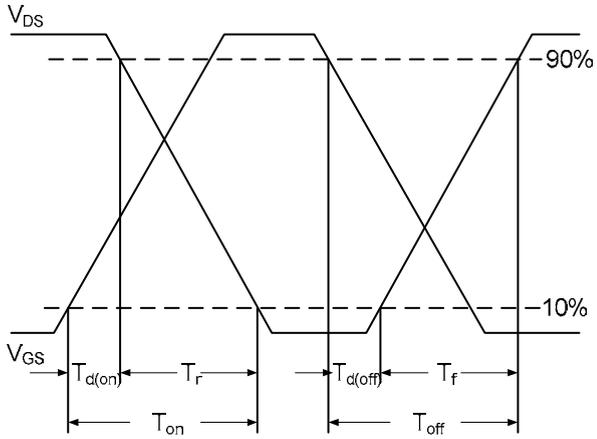


Fig.7 Switching Time Waveform

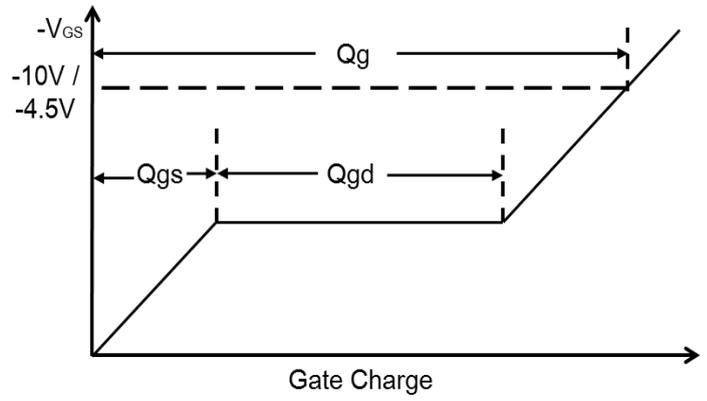


Fig.8 Gate Charge Waveform



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### TO-263 PACKAGE INFORMATION

