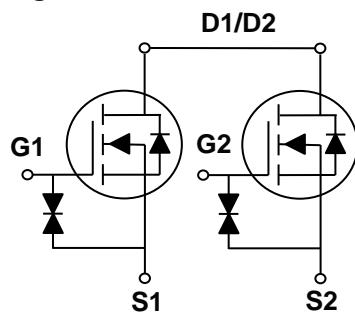
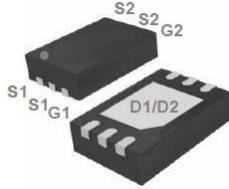


20V Dual N-Channel MOSFETs

General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

DFN2x3 Dual Pin Configuration



BVDSS	RDS(ON)	ID
20V	8.2mΩ	11A

Features

- 20V, 11A, RDS(ON) = 8.2mΩ @ VGS = 4.5V
- Improved dv/dt capability
- Fast switching
- G-S ESD Protection Diode Embedded
- Green Device Available

Applications

- Handheld Instruments
- POL Applications
- Battery Protection Applications

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	± 12	V
I _D	Drain Current – Continuous (Tc=25 °C)	11	A
	Drain Current – Continuous (Tc=70 °C)	8.8	A
I _{DM}	Drain Current – Pulsed ¹	70	A
P _D	Power Dissipation (Tc=25 °C)	1.56	W
	Power Dissipation – Derate above 25 °C	0.0125	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	80	°C/W



FTK2516DFN23

20V Dual N-Channel MOSFETs

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	20	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=18\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=16\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=70^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 12\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 10	μA

On Characteristics

$\text{R}_{\text{DS(ON)}}$	Static Drain-Source On-Resistance ³	$V_{\text{GS}}=4.5\text{V}$, $I_D=5.5\text{A}$	4.5	6	8.2	$\text{m}\Omega$
		$V_{\text{GS}}=4\text{V}$, $I_D=5.5\text{A}$	4.7	6.2	8.5	$\text{m}\Omega$
		$V_{\text{GS}}=3.7\text{V}$, $I_D=5.5\text{A}$	5	6.5	9	$\text{m}\Omega$
		$V_{\text{GS}}=3.1\text{V}$, $I_D=5.5\text{A}$	5.5	7	9.4	$\text{m}\Omega$
		$V_{\text{GS}}=2.5\text{V}$, $I_D=5.5\text{A}$	6	8.2	11	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	0.5	0.72	1.5	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}$, $I_D=5.5\text{A}$	---	20	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2,3}	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $I_D=11\text{A}$	---	15	30	nC
Q_{gs}	Gate-Source Charge ^{2,3}		---	2.8	5.6	
Q_{gd}	Gate-Drain Charge ^{2,3}		---	4.4	8.8	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2,3}	$V_{\text{DD}}=15\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=6\Omega$	---	28	56	ns
T_r	Rise Time ^{2,3}		---	64	128	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2,3}		---	60	120	
T_f	Fall Time ^{2,3}		---	55	110	
C_{iss}	Input Capacitance	$V_{\text{DS}}=10\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	1350	2500	pF
C_{oss}	Output Capacitance		---	185	350	
C_{rss}	Reverse Transfer Capacitance		---	160	300	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	11	A
V_{SD}	Diode Forward Voltage ²	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

20V Dual N-Channel MOSFETs

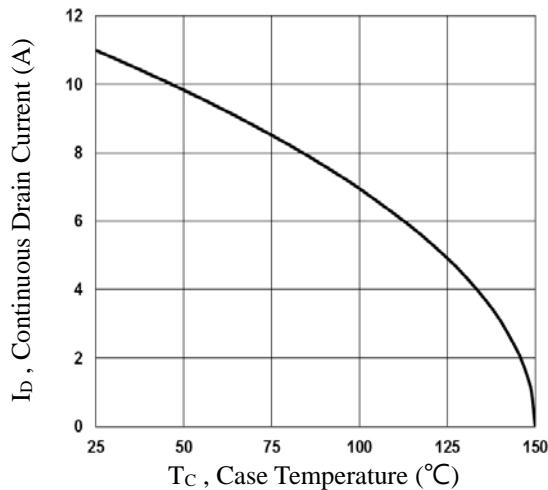


Fig.1 Continuous Drain Current vs. T_C

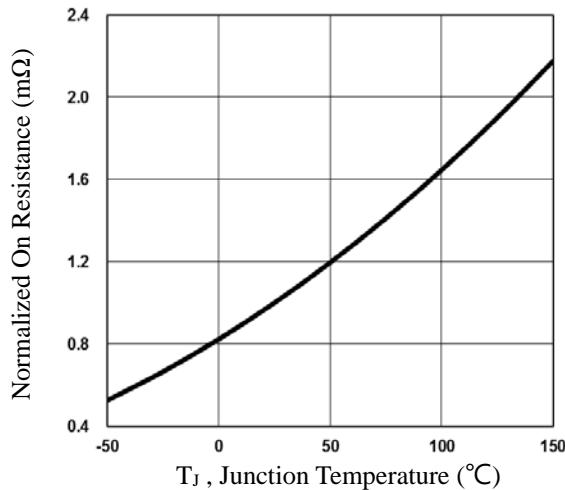


Fig.2 Normalized RDS(on) vs. T_J

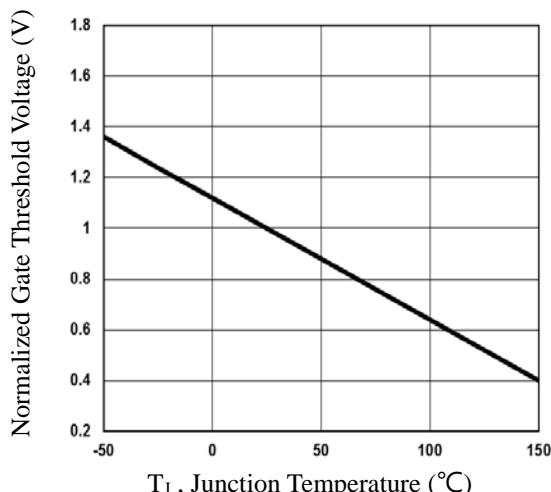


Fig.3 Normalized V_{th} vs. T_J

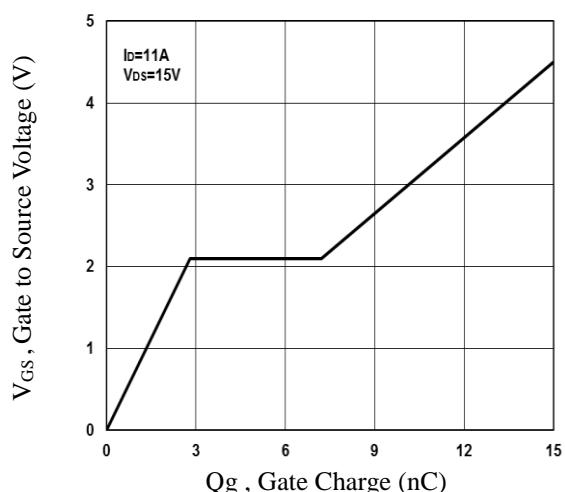


Fig.4 Gate Charge Waveform

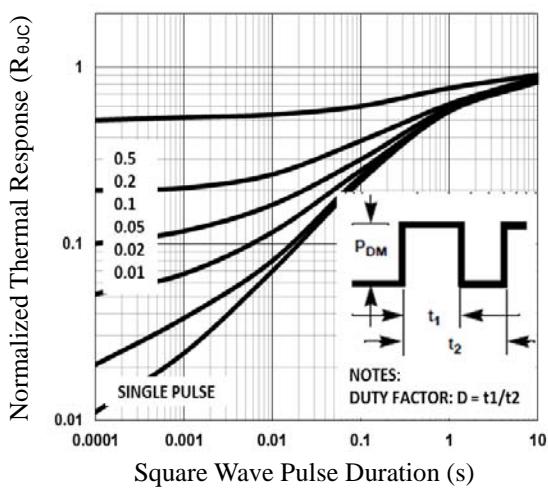


Fig.5 Normalized Transient Response

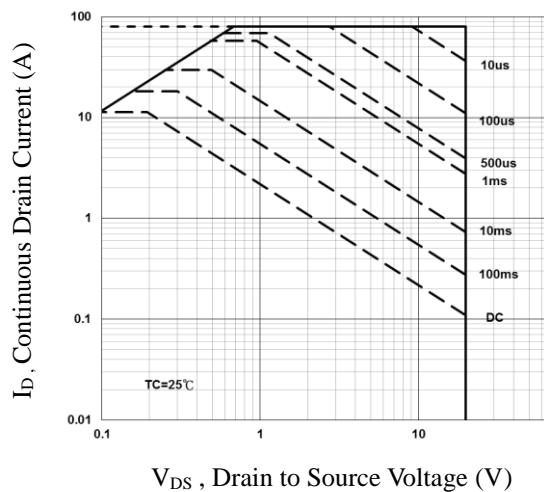


Fig.6 Maximum Safe Operation Area

20V Dual N-Channel MOSFETs

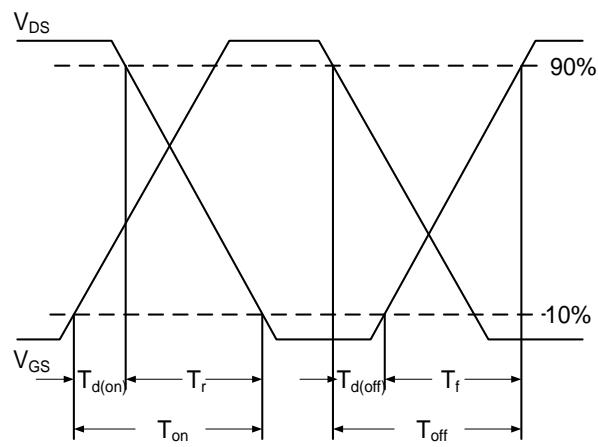


Fig.7 Switching Time Waveform

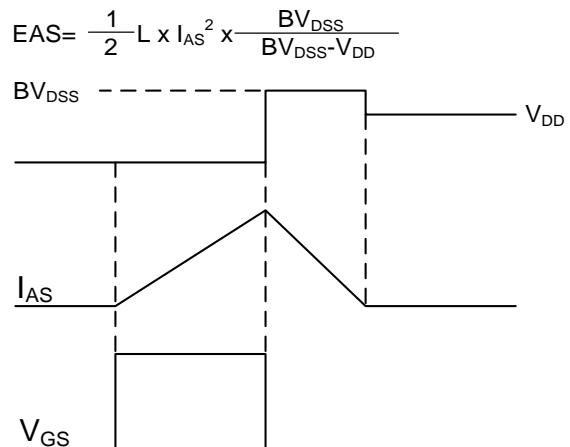
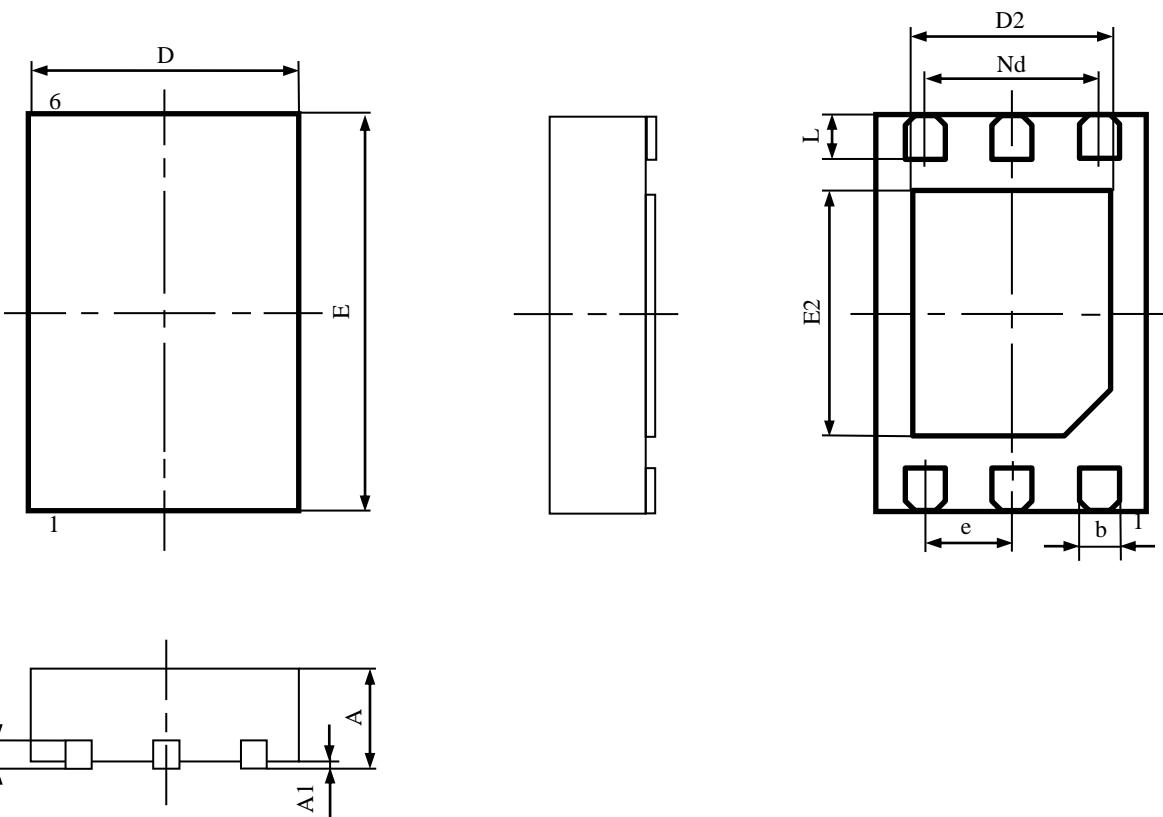


Fig.8 EAS Waveform

20V Dual N-Channel MOSFETs

DFN2X3 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.800	0.700	0.031	0.028
A1	0.050	0.02typ.	0.002	0.001typ.
b	0.350	0.200	0.014	0.008
c	0.250	0.180	0.010	0.007
D	2.100	1.900	0.083	0.075
D2	1.600	1.400	0.063	0.055
e	0.5BSC		0.02BSC	
Nd	1.0BSC		0.04BSC	
E	3.100	2.900	0.122	0.114
E2	1.750	1.650	0.069	0.065
L	0.400	0.300	0.016	0.012

20V Dual N-Channel MOSFETs**DFN2X3 RECOMMENDED LAND PATTERN**