

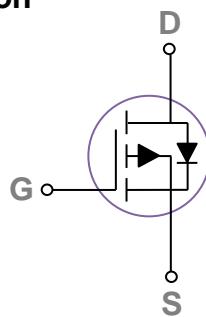
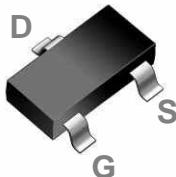
General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
-60V	200mΩ	-2A

Features

- -60V,-2A, RDS(ON) =200mΩ @VGS = -10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

SOT23 Pin Configuration**Applications**

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings T_c=25 °C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-60	V
V _{Gs}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _A =25 °C)	-2	A
	Drain Current – Continuous (T _A =70 °C)	-1.6	A
I _{DM}	Drain Current – Pulsed ¹	-6	A
P _D	Power Dissipation (T _A =25 °C)	1.56	W
	Power Dissipation – Derate above 25 °C	0.012	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	80	°C/W



RM2309

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=-250\mu\text{A}$	-60	---	---	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	BV_{DSS} Temperature Coefficient	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	---	-0.05	---	$\text{V}/^\circ\text{C}$
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=-60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
		$V_{\text{DS}}=-48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-2\text{A}$	---	200	220	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_{\text{D}}=-1.5\text{A}$	---	250	300	$\text{m}\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=-250\mu\text{A}$	-1.2	-1.9	-2.5	V
			---	5	---	$\text{mV}/^\circ\text{C}$
g_{fs}	Forward Transconductance	$V_{\text{DS}}=-10\text{V}$, $I_{\text{D}}=-2\text{A}$	---	3.5	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2, 3}	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_{\text{D}}=-2\text{A}$	---	8.2	12	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	1.8	3.6	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	1.5	3	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2, 3}	$V_{\text{DD}}=-30\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_{\text{G}}=6\Omega$	---	5.2	10	ns
T_r	Rise Time ^{2, 3}		---	19	36	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2, 3}		---	35	67	
T_f	Fall Time ^{2, 3}		---	10.6	20	
C_{iss}	Input Capacitance	$V_{\text{DS}}=-30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	425	615	pF
C_{oss}	Output Capacitance		---	35	50	
C_{rss}	Reverse Transfer Capacitance		---	20	30	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $f=1\text{MHz}$	---	17	---	Ω

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	-2	A
I_{SM}	Pulsed Source Current		---	---	-4	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{s}}=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

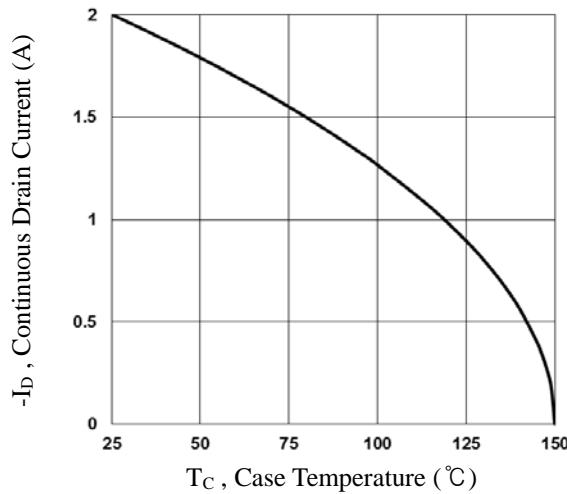


Fig.1 Continuous Drain Current vs.TC

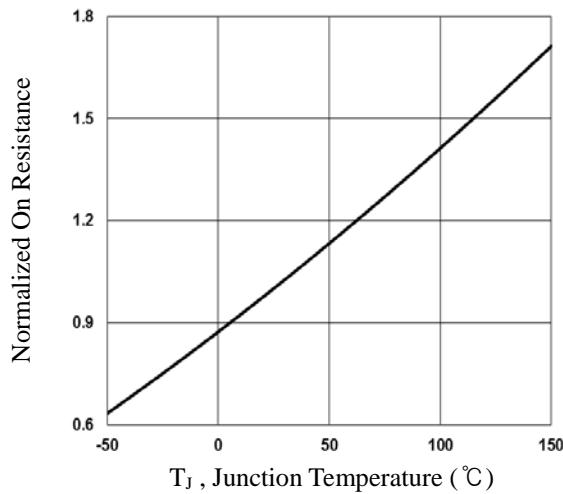


Fig.2 NormalizedRDS(ON)vs.TJ

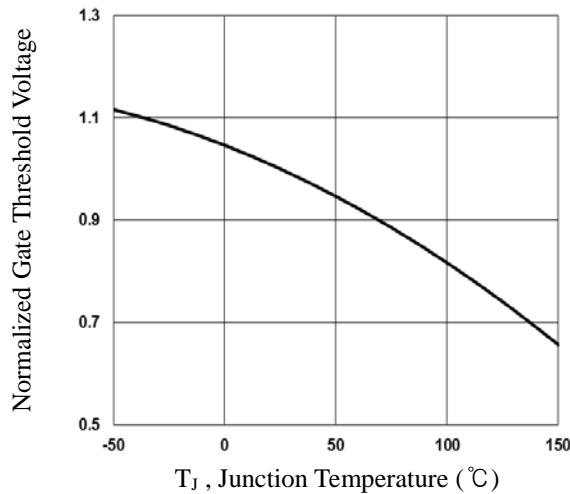


Fig.3 NormalizedVthvs.TJ

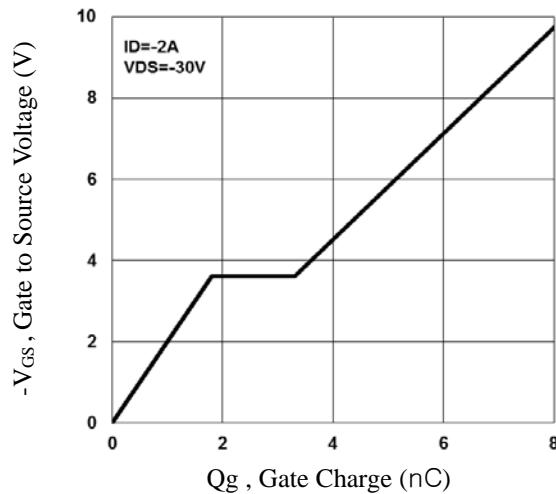


Fig.4 Gate Charge Waveform

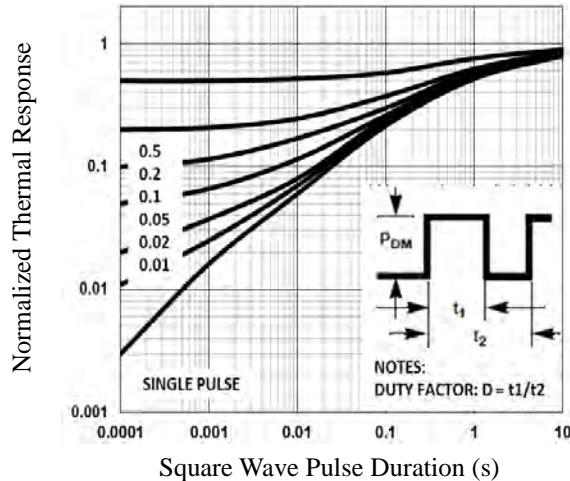


Fig.5 Normalized Transient Impedance

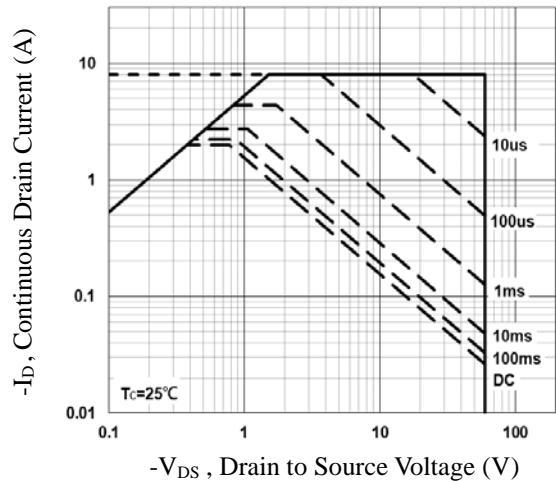


Fig.6 Maximum Safe Operation Area

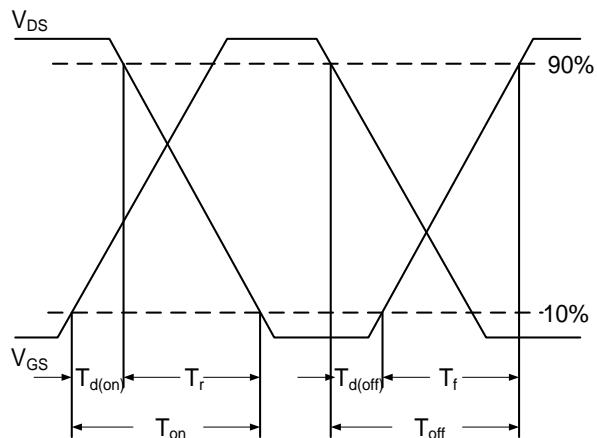


Fig.7 Switching Time Waveform

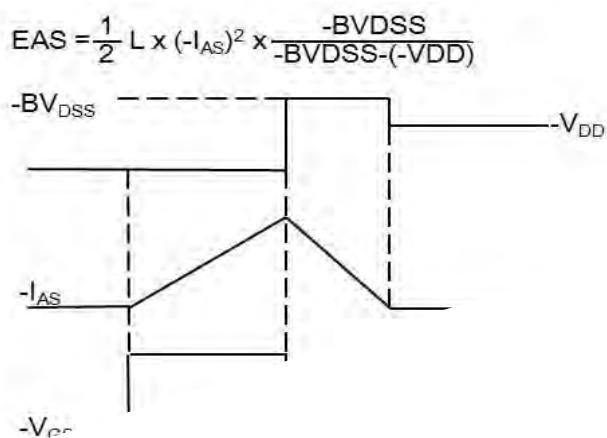
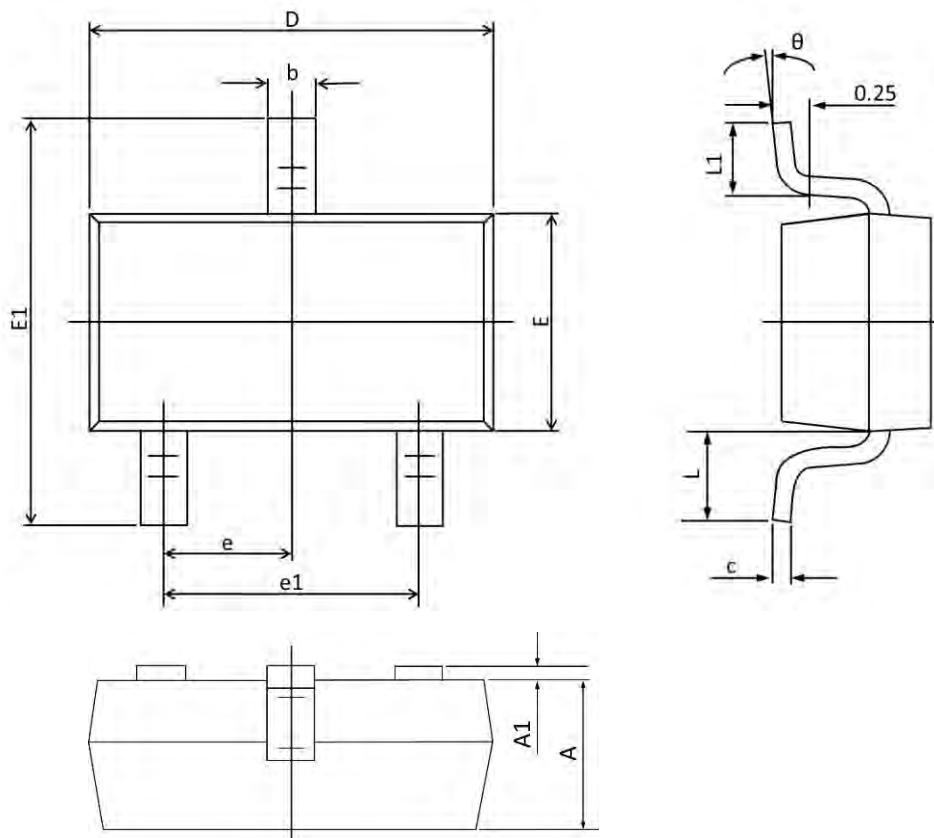


Fig.8 EAS Waveform

SOT23
PACKAGE INFORMATION


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.110	0.035	0.044
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.800	3.040	0.110	0.120
E	1.200	1.400	0.047	0.055
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
θ	1°	10°	1°	10°