

55V N-Channel MOSFETs

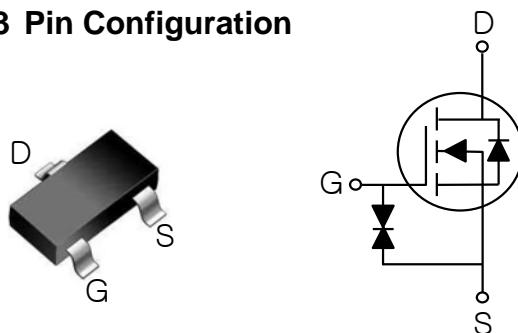
General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDS(ON)	ID
55V	1.3Ω	0.3A

Features

- 55V,0.3A, RDS(ON) = 1.3Ω@VGS=10V
- Improved dv/dt capability
- Fast switching
- Green Device Available
- G-S ESD Protection Diode Embedded
- ESD protected up to 2KV

SOT-23 Pin Configuration**Applications**

- Motor Drive
- Power Tools
- LED Lighting

Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	55	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current- Continuous (T _A =25°C)	0.3	A
	Drain Current- Continuous (T _A =70°C)	0.16	A
I _{DM}	Drain Current- Pulsed ¹	0.8	A
P _D	Power Dissipation (T _A =25°C)	0.35	W
	Power Dissipation-Derate above 25°C	0.003	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	—	357	°C/W



BSS138K

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Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	55	---	---	V
I_{DS}	Drain-Source Leakage Current	$V_{\text{DS}}=55\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=40\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	100	μA
I_{GS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 10	μA

On Characteristics

$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=0.2\text{A}$	---	1.3	1.5	Ω
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=0.1\text{A}$	---	1.5	2.5	Ω
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	0.8	1.1	1.5	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_{\text{D}}=0.2\text{A}$	---	0.5	---	S

Dynamic and switching Characteristics

Q_g	Total Gate Charge ^{2, 3}	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=0.2\text{A}$	---	3.7	---	nC
Q_{gs}	Gate-Source Charge ^{2, 3}		---	0.9	---	
Q_{gd}	Gate-Drain Charge ^{2, 3}		---	0.4	---	
$T_{\text{d(on)}}$	Turn-On Delay Time ^{2, 3}	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_{\text{G}}=6\Omega$	---	3	---	ns
T_r	Rise Time ^{2, 3}		---	5	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time ^{2, 3}		---	14	---	
T_f	Fall Time ^{2, 3}		---	9	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	25.5	---	pF
C_{oss}	Output Capacitance		---	17	---	
C_{rss}	Reverse Transfer Capacitance		---	7.8	---	

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	0.3	A
			---	---	0.6	A
I_{SM}	Pulsed Source Current					
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=0.2\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.4	V
t_{rr}	Reverse Recovery Time	$V_R=50\text{V}$, $I_s=0.2\text{A}$	---	3.4	---	ns
Q_{rr}	Reverse Recovery Charge	$dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	0.7	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

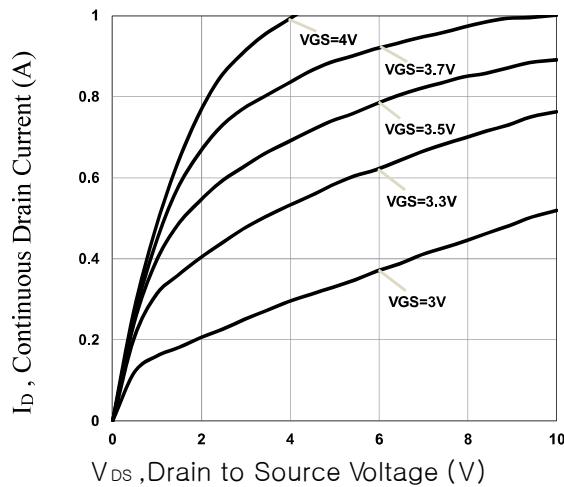


Fig.1 Typical Output Characteristics

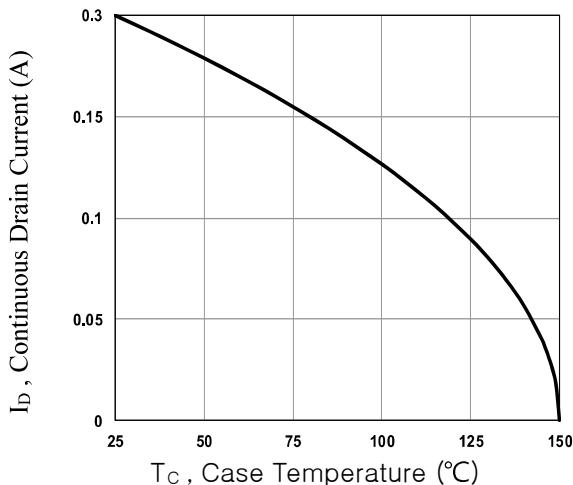


Fig.2 Continuous Drain Current vs. T_c

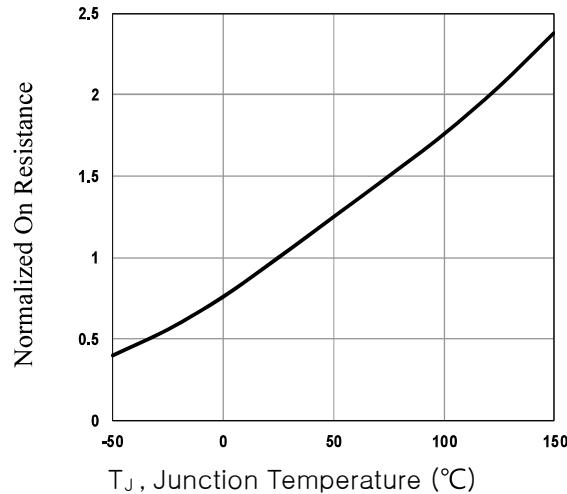


Fig.3 Normalized $R_{DS(on)}$ vs. T_J

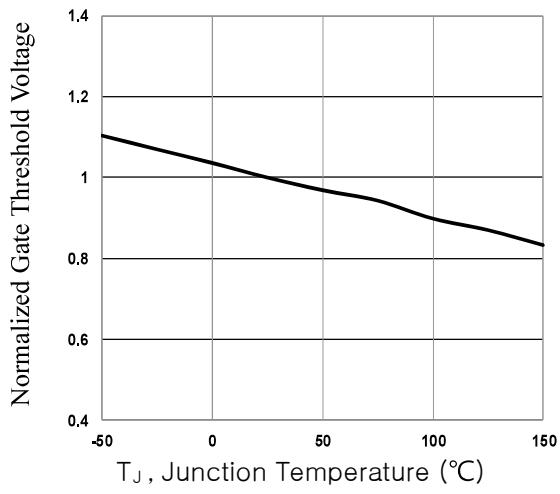


Fig.4 Normalized V_{th} vs. T_J

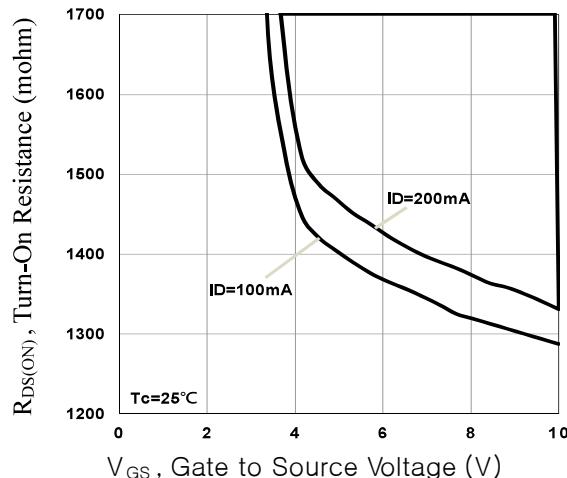


Fig.5 Turn-On Resistance vs. V_{GS}

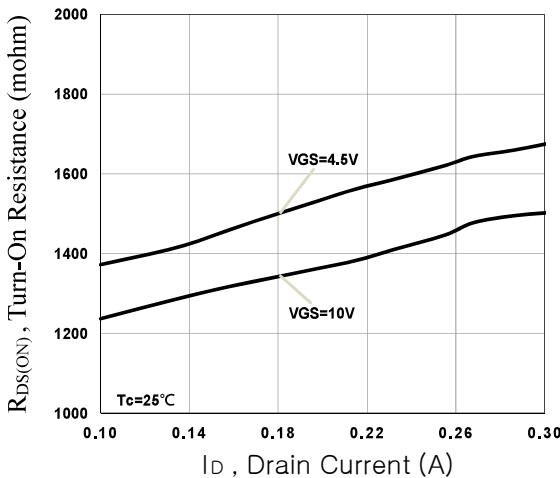


Fig.6 Turn-On Resistance vs. I_D

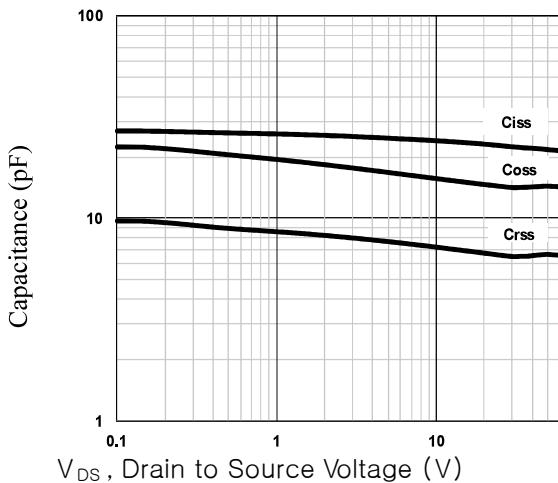


Fig.7 Capacitance Characteristics

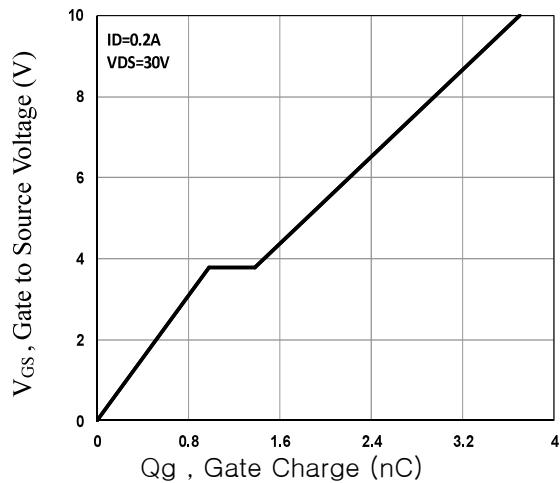


Fig.8 Gate Charge Characteristics

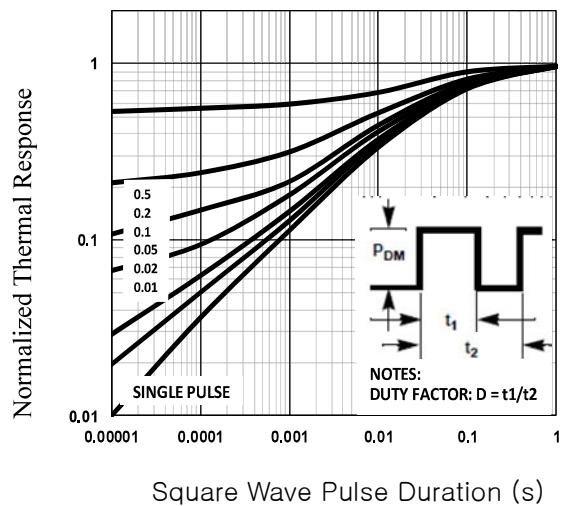


Fig.9 Normalized Transient

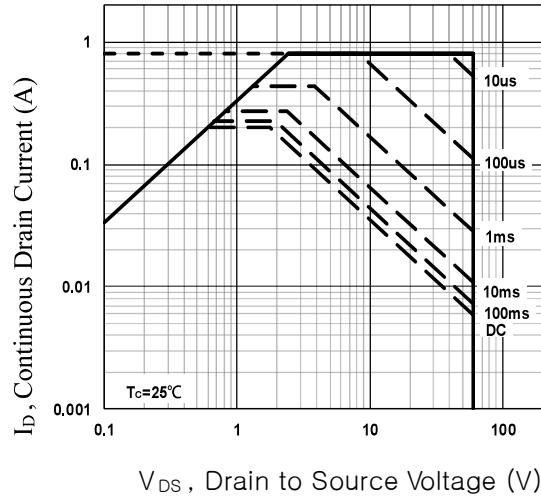
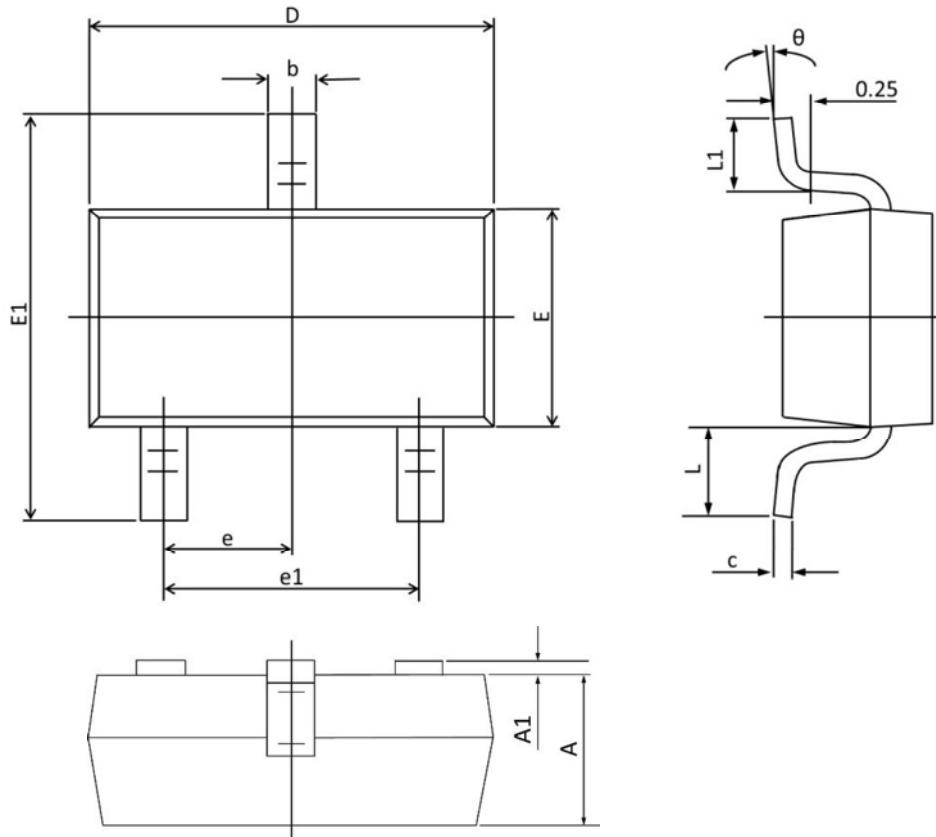


Fig.10 Maximum Safe Operation Area

SOT-23 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.001	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.080	0.180	0.003	0.008
D	2.700	3.100	0.106	0.122
E	1.100	1.500	0.043	0.059
E1	2.100	2.640	0.080	0.104
e	0.950 TYP.		0.037 TYP.	
e1	1.780	2.040	0.070	0.080
L	0.550 REF.		0.022 REF.	
L1	0.100	0.500	0.004	0.020
θ	1°	10°	1°	10°