

## 40V Dual N-Channel MOSFETs

### General Description

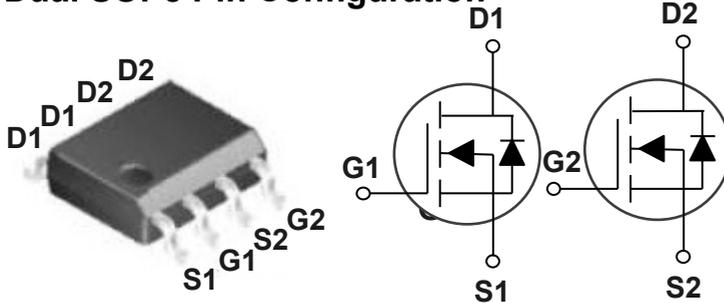
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
40V	28mΩ	8.5A

### Features

- 40V,8.5A, RDS(ON) =28mΩ @VGS = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Dual SOP8 Pin Configuration



### Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2<sup>nd</sup> SR

### Absolute Maximum Ratings Tc=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>C</sub> =25°C)	8.5	A
	Drain Current – Continuous (T <sub>C</sub> =100°C)	5	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	34	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	14	mJ
IAS	Single Pulse Avalanched Current <sup>2</sup>	17	A
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> =25°C)	3.7	W
	Power Dissipation – Derate above 25°C	0.029	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	60	°C/W



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### Electrical Characteristics (T<sub>J</sub>=25°C , unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	40	---	---	V
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =40V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =32V , V <sub>GS</sub> =0V , T <sub>J</sub> =125°C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V , V <sub>DS</sub> =0V	---	---	±100	nA

#### On Characteristics

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>3</sup>	V <sub>GS</sub> =10V , I <sub>D</sub> =3A	---	22.6	28	mΩ
		V <sub>GS</sub> =4.5V , I <sub>D</sub> =2A	---	29	38	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.5	2.5	V
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V , I <sub>D</sub> =1A	---	3	---	S

#### Dynamic and switching Characteristics

Q <sub>g</sub>	Total Gate Charge <sup>3, 4</sup>	V <sub>DS</sub> =20V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =5A	---	4.5	10	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>3, 4</sup>		---	1.5	2.4	
Q <sub>gd</sub>	Gate-Drain Charge <sup>3, 4</sup>		---	1.9	5	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>3, 4</sup>	V <sub>DD</sub> =20V , V <sub>GS</sub> =4.5V , R <sub>G</sub> =25Ω I <sub>D</sub> =1A	---	3.2	6	ns
T <sub>r</sub>	Rise Time <sup>3, 4</sup>		---	8.6	16	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>3, 4</sup>		---	18	36	
T <sub>f</sub>	Fall Time <sup>3, 4</sup>		---	6	12	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V , V <sub>GS</sub> =0V , F=1MHz	---	420	800	pF
C <sub>oss</sub>	Output Capacitance		---	65	120	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	40	80	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V , V <sub>DS</sub> =0V , F=1MHz	---	1.2	2.4	Ω

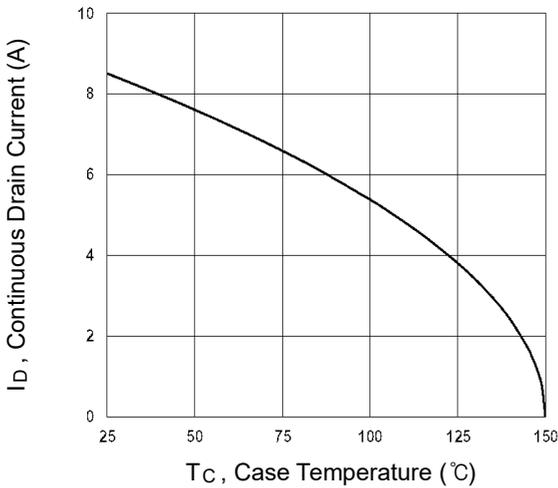
#### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V , Force Current	---	---	8.5	A
I <sub>SM</sub>	Pulsed Source Current <sup>3</sup>		---	---	17	A
V <sub>SD</sub>	Diode Forward Voltage <sup>3</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A , T <sub>J</sub> =25°C	---	---	1	V

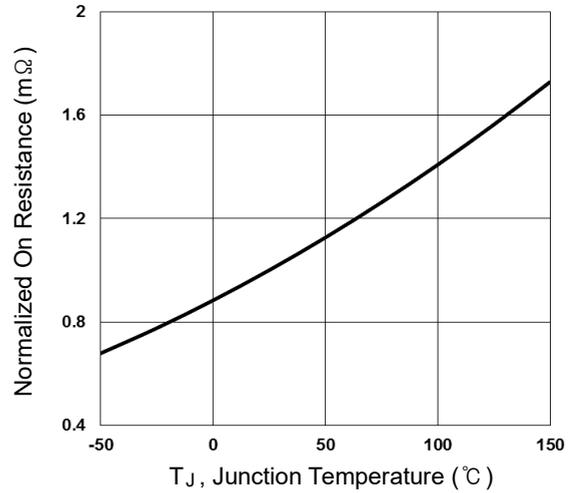
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=17A., R<sub>G</sub>=25Ω, Starting T<sub>J</sub>=25°C.
3. The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

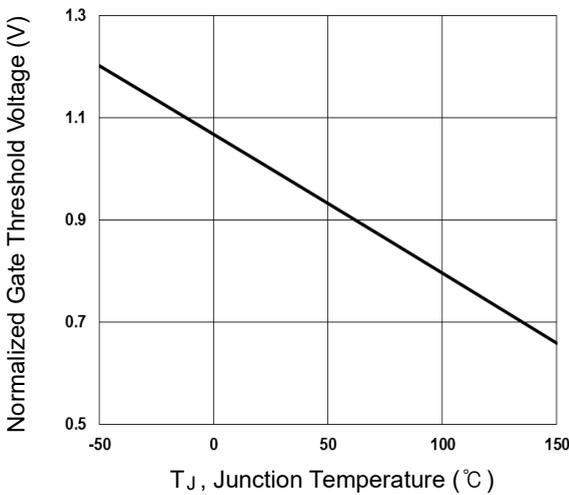
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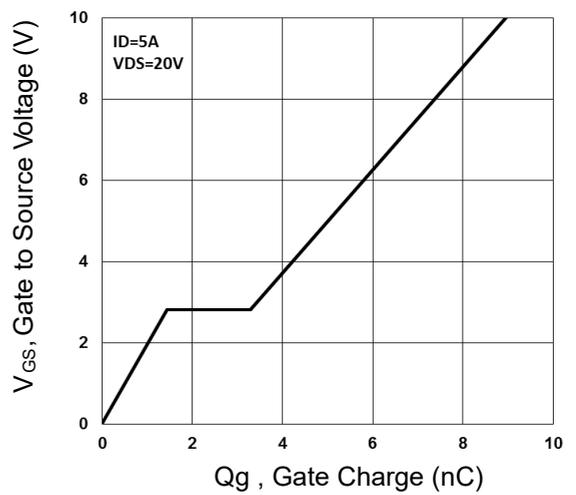
**Fig.1 Continuous Drain Current vs. Tc**



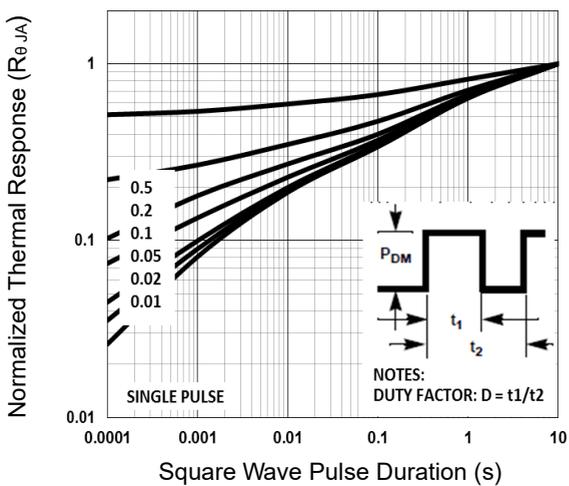
**Fig.2 Normalized RDS(on) vs. TJ**



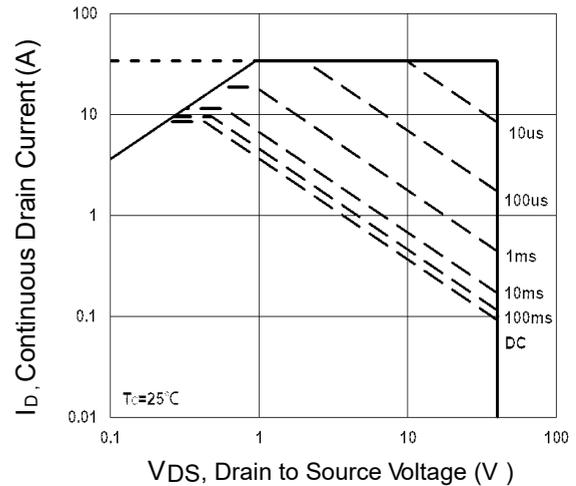
**Fig.3 Normalized Vth vs. TJ**



**Fig.4 Gate Charge Waveform**



**Fig.5 Normalized Transient Response**



**Fig.6 Maximum Safe Operation Area**

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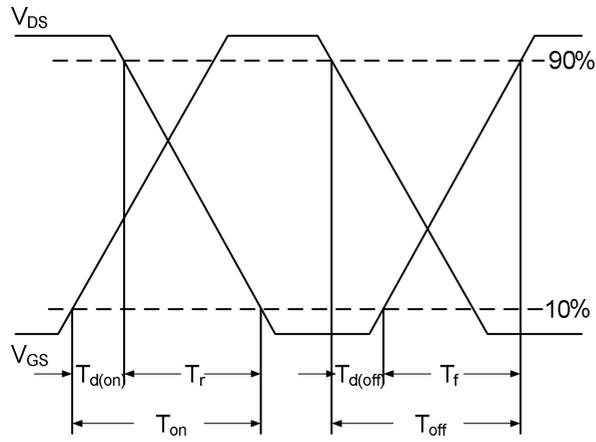


Fig.7 Switching Time Waveform

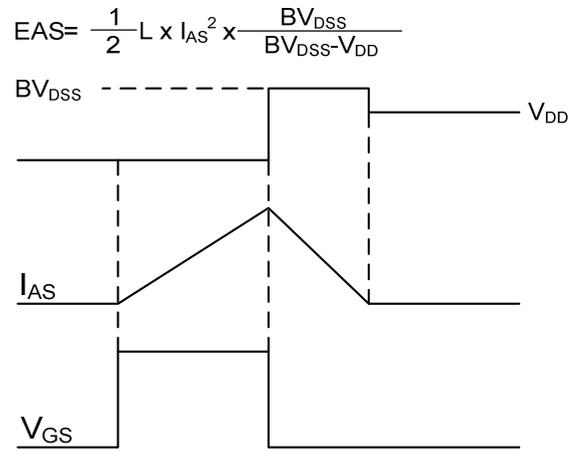
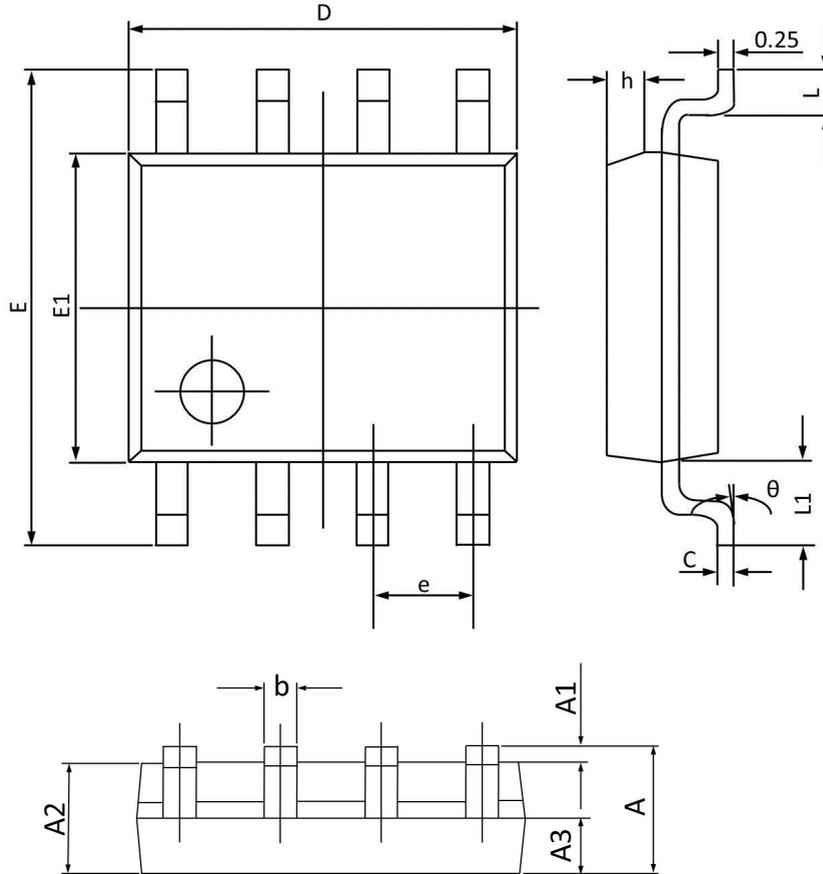


Fig.8 EAS Waveform

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SOP8 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.250	1.650	0.049	0.065
A3	0.500	0.700	0.020	0.028
b	0.380	0.510	0.015	0.020
c	0.170	0.260	0.007	0.010
D	4.700	5.100	0.185	0.201
E	5.800	6.200	0.228	0.244
E1	3.700	4.100	0.146	0.161
e	1.270(BSC)		0.050(BSC)	
h	0.250	0.500	0.010	0.020
L	0.400	0.800	0.016	0.031
L1	1.050(BSC)		0.041(BSC)	
theta	0°	8°	0°	8°