

30V P-Channel MOSFETs

General Description

These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

| BVDSS | RDS(ON) | ID |
|-------|---------|------|
| -30V | 8.0mΩ | -55A |

Features

- -30V,-55A, RDS(ON) =8.0mΩ@VGS = -10V
- Fast switching
- Green Device Available
- Suit for -4.5V Gate Drive Applications

PDFN3x3 Pin Configuration**Applications**

- MB / VGA / Vcore
- POL Applications
- Load Switch
- LED Application

Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

| Symbol | Parameter | Rating | Units |
|-----------|--|------------|-------|
| V_{DS} | Drain-Source Voltage | -30 | V |
| V_{GS} | Gate-Source Voltage | ± 20 | V |
| I_D | Drain Current – Continuous ($T_c=25^\circ\text{C}$) | -55 | A |
| | Drain Current – Continuous ($T_c=100^\circ\text{C}$) | -34 | A |
| I_{DM} | Drain Current – Pulsed ¹ | -220 | A |
| P_D | Power Dissipation ($T_c=25^\circ\text{C}$) | 59 | W |
| | Power Dissipation – Derate above 25°C | 0.47 | W/°C |
| T_{STG} | Storage Temperature Range | -55 to 150 | °C |
| T_J | Operating Junction Temperature Range | -55 to 150 | °C |

Thermal Characteristics

| Symbol | Parameter | Typ. | Max. | Unit |
|-----------------|--|------|------|------|
| $R_{\theta JA}$ | Thermal Resistance Junction to ambient | --- | 62 | °C/W |
| $R_{\theta JC}$ | Thermal Resistance Junction to Case | --- | 2.1 | °C/W |



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Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Off Characteristics

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|------------------------------|------------------------------------|---|------|-------|-----------|---------------------|
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS}=0\text{V}$, $I_D=-250\mu\text{A}$ | -30 | --- | --- | V |
| $\Delta BV_{DSS}/\Delta T_J$ | BV_{DSS} Temperature Coefficient | Reference to 25°C , $I_D=-1\text{mA}$ | --- | -0.03 | --- | V/ $^\circ\text{C}$ |
| I_{DSS} | Drain-Source Leakage Current | $V_{DS}=-30\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$ | --- | --- | -1 | μA |
| | | $V_{DS}=-24\text{V}$, $V_{GS}=0\text{V}$, $T_J=100^\circ\text{C}$ | --- | --- | -10 | μA |
| I_{GSS} | Gate-Source Leakage Current | $V_{GS}=\pm 20\text{V}$, $V_{DS}=0\text{V}$ | --- | --- | ± 100 | nA |

On Characteristics

| | | | | | | |
|---------------------|-----------------------------------|---|------|------|------|----------------------------|
| $R_{DS(ON)}$ | Static Drain-Source On-Resistance | $V_{GS}=-10\text{V}$, $I_D=-10\text{A}$ | --- | 8.0 | 9.0 | $\text{m}\Omega$ |
| | | $V_{GS}=-4.5\text{V}$, $I_D=-8\text{A}$ | --- | 10 | 12.5 | $\text{m}\Omega$ |
| $V_{GS(\text{th})}$ | Gate Threshold Voltage | $V_{GS}=V_{DS}$, $I_D = -250\mu\text{A}$ | -1.2 | -1.4 | -2.5 | V |
| | | | --- | 4 | --- | $\text{mV}/^\circ\text{C}$ |
| g_{fs} | Forward Transconductance | $V_{DS}=-10\text{V}$, $I_D=-8\text{A}$ | --- | 14 | --- | S |

Dynamic and switching Characteristics

| | | | | | | |
|--------------|-------------------------------------|---|-----|-------|------|----------|
| Q_g | Total Gate Charge ^{2, 3} | $V_{DS}=-15\text{V}$, $V_{GS}=-4.5\text{V}$, $I_D=-10\text{A}$ | --- | 35 | 56 | nC |
| Q_{gs} | Gate-Source Charge ^{2, 3} | | --- | 10.8 | 16 | |
| Q_{gd} | Gate-Drain Charge ^{2, 3} | | --- | 10.6 | 16 | |
| $T_{d(on)}$ | Turn-On Delay Time ^{2, 3} | $V_{DD}=-15\text{V}$, $V_{GS}=-10\text{V}$, $R_G=6\Omega$ $I_D=-1\text{A}$ | --- | 24.5 | 38 | ns |
| T_r | Rise Time ^{2, 3} | | --- | 10.5 | 16 | |
| $T_{d(off)}$ | Turn-Off Delay Time ^{2, 3} | | --- | 156.8 | 230 | |
| T_f | Fall Time ^{2, 3} | | --- | 50 | 75 | |
| C_{iss} | Input Capacitance | $V_{DS}=-15\text{V}$, $V_{GS}=0\text{V}$, $F=1\text{MHz}$ | --- | 3300 | 4800 | pF |
| C_{oss} | Output Capacitance | | --- | 410 | 700 | |
| C_{rss} | Reverse Transfer Capacitance | | --- | 280 | 500 | |
| R_g | Gate resistance | $V_{GS}=0\text{V}$, $V_{DS}=0\text{V}$, $F=1\text{MHz}$ | --- | 8.5 | 12 | Ω |

Drain-Source Diode Characteristics and Maximum Ratings

| Symbol | Parameter | Conditions | Min. | Typ. | Max. | Unit |
|----------|---------------------------|--|------|------|------|------|
| I_s | Continuous Source Current | $V_G=V_D=0\text{V}$, Force Current | --- | --- | -55 | A |
| I_{sM} | Pulsed Source Current | | --- | --- | -110 | A |
| V_{SD} | Diode Forward Voltage | $V_{GS}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$ | --- | --- | -1 | V |

Note :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

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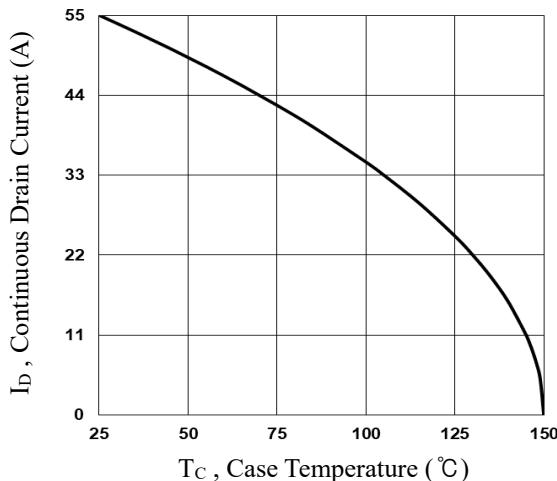


Fig.1 Continuous Drain Current vs. T_c

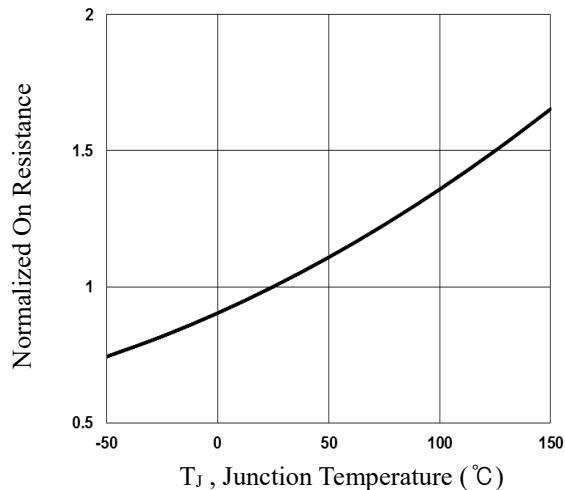


Fig.2 Normalized $R_{DS(on)}$ vs. T_j

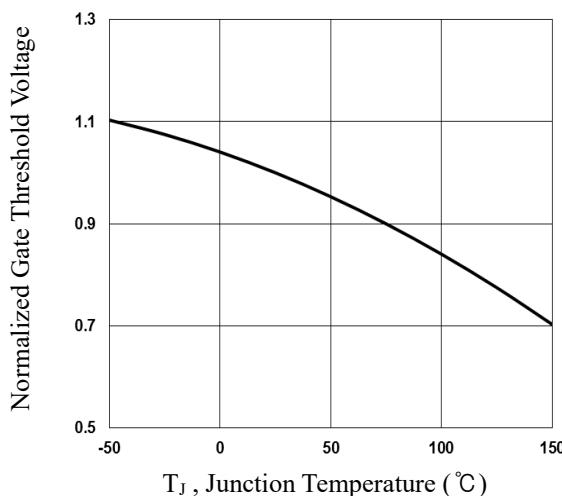


Fig.3 Normalized V_{th} vs. T_j

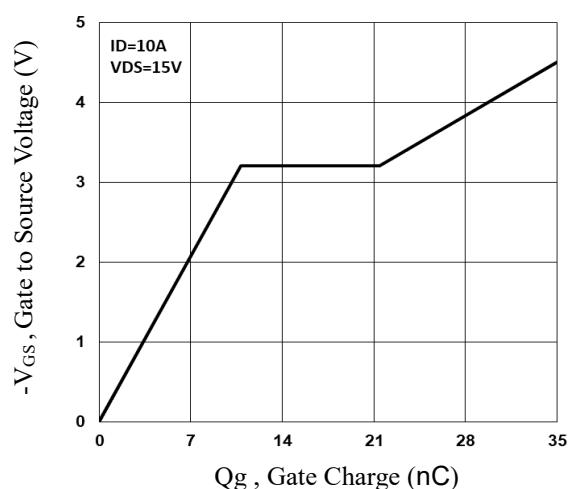


Fig.4 Gate Charge Waveform

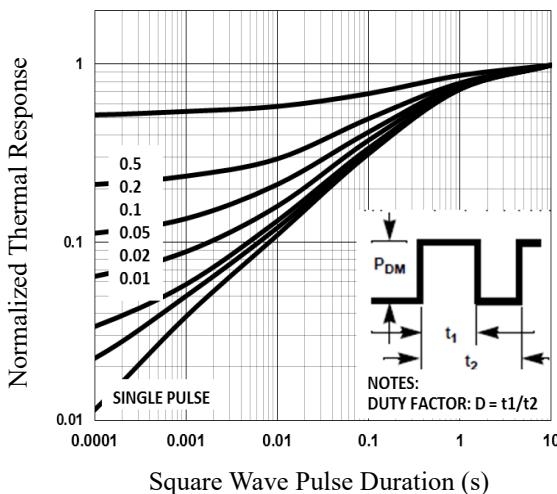


Fig.5 Normalized Transient Impedance

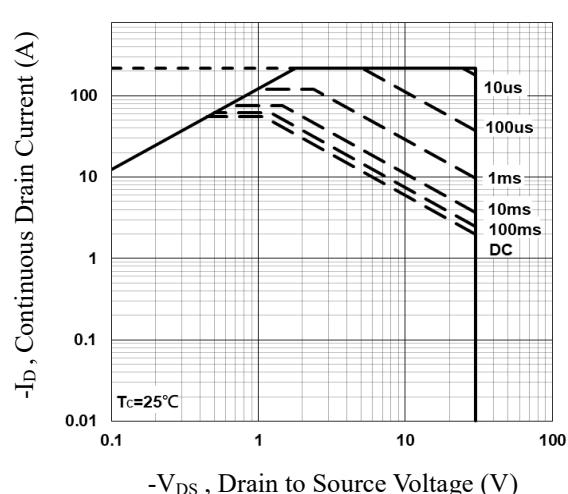


Fig.6 Maximum Safe Operation Area

30V P-Channel MOSFETs

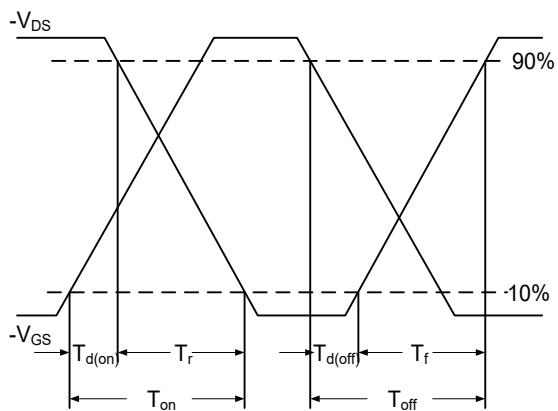


Fig.7 Switching Time Waveform

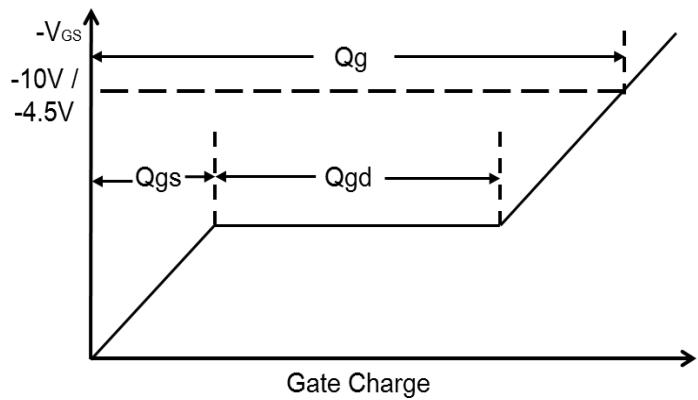
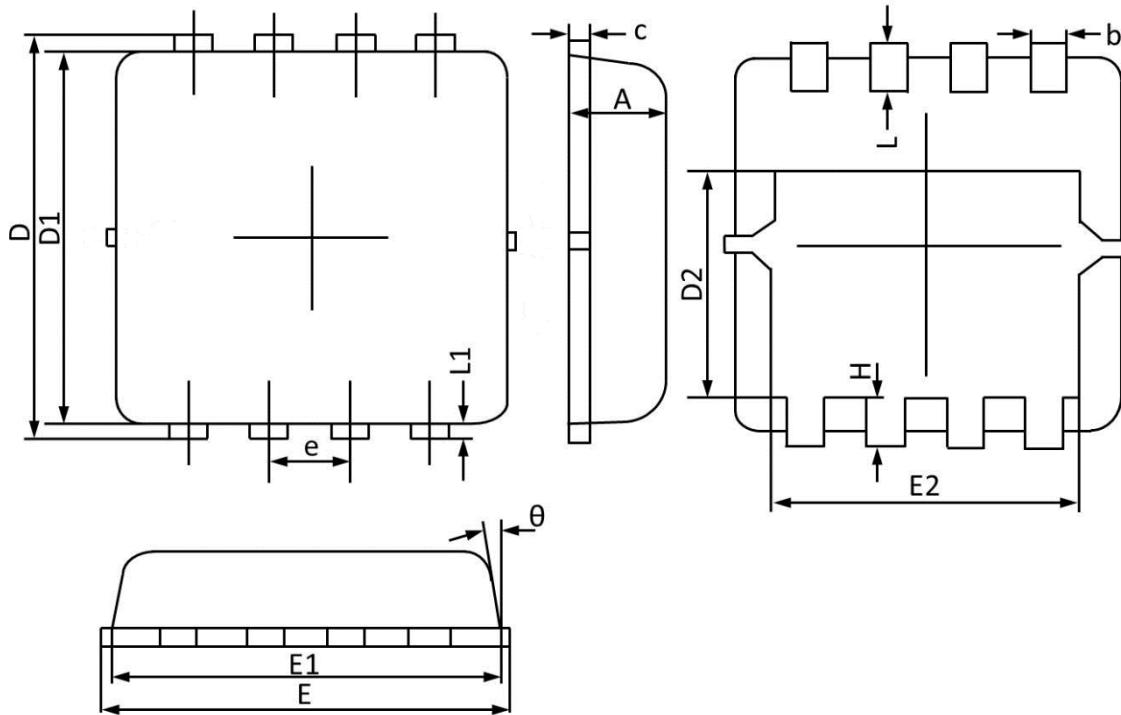


Fig.8 Gate Charge Waveform

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PDFN3x3 PACKAGE INFORMATION



| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | MAX | MIN | MAX | MIN |
| A | 0.900 | 0.700 | 0.035 | 0.028 |
| b | 0.350 | 0.250 | 0.014 | 0.010 |
| c | 0.250 | 0.100 | 0.010 | 0.004 |
| D | 3.500 | 3.050 | 0.138 | 0.120 |
| D1 | 3.200 | 2.900 | 0.126 | 0.114 |
| D2 | 1.950 | 1.350 | 0.077 | 0.053 |
| E | 3.400 | 3.000 | 0.134 | 0.118 |
| E1 | 3.300 | 2.900 | 0.130 | 0.114 |
| E2 | 2.600 | 2.350 | 0.102 | 0.093 |
| e | 0.65BSC | | 0.026BSC | |
| H | 0.750 | 0.300 | 0.030 | 0.012 |
| L | 0.600 | 0.300 | 0.024 | 0.012 |
| L1 | 0.200 | 0.060 | 0.008 | 0.002 |
| θ | 14° | 6° | 14° | 6° |