

**42mΩ, 650V, Super Junction N-Channel Power MOSFET**

**General Description**

The Sanrise FRC65R042BT4 is a high voltage power MOSFET, fabricated using advanced super junction technology. The resulting device has extremely low on resistance, low gate charge and fast switching time, making it especially suitable for applications which require superior power density and outstanding efficiency. The FRC65R042BT4 break down voltage is 650V and it has a high rugged avalanche characteristics. The FRC65R042BT4 is available in TO-247-4 package.

**Symbol**

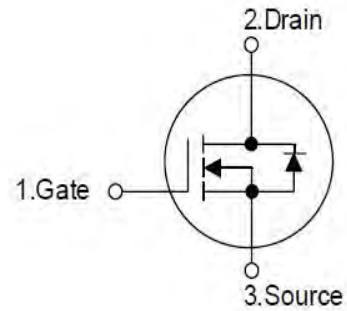


Figure 1 Symbol of FRC65R042B

**Features**

- Ultra Low  $R_{DS(ON)} = 42m\Omega @ V_{GS} = 10V$ .
- Ultra Low Gate Charge,  $Q_g=182nC$  typ.
- Intrinsic Fast-Recovery Body Diode
- Fast switching capability
- Robust design with better EAS performance
- Non-automotive Qualified

**Package Type**

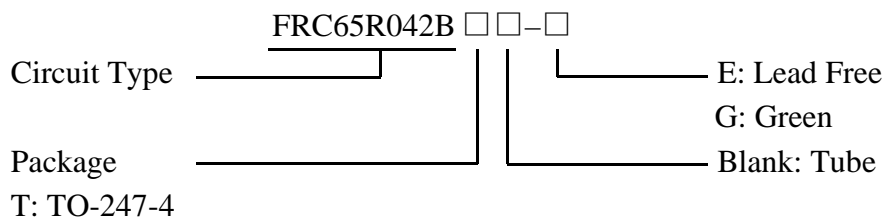


Figure 2 Package Type of FRC65R042B

**Application**

- AC/DC Power Supply
- EV Charger
- Sever / Telecom
- Solar Inverter

**Ordering Information**



Package	Part Number		Marking ID		Packing Type
	Lead Free	Green	Lead Free	Green	
TO-247-4	FRC65R042BT4-E	FRC65R042BT4-G	SRC65R042BT4E	SRC65R042BT4G	Tube



# FRC65R042BT4

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### Absolute Maximum Ratings<sup>Note 1</sup>

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V <sub>DSS</sub>	650	V
Gate-Source Voltage (static)		V <sub>GSS</sub>	±20	V
Gate-Source Voltage (dynamic), AC (f>1Hz)		V <sub>GSS</sub>	±30	V
Power Dissipation(T <sub>C</sub> =25°C,TO-247-4)		P <sub>tot</sub>	403.2	W
Continuous Drain Current	T <sub>C</sub> =25°C	I <sub>D</sub>	78	A
	T <sub>C</sub> =100°C		49.2	
	T <sub>C</sub> =125°C		34.8	
Pulsed Drain Current (Note 2)		I <sub>DM</sub>	234	A
Avalanche Energy, Single Pulse (Note 3)		E <sub>AS</sub>	400	mJ
Avalanche Energy, Repetitive (Note 2)		E <sub>AR</sub>	0.9	mJ
Avalanche Current, Repetitive (Note 2)		I <sub>AR</sub>	4.5	A
Continuous Diode Forward Current		I <sub>S</sub>	78	A
Diode Pulse Current		I <sub>S.PULSE</sub>	234	A
MOSFET dv/dt Ruggedness, V <sub>DS</sub> ≤480V		dv/dt	50	V/ns
Reverse Diode dv/dt, V <sub>DS</sub> ≤480V, I <sub>SD</sub> ≤I <sub>D</sub>		dv/dt	50	V/ns
Operating Junction Temperature		T <sub>J</sub>	150	°C
Storage Temperature		T <sub>STG</sub>	-55 to 150	°C
Lead Temperature (Soldering, 10 sec)		T <sub>LEAD</sub>	260	°C

Note:

- Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.
- Repetitive Rating: Pulse width limited by maximum junction temperature
- I<sub>AS</sub> = 4.5A, V<sub>DD</sub> = 40V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C

### Thermal characteristics

Parameter		Symbol	Min	Typ	Max	Unit
Thermal resistance, Junction-to-Case	TO-247-4	R <sub>thJC</sub>			0.31	°C /W
Thermal resistance, Junction-to-Ambient	TO-247-4	R <sub>thJA</sub>			62	°C /W



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### Electrical Characteristics

T<sub>J</sub> = 25°C, unless otherwise specified.

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
<b>Statistic Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	650			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V			10	uA
Gate-Body Leakage Current	Forward	I <sub>GSSF</sub>	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V		100	nA
	Reverse	I <sub>GSSR</sub>	V <sub>GS</sub> =-20V, V <sub>DS</sub> =0V		-100	nA
Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =1.6mA	3.0	4.0	5.0	V
Static Drain-Source On-Resistance	R <sub>Ds(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =30A		35	42	mΩ
Gate Resistance	R <sub>G</sub>	f=1MHz, Open Drain		0.7		Ω
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1MHz		7200		pF
Output Capacitance	C <sub>OSS</sub>			4300		
Reverse Transfer Capacitance	C <sub>RSS</sub>			57.1		
Effective output capacitance, energy related <sup>NOTE5</sup>	C <sub>O(er)</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =0...400V		155.1		pF
Effective output capacitance, time related <sup>NOTE6</sup>	C <sub>O(tr)</sub>			905.9		
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =400V, I <sub>D</sub> =38A R <sub>G</sub> =3.3Ω, V <sub>GS</sub> =10V		17		ns
Rise Time	t <sub>r</sub>			6.6		
Turn-off Delay Time	t <sub>d(off)</sub>			112		
Fall Time	t <sub>f</sub>			4.3		
<b>Gate Charge Characteristics</b>						
Gate to Source Charge	Q <sub>gs</sub>	V <sub>DD</sub> =480V, I <sub>D</sub> =38A V <sub>GS</sub> =0 to 10V		46.3		nC
Gate to Drain Charge	Q <sub>gd</sub>			92.2		
Gate Charge Total	Q <sub>g</sub>			182		
Gate Plateau Voltage	V <sub>plateau</sub>			6.5		V
<b>Reverse Diode Characteristics</b>						
Drain-Source Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>SD</sub> =30A		0.9	1.1	V
Reverse Recovery Time	t <sub>rr</sub>	V <sub>R</sub> =100V, I <sub>F</sub> =38A dI <sub>F</sub> /dt=100A/us		168		ns
Reverse Recovery Charge	Q <sub>rr</sub>			1.17		uC
Peak Reverse Recovery Current	I <sub>rrm</sub>			14.0		A

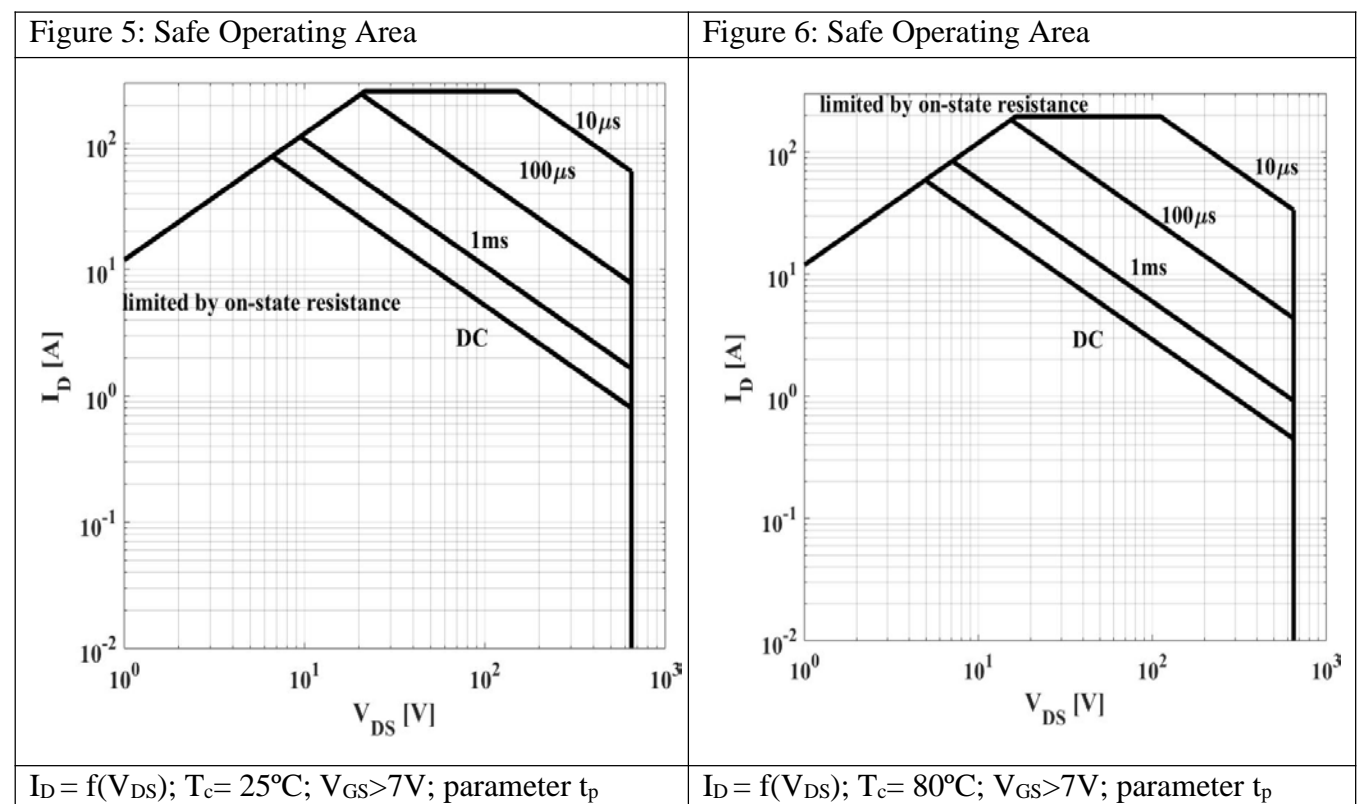
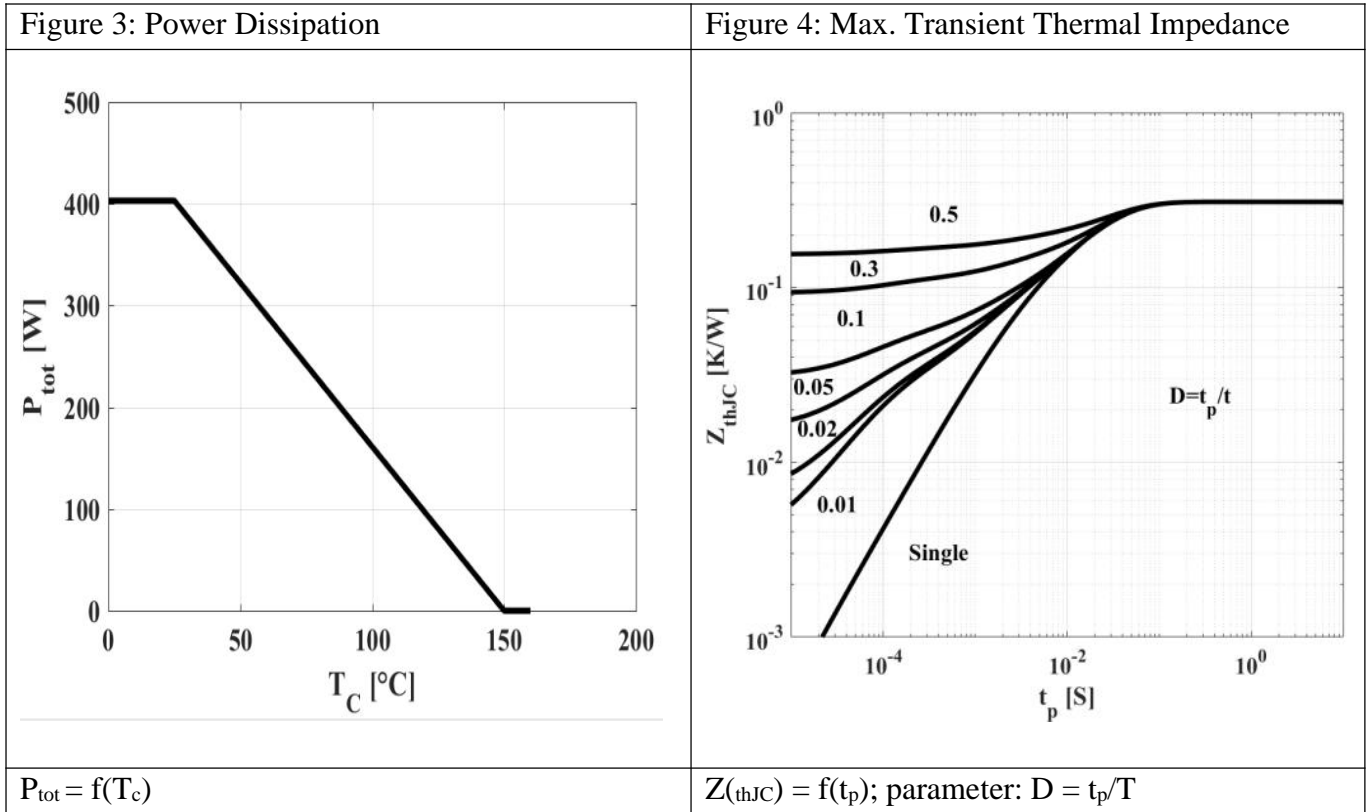
Note:

5. C<sub>O(er)</sub> is a fixed capacitance that gives the same stored energy as C<sub>OSS</sub> while V<sub>DS</sub> is rising from 0 to 480V

6. C<sub>O(tr)</sub> is a fixed capacitance that gives the same charging time as C<sub>OSS</sub> while V<sub>DS</sub> is rising from 0 to 480 V

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### Typical Performance Characteristics

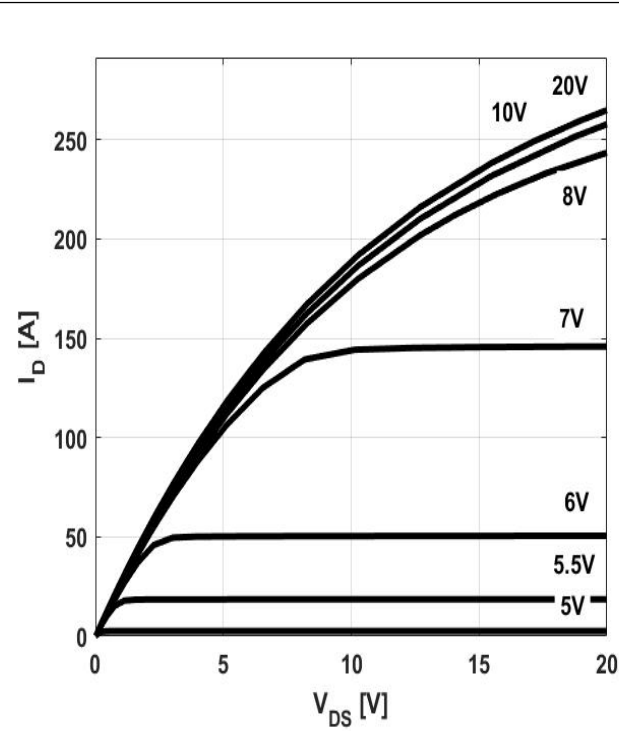




# FRC65R042BT4

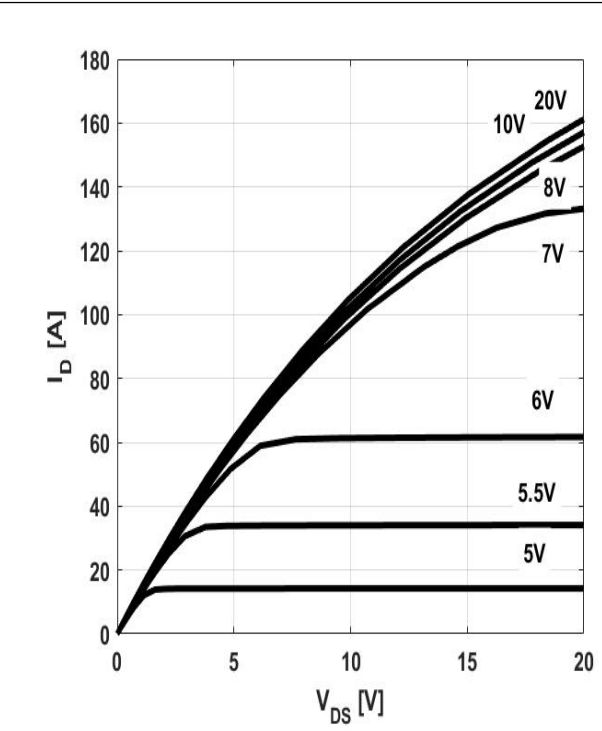
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Figure 7: Typ. Output Characteristics



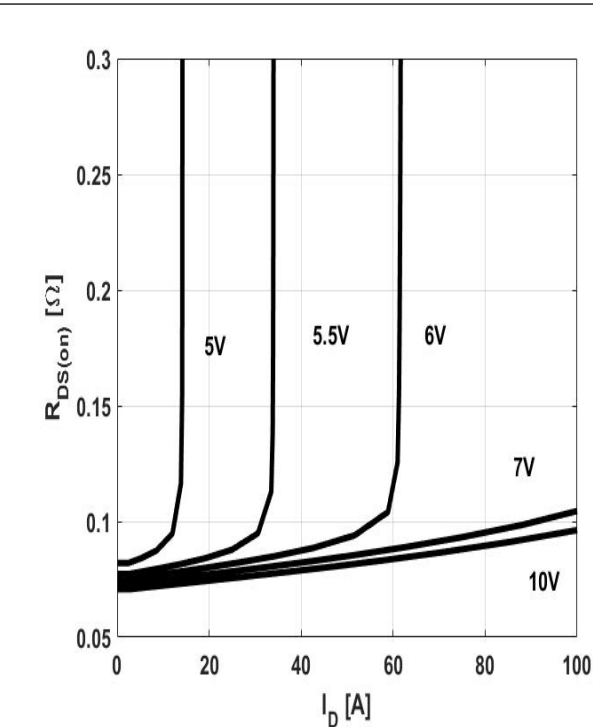
$I_D = f(V_{DS})$ ;  $T_j = 25^\circ\text{C}$ ; parameter:  $V_{GS}$

Figure 8: Typ. Output Characteristics



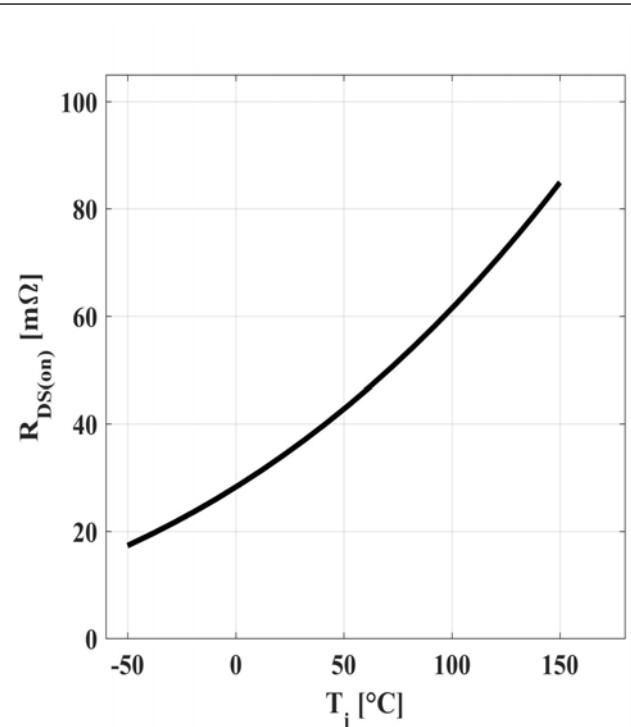
$I_D = f(V_{DS})$ ;  $T_j = 125^\circ\text{C}$ ; parameter:  $V_{GS}$

Figure 9: Typ. Drain-Source On-State Resistance



$R_{DS(ON)} = f(I_D)$ ;  $T_j = 125^\circ\text{C}$ ; parameter:  $V_{GS}$

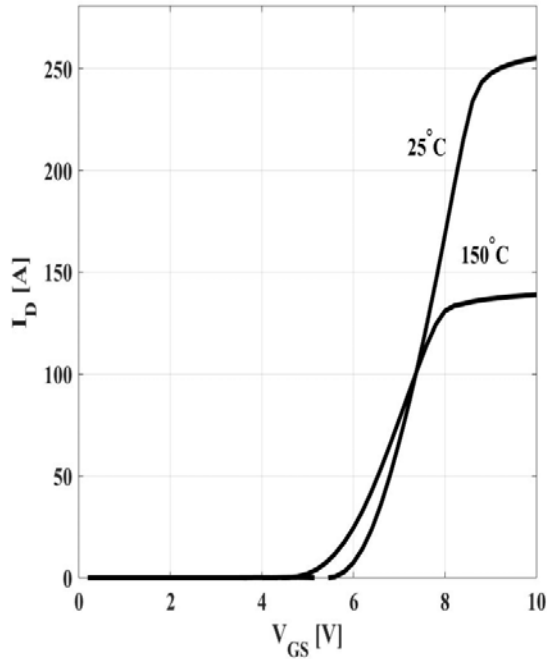
Figure 10: Typ. Drain-Source On-State Resistance



$R_{DS(ON)} = f(T_j)$ ;  $I_D = 30\text{A}$ ;  $V_{GS} = 10\text{V}$

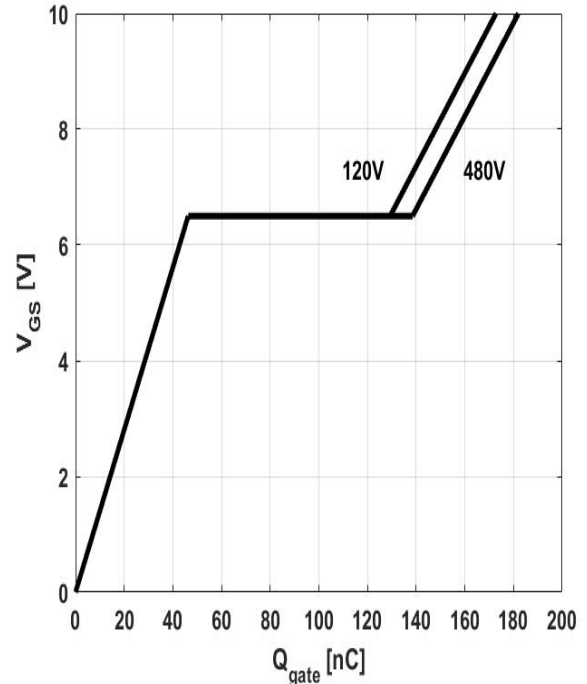
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Figure 11: Typ. Transfer Characteristics



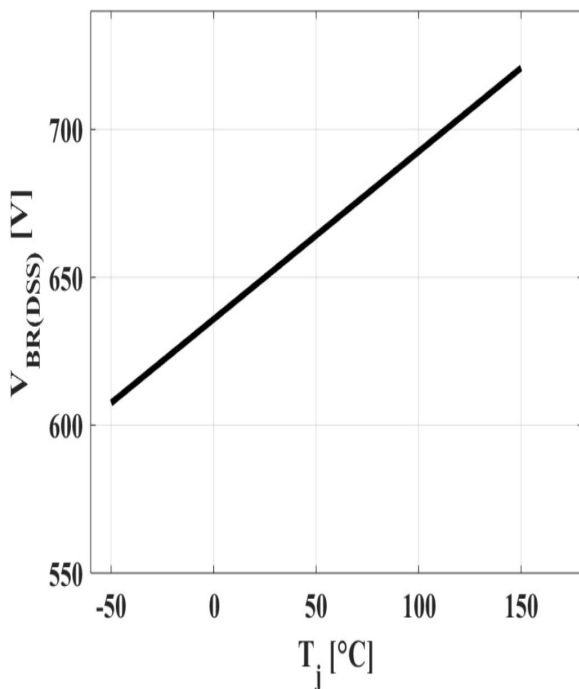
$$I_D = f(V_{GS}); V_{DS} = 20V$$

Figure 12: Typ. Gate Charge



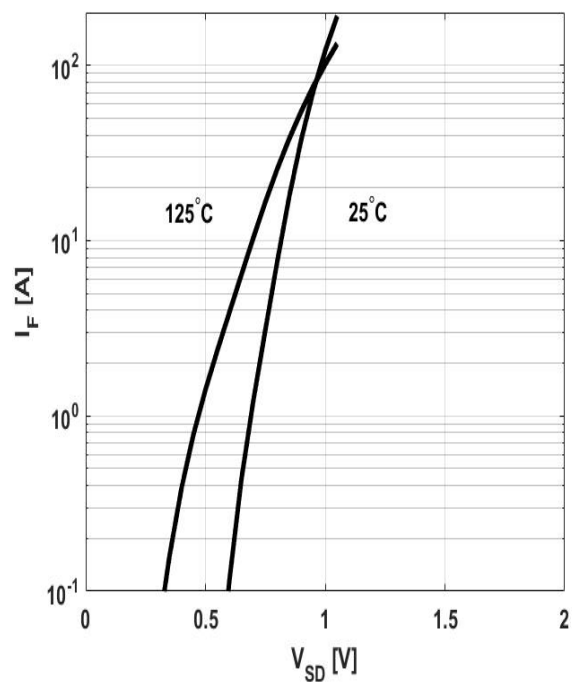
$$V_{GS} = f(Q_{gate}), I_D = 30A \text{ pulsed}$$

Figure 13: Drain-Source Breakdown Voltage



$$V_{BR(DSS)} = f(T_j); I_D = 10mA$$

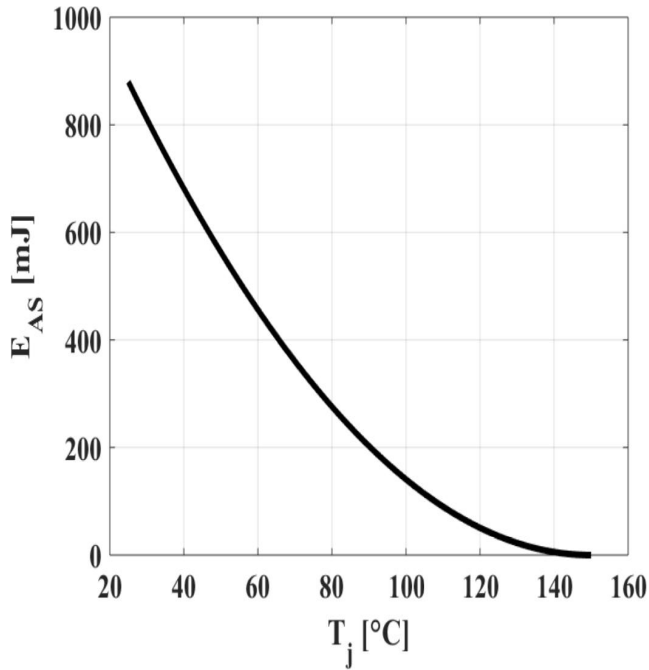
Figure 14: Forward Characteristics of Reverse Diode



$$I_F = f(V_{SD}); \text{parameter: } T_j$$

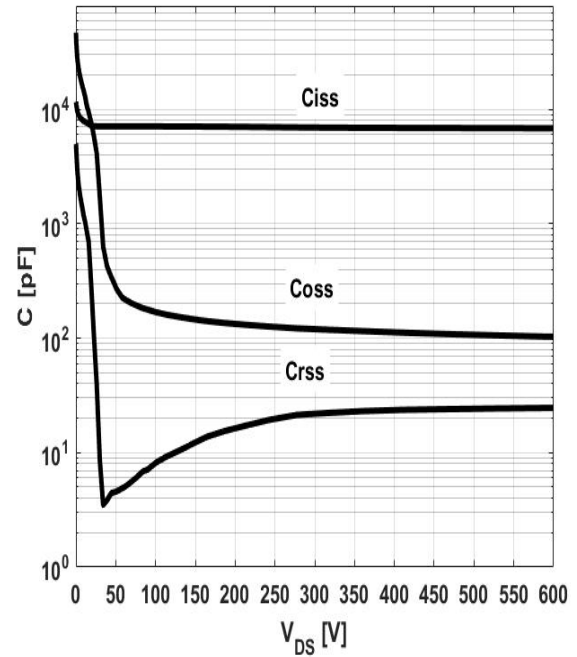
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Figure 15: Avalanche Energy



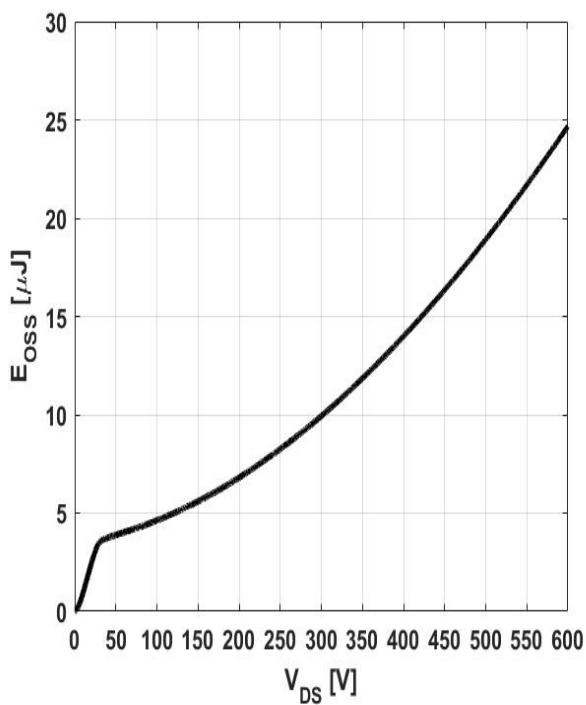
$E_{AS}=f(T_j)$ ;  $I_D=6.0A$ ;  $V_{DD}=60V$

Figure 16: Typ. Capacitances



$C=f(V_{DS})$ ;  $V_{GS}=0$ ;  $f=1MHz$

Figure 17:  $C_{oss}$  Stored Energy

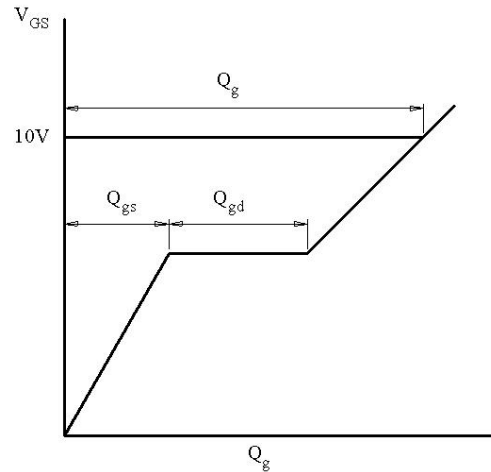
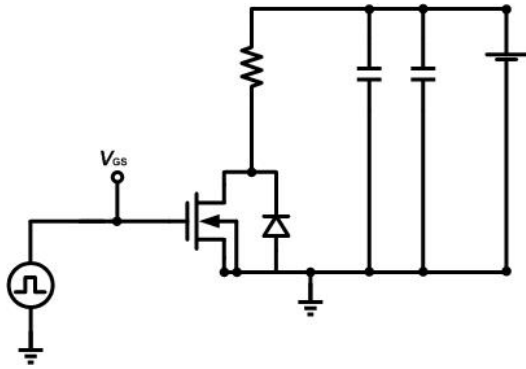


$E_{OSS}=f(V_{DS})$

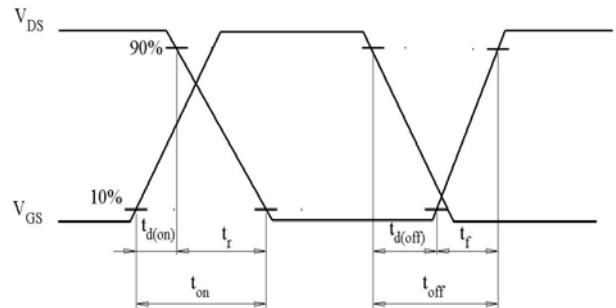
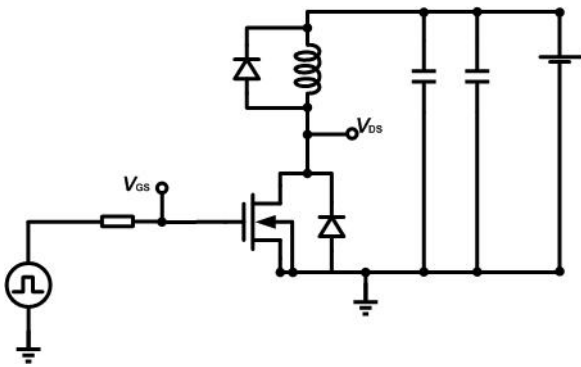
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### Test Circuits

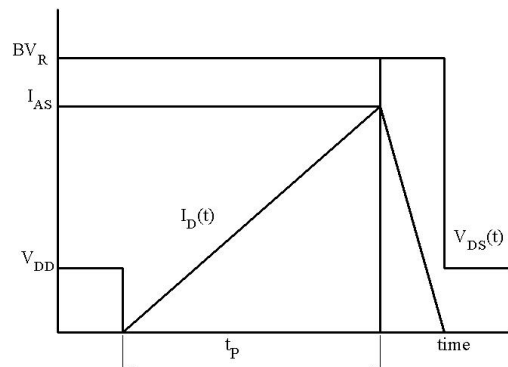
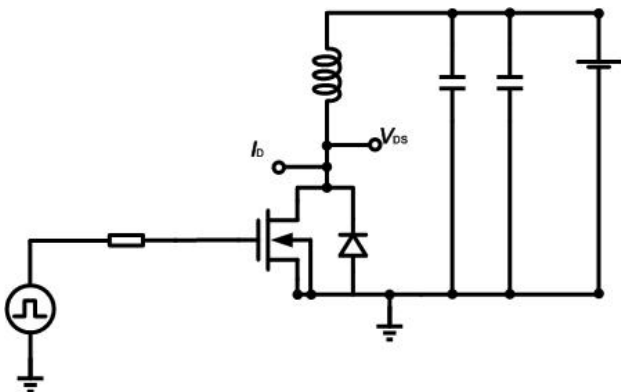
#### 1. Gate Charge Test Circuit & Waveform



#### 2. Switch Time Test Circuit



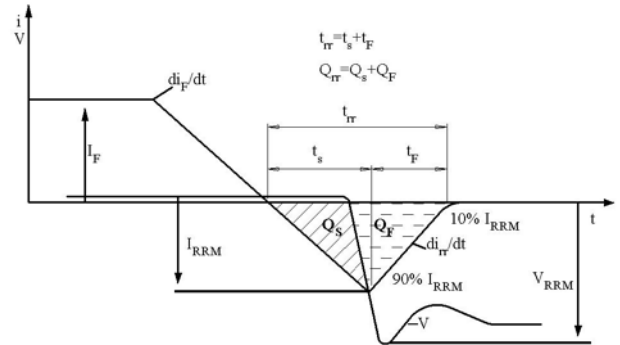
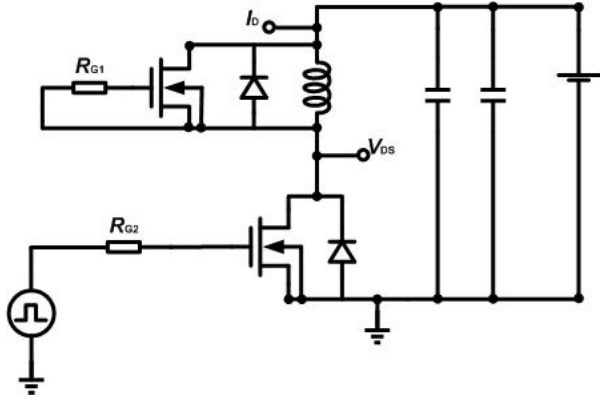
#### 3. Unclamped Inductive Switching Test Circuit & Waveforms





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### 4. Test Circuit and Waveform for Diode Characteristics

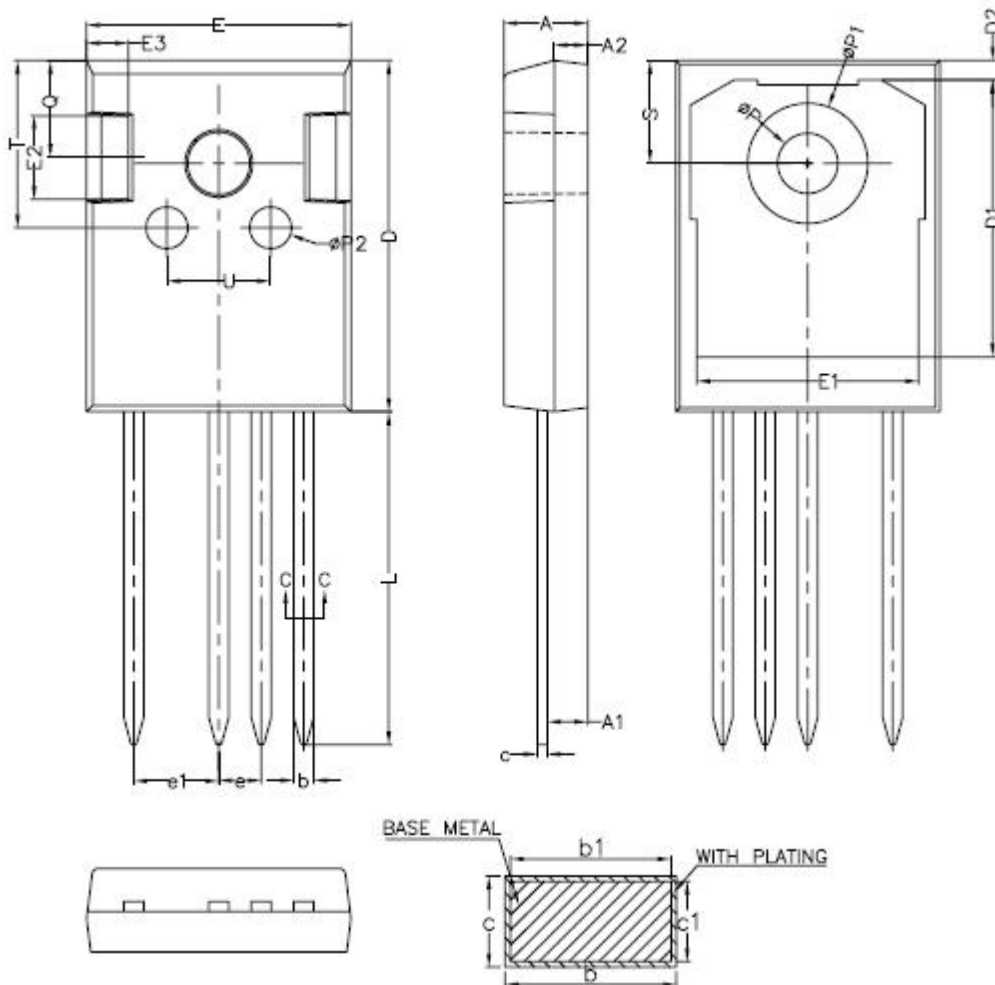


## 42mΩ, 650V, Super Junction N-Channel Power MOSFET

### Mechanical Dimensions

TO-247-4

Unit: mm



Symbol	Dimensions(mm)			Symbol	Dimensions(mm)		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	4.90	5.00	5.10	E2	4.90	5.00	5.10
A1	2.31	2.41	2.51	E3	2.40	2.50	2.60
A2	1.90	2.00	2.10	e	2.44	2.54	2.64
b	1.16	-	1.29	e1	4.98	5.08	5.18
b1	1.15	1.20	1.25	L	19.80	19.92	20.10
c	0.59	-	0.66	P	3.50	3.60	3.70
c1	0.58	0.60	0.62	P1	-	-	7.40
D	20.90	21.00	21.10	P2	2.40	2.50	2.60
D1	16.25	16.55	16.85	Q	5.60	-	6.00
D2	1.05	1.20	1.35	S	6.15 BSC		
E	15.70	15.80	15.90	T	9.80	-	10.20
E1	13.10	13.30	13.50	U	6.00	-	6.40