

**GLASS PASSIVATED SINGLE-PHASE BRIDGE RECTIFIER**

Reverse Voltage 200to1000 Volts ForwardCurrent 0.8 Amperes

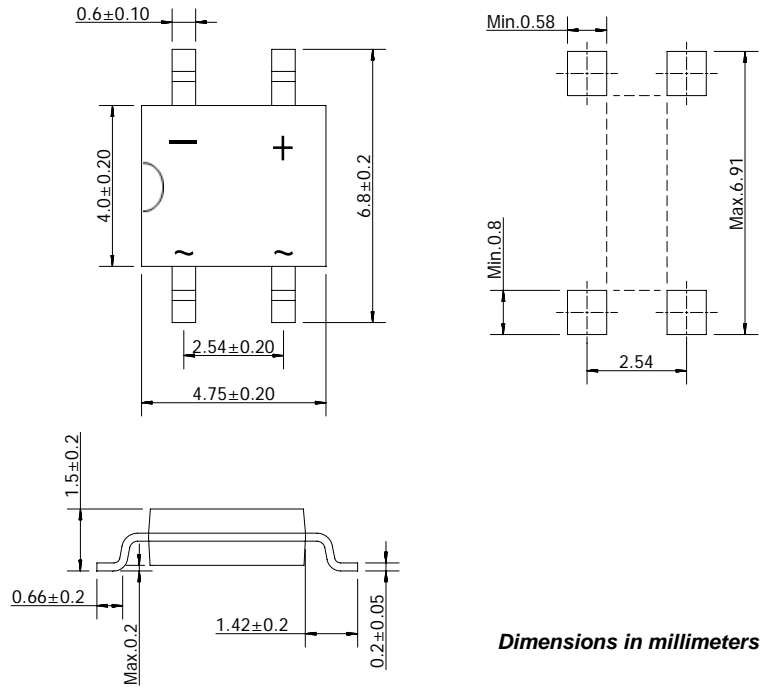
**Features**

- ◆ Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- ◆ High surge current capability
- ◆ Ideal for printed circuit boards
- ◆ Glass passivated chip junction

**Mechanical Data**

**Case:** Molded plastic body over passivated junctions  
**Terminals:** Plated leads solderable per MIL-STD-750, Method 2026  
 High temperature soldering guaranteed:  
 260°C/20 seconds  
**Mounting Position:** Any  
**Weight:** 0.07oz., 0.2g

**Case Style: MBF**



*Dimensions in millimeters*

Absolute Maximum Ratings TL=25 °C unless otherwise specified.							
Parameter	Symbol	MB2F	MB4F	MB6F	MB8F	MB10F	Unit
Maximum repetitive peak reverse voltage	$V_{RRM}$	200	400	600	800	1000	V
Maximum RMS voltage	$V_{RMS}$	140	280	420	560	700	V
Maximum DC blocking voltage	$V_{DC}$	200	400	600	800	1000	V
Maximum average forward output current rectified (see Fig.1)	$I_{F(AV)}$	0.5 <sup>(1)</sup>					A
on glass-epoxy P.C.B.		0.8 <sup>(2)</sup>					
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	30					A
Rating for fusig (t<8.3ms)	$I^2t$	3.7					A <sup>2</sup> sec
Typical junction capacitance per leg at 4.0V 1.0Mhz	CJ	13					pF
Operating junction temperature range	TJ	-55 to +150					°C
Storage temperature range	TSTG	-55 to +150					°C

Electrical Characteristics TL=25 °C unless otherwise specified.				
Maximum instantaneous forward voltage drop per leg at 0.4A	VF	1.00		V
Maximum DC reverse current at rated DC blocking voltage per leg	IR	5		μA
Typical thermal resistance per leg	$R_{\theta JA}$	70 <sup>(2)</sup>		°C/W
	$R_{\theta JA}$	85 <sup>(1)</sup>		
	$R_{\theta JL}$	20 <sup>(1)</sup>		

**Notes:**

- (1) On glass epoxy P.C B mounted on 0.05"x0.05"(1.3x1.3mm) pads
- (2) On aluminum substrate P.C B.with an area of 0.8"x0.8" (20x20mm) mounted on 0.05"x0.05"(1.3x1.3mm)solder pad

## ■ Ratings and Characteristic Curves (TA= 25°C unless otherwise noted)

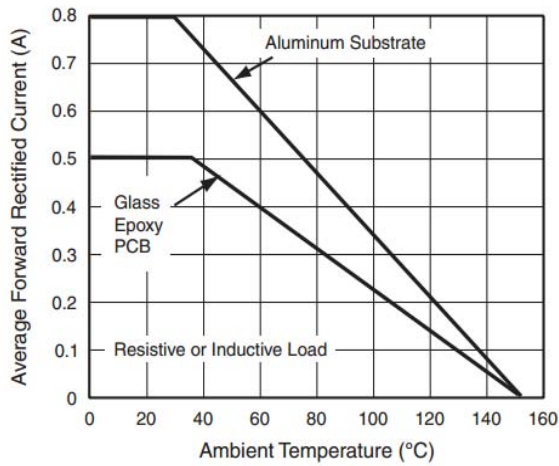


Fig. 1 - Derating Curve for Output Rectified Current

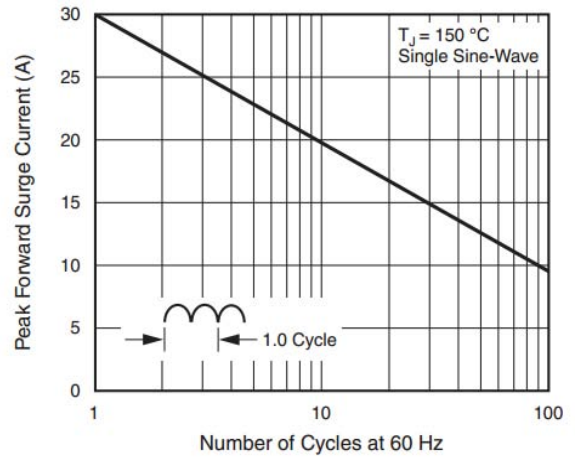


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

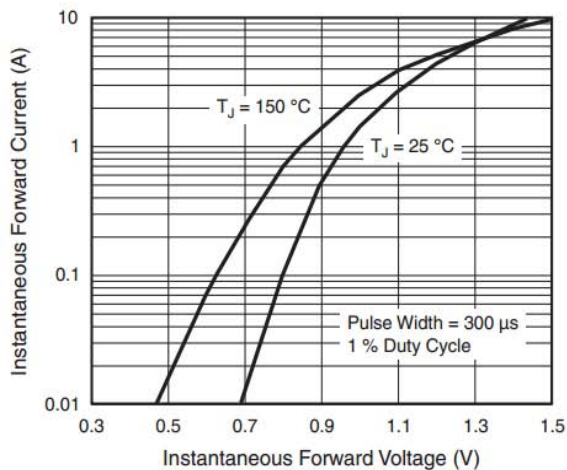


Fig. 3 - Typical Forward Voltage Characteristics Per Diode

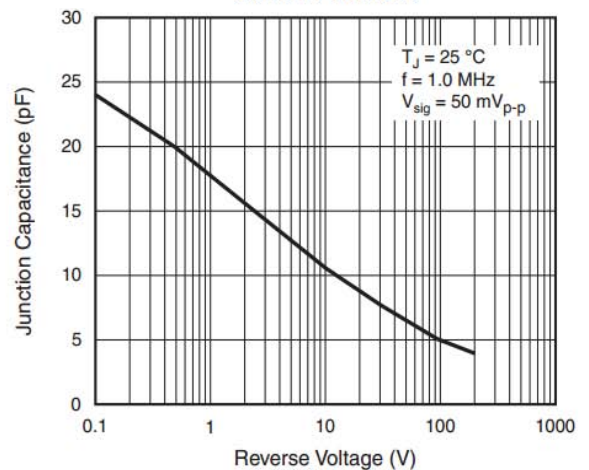


Fig. 5 - Typical Junction Capacitance Per Diode

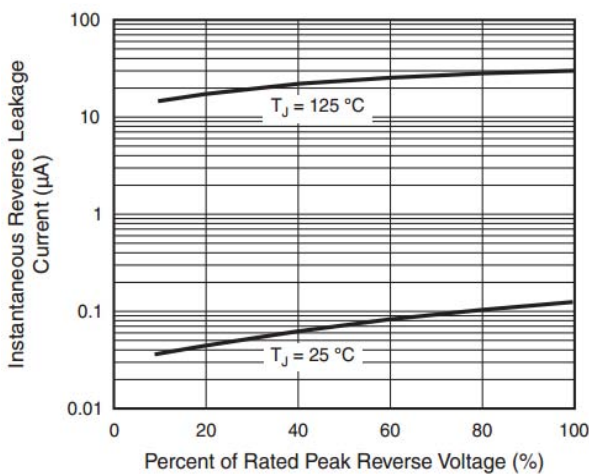


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode