

Transil array for data protection

General Description

The ESDA6V1W6 is a monolithic suppressor designed to protect components connected to data and transmission lines against ESD. The device clamp the voltage just above the logic level supply for positive transients, and to a diode drop below ground for negative transients.

Applications

- Computers
- Printers
- Communication systems
- Cellular phones handsets and accessories
- Wireline and wireless telephone sets
- Set top boxes

Features

- 4 Unidirectional Transil functions
- Breakdown voltage:
- VBR = 6.1 V min. and 25 V min.
- Low leakage current: < 1 mA
- Very small PCB area < 4.2 mm² typically
- High ESD protection level: up to 25 kV
- High integration

Complies with the following standards

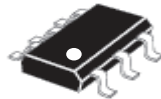
IEC61000-4-2

Level 4 15 kV (air discharge)

9 kV(contact discharge)

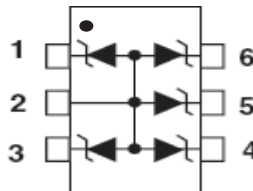
MIL STD 883E - Method 3015-7 Class 3

25 kV HBM (Human Body Model)



SOT-363

Functional diagram



ESDA6V1W6

Absolute Ratings (T_{amb}=25°C)

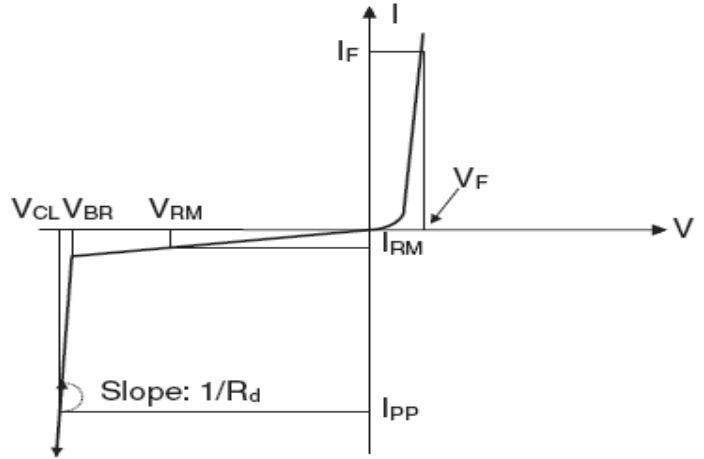
Symbol	Parameter		Value	Units
P _{PP}	Peak Pulse Power (t _p = 8/20μs)	ESDA6V1W6	100	W
T _L	Maximum lead temperature for soldering during 10s		260	°C
T _{stg}	Storage Temperature Range		-40 to +150	°C
T _{op}	Operating Temperature Range		-40 to +150	°C



ESDA6V1W6

Electrical Parameter

Symbol	Parameter
V_{RM}	Stand-off voltage
V_{BR}	Breakdown voltage
V_{CL}	Clamping voltage
I_{RM}	Leakage current
I_{PP}	Peak pulse current
I_R	Reverse current
I_F	Forward current
αT	Voltage temperature coefficient
V_F	Forward voltage drop
C	Capacitance
R_d	Dynamic



Electrical Characteristics

Part Numbers	V_{BR}		I_R	V_{RM}	I_{RM}	V_F	I_F	R_d	αT	C
	Min.	Max.				Max.		Typ. ⁽¹⁾	Max. ⁽²⁾	Typ. 0v bias
	v	v				v		Ω	$10^{-4}/^{\circ}C$	pF
ESDA6V1W6	6.1	7.2	1	3	1	1.25	200	0.61	6	50

1. Square pulse $I_{PP}=15A, t_p=2.5\mu s$ 2. $V_{BR}=\alpha T * (T_{amb}-25^{\circ}C) * V_{BR}(25^{\circ}C)$

Typical Characteristics

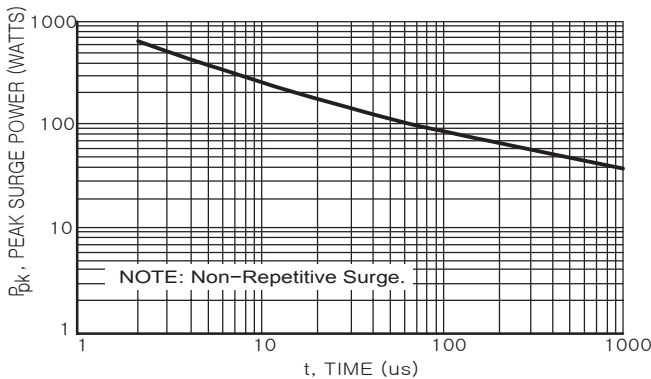


Figure 1. Pulse Width

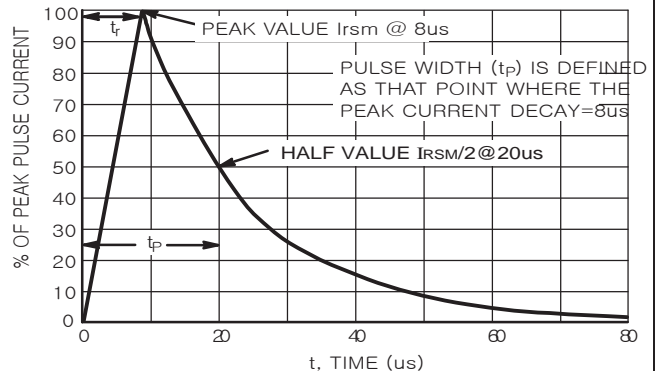


Figure 2. 8 x 20 μs Pulse Waveform

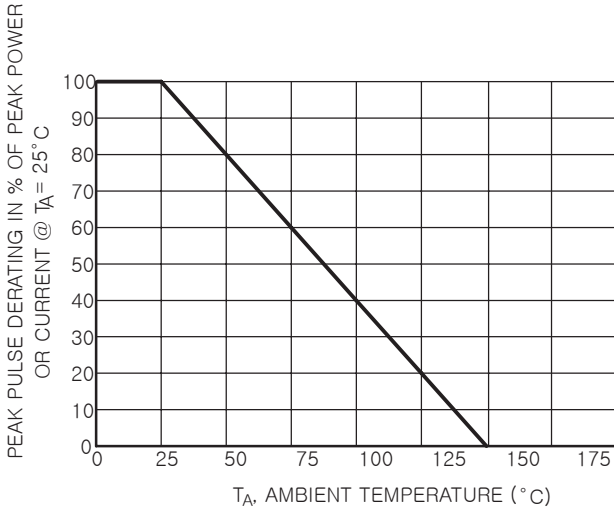


Figure 3. Pulse Derating Curve

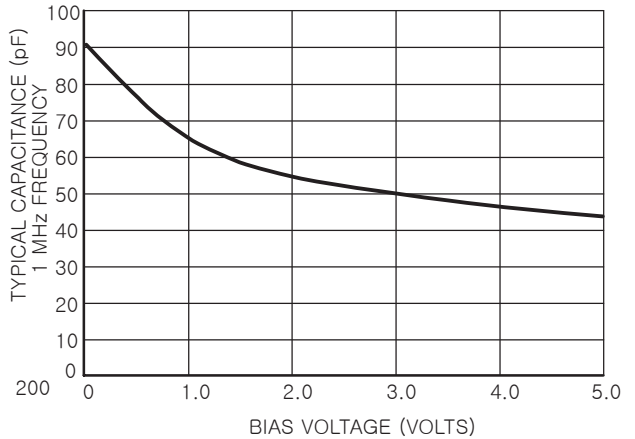


Figure 4. Capacitance

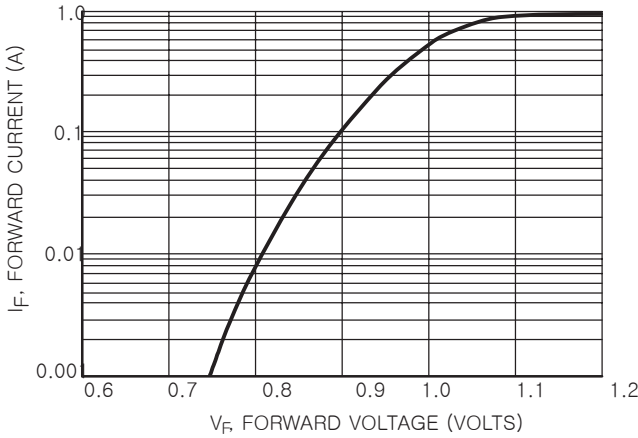


Figure 5. Forward Voltage

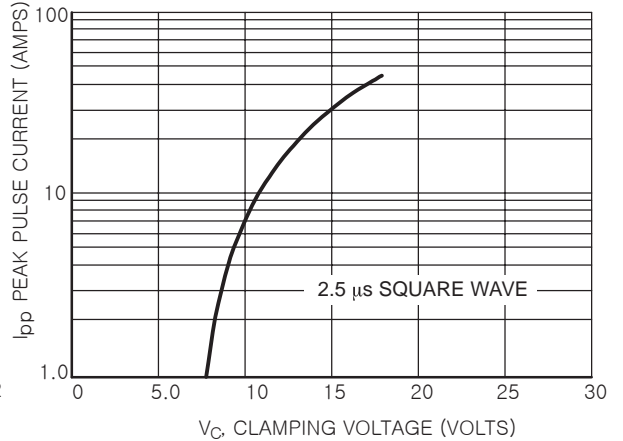


Figure 6. Clamping Voltage versus Peak Pulse Current (Reverse Direction)

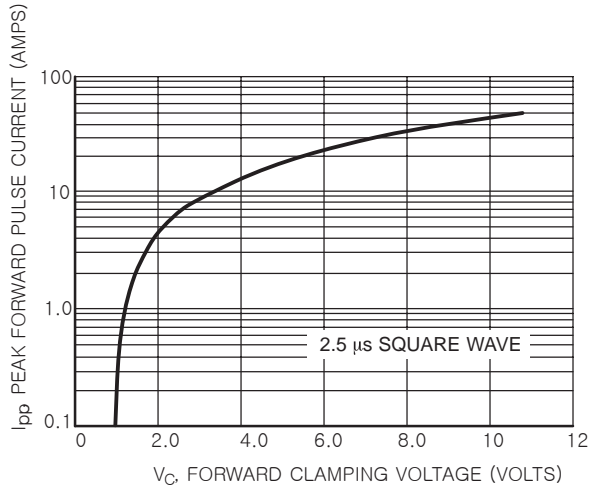


Figure 7. Clamping Voltage versus Peak Pulse Current (Forward Direction)

Package mechanical data

SOT-363 Package

