

# Small Signal MOSFET

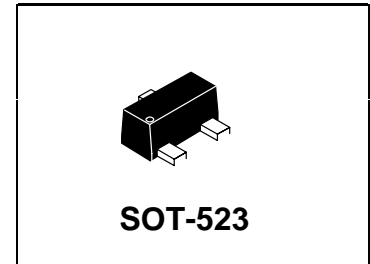
**20 V, 238 mA, Single, N- Channel, Gate ESD Protection**

### Features

- Low Gate Charge for Fast Switching
- Small 1.6 x 1.6 mm Footprint
- ESD Protected Gate
- Pb-Free Package is Available
- ESD Protected:2000V

### Applications

- Power Management Load Switch
- Level Shift
- Portable Applications such as Cell Phones, Media Players, Digital Cameras, PDA's, Video Games, Hand Held Computers, etc.

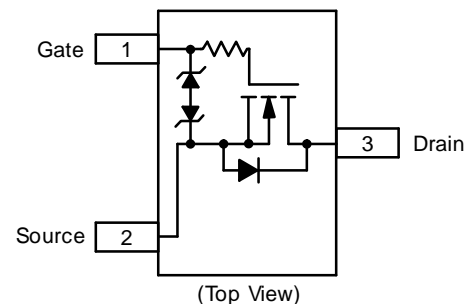


$V_{(BR)DSS}$	$R_{DS(on)}$ Typ @ $V_{GS}$	$I_D$ MAX (Note 1)
20 V	1.5 $\Omega$ @ 4.5 V	238 mA
	2.2 $\Omega$ @ 2.5 V	

### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Parameter	Symbol	Value	Unit
Drain- to- Source Voltage	$V_{DSS}$	20	V
Gate- to- Source Voltage	$V_{GS}$	$\pm 10$	V
Continuous Drain Current (Note 1)	$I_D$	Steady State = 25C	238 mA
Power Dissipation (Note 1)		Steady State = 25C	300 mW
Pulsed Drain Current	$I_{DM}$	$t_p \leq 10 \mu s$	714 mA
Operating Junction and Storage Temperature		$T_J, T_{STG}$	- 55 to 150
Continuous Source Current (Body Diode)	$I_{SD}$	238	mA
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)	$T_L$	260	°C

### PIN CONNECTIONS



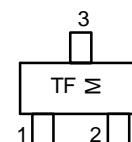
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

### THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction- to- Ambient – Steady State (Note 1)	$R_{\theta JA}$	416	°C/W

1. Surface- mounted on FR4 board using 1 in sq. pad size (Cu area = 1.127 in sq. [1 oz] including traces).

### MARKING DIAGRAM



TF = Specific Device Code  
M = Month Code

### ORDERING INFORMATION

Device	Package	Shipping
FTK4001E	SOT- 523	3000 Tape & Reel



# FTK4001E

## ELECTRICAL CHARACTERISTICS (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
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### OFF CHARACTERISTICS

Drain - to - Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 100μA	20			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 20 V			1.0	μA
Gate - to - Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ±10 V			±100	μA

### ON CHARACTERISTICS (Note 2)

Gate Threshold Voltage	V <sub>GS(TH)</sub>	V <sub>DS</sub> = 3 V, I <sub>D</sub> = 100μA	0.5	1.0	1.5	V
Drain - to - Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 mA		1.5	3.0	Ω
		V <sub>GS</sub> = 2.5 V, I <sub>D</sub> = 10 mA		2.2	3.5	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> = 3 V, I <sub>D</sub> = 10 mA		50		mS

### CAPACITANCES

Input Capacitance	C <sub>ISS</sub>	V <sub>DS</sub> = 5 V, f = 1 MHz, V <sub>GS</sub> = 0 V		11.5	20	pF
Output Capacitance	C <sub>OSS</sub>			10	15	
Reverse Transfer Capacitance	C <sub>RSS</sub>			3.5	6.0	

### SWITCHING CHARACTERISTICS (Note 3)

Turn - On Delay Time	t <sub>d(ON)</sub>	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 5 V, I <sub>D</sub> = 10 mA, R <sub>G</sub> = 10Ω		13		ns
Rise Time	t <sub>r</sub>			15		ns
Turn - Off Delay Time	t <sub>d(OFF)</sub>			98		
Fall Time	t <sub>f</sub>			60		

### DRAIN - SOURCE DIODE CHARACTERISTICS

Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 10 mA		0.66	0.8	V
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2. Pulse Test: pulse width ≤ 300 μs, duty cycle ≤ 2%.
3. Switching characteristics are independent of operating junction temperatures.

## Typical Characteristics (TA = 25°C Noted)

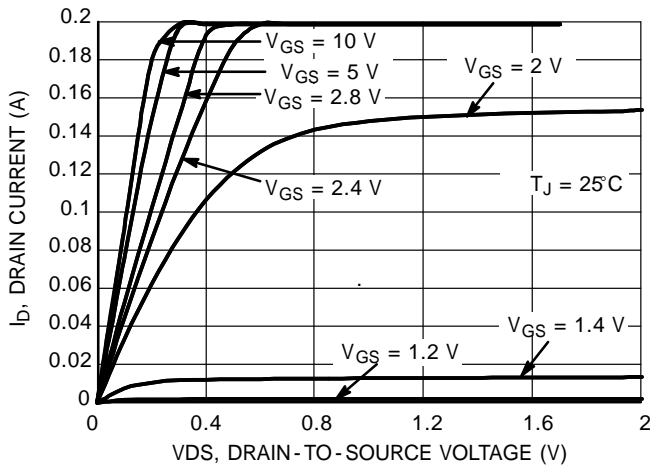


Figure 1. On-region Characteristics

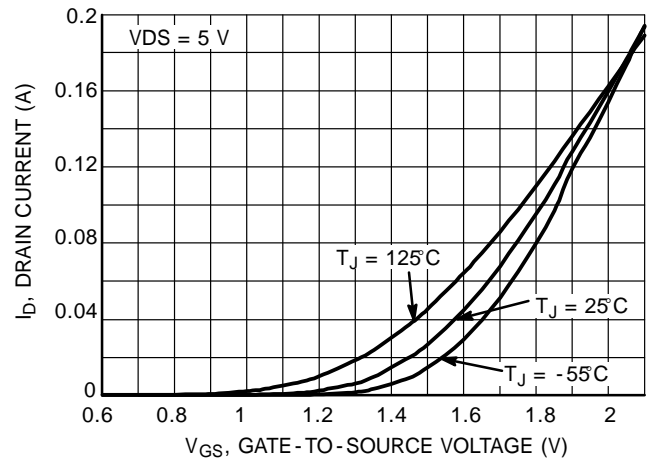


Figure 2. Transfer Characteristics

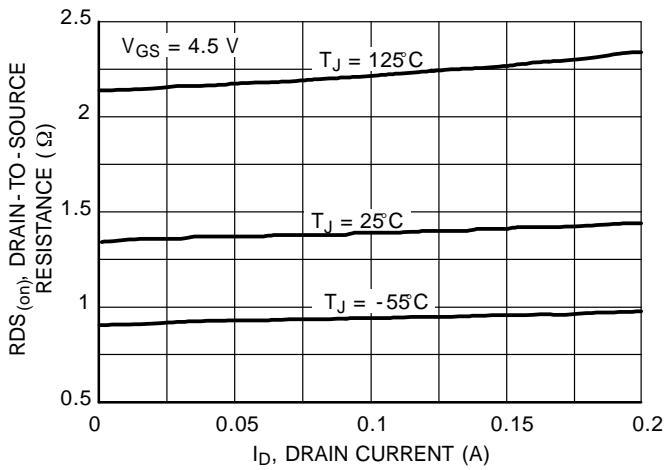


Figure 3. On-resistance versus Drain Current and Temperature

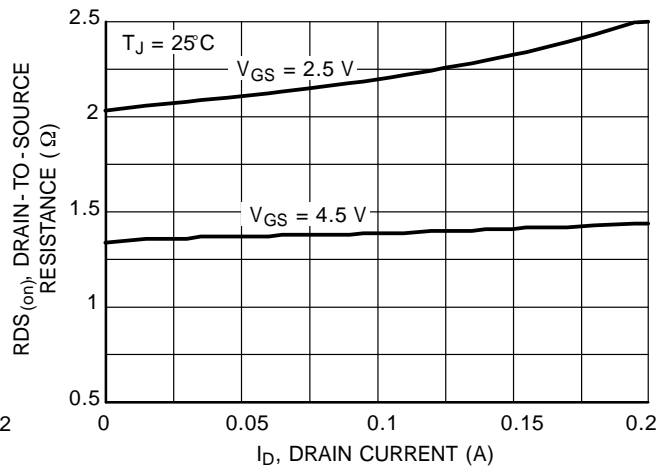


Figure 4. On-resistance versus Drain Current and Gate Voltage

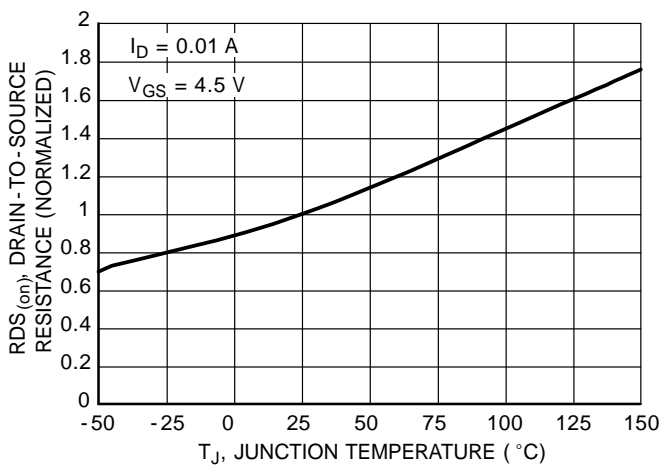


Figure 5. On-resistance Variation with Temperature

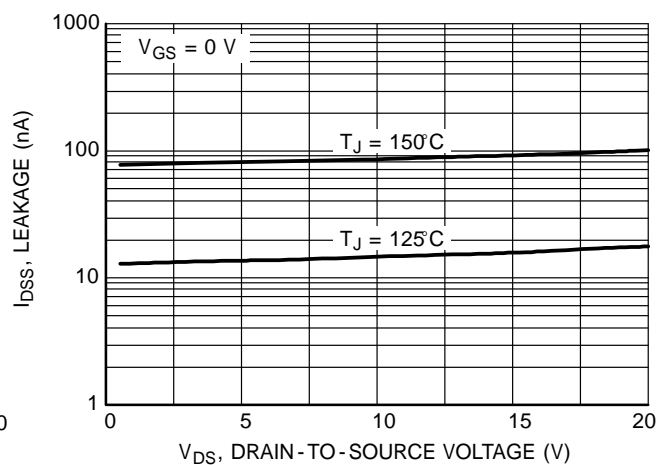
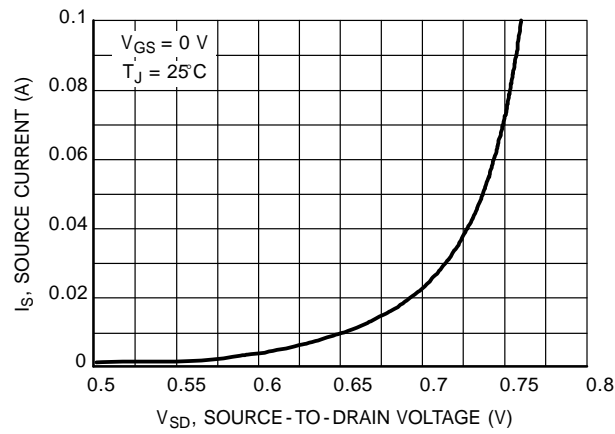
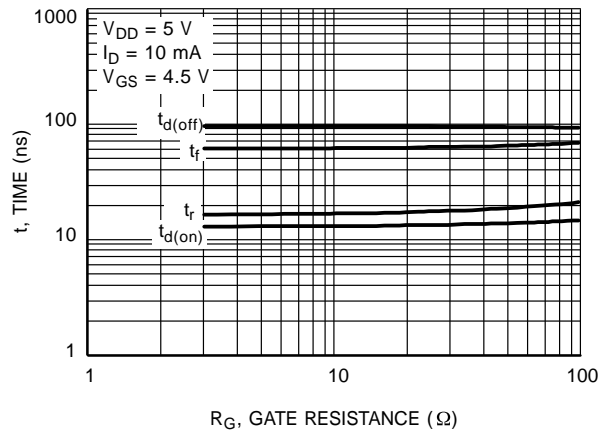
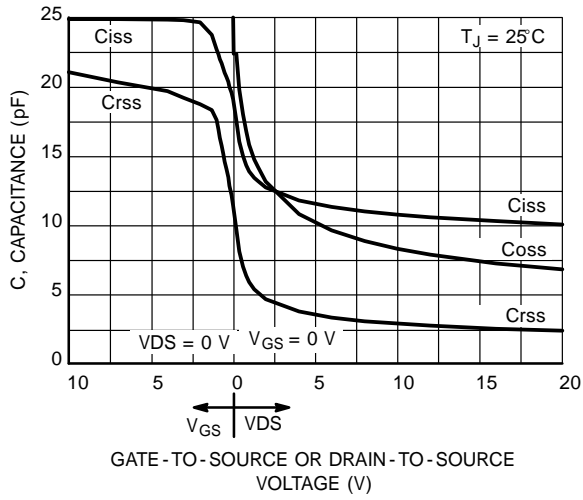
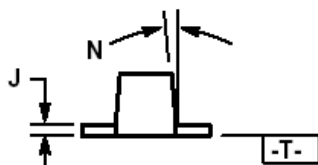
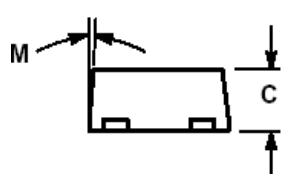
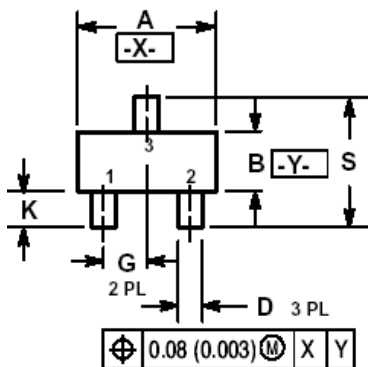


Figure 6. Drain-to-Source Leakage Current versus Voltage



# SOT-523



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. 463C-01 OBSOLETE, NEW STANDARD 463C-02.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.50	1.60	1.70	0.059	0.063	0.067
B	0.75	0.85	0.95	0.030	0.034	0.040
C	0.60	0.70	0.80	0.024	0.028	0.031
D	0.23	0.28	0.33	0.009	0.011	0.013
G	0.50 BSC			0.020 BSC		
H	0.53 REF			0.021 REF		
J	0.10	0.15	0.20	0.004	0.006	0.008
K	0.30	0.40	0.50	0.012	0.016	0.020
L	1.10 REF			0.043 REF		
M	---	---	10 °	---	---	10 °
N	---	---	10 °	---	---	10 °
S	1.50	1.60	1.70	0.059	0.063	0.067

