Dual N-Channel MOSFET

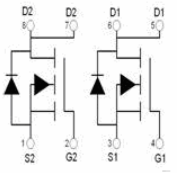
DESCRIPTION

The FTK4828 uses advanced trench te chnology to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a load switch or in PWM applications.

FEATURES

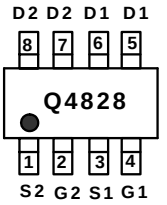
VDS (V) = 60V
ID = 4.5A (VGS = 10V)
RDS(ON) < 56mΩ (VGS = 10V)
RDS(ON) < 77mΩ (VGS = 4.5V)

AEC-Q101 qualified

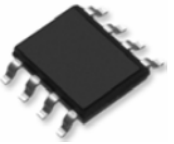


N-channel N-channel

Schematic diagram



Marking and pin Assignment



SOP-8 top view

Maximum ratings (T_a=25℃ unless otherwise noted)

Parameter	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current (t ≤10s) (note 1)	I_D	4.5	A
Pulsed Drain Current (note 2)	I_{DM}	20	A
Power Dissipation	P_D	1.25	W
Thermal Resistance from Junction to Ambient (t ≤10s) (note 1)	$R_{\theta JA}$	100	℃/W
Avalanche Current (note 2)	I_{AR},I_{AS}	19	A
Repetitive Avalenche Energy 0.1mH (note 2)	E_{AR},E_{AS}	18	mJ
Junction Temperature	T_J	150	℃
Storage Temperature	T_{STG}	-55~ 150	℃

Electrical characteristics (T_a=25℃ unless otherwise noted)

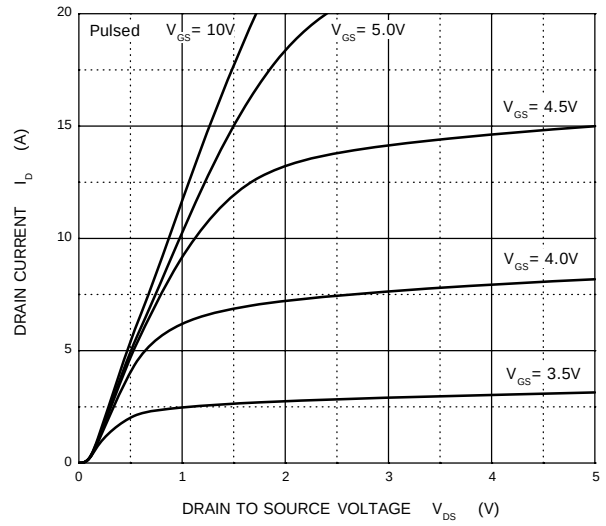
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
STATIC PARAMETERS						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D =250μA	60			V
Zero gate voltage drain current	I_{DSS}	V_{DS} =60V,V_{GS} = 0V			1	μA
Gate-body leakage current	I_{GSS}	V_{GS} =±20V, V_{DS} = 0V			±100	nA
Gate threshold voltage (note 3)	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =250μA	1		3	V
Drain-source on-resistance (note 3)	$R_{DS(on)}$	V_{GS} =10V, I_D =4.5A			56	mΩ
		V_{GS} =4.5V, I_D =3A			77	mΩ
Forward tranconductance (note 3)	g_{FS}	V_{DS} =5V, I_D =4.5A	6			S
Diode forward voltage (note 3)	V_{SD}	I_S=1A, V_{GS} = 0V			1	V
DYNAMIC PARAMETERS (note 4)						
Input Capacitance	C_{iss}	V_{DS} =30V,V_{GS} =0V,f =1MHz			540	pF
Output Capacitance	C_{oss}			60		pF
Reverse Transfer Capacitance	C_{rss}			25		pF
SWITCHING PARAMETERS (note 4)						
Turn-on delay time	$t_{d(on)}$	V_{GS}=10V,V_{DS}=30V, R_L=6.7Ω,R_{GEN}=3Ω		4.7		ns
Turn-on rise time	t_r			2.3		ns
Turn-off delay time	$t_{d(off)}$			15.7		ns
Turn-off fall time	t_f			1.9		ns
Total Gate Charge (10V)	Q_g	V_{DS} =30V,V_{GS} =10V,I_D =4.5A			10.5	nC
Total Gate Charge (4.5V)					5.5	nC
Gate-Source Charge	Q_{gs}			1.6		nC
Gate-Drain Charge	Q_{gd}			2.2		nC

Notes :

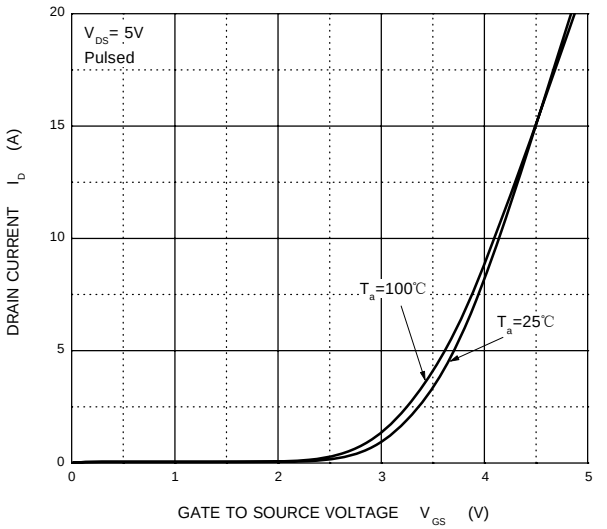
1. The value in any given application depends on the user's specific board design.
2. Repetitive rating : Pulse width limited by junction temperature.
3. Pulse Test : Pulse Width≤300μs, Duty Cycle≤0.5%.
4. These parameters have no way to verify.

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

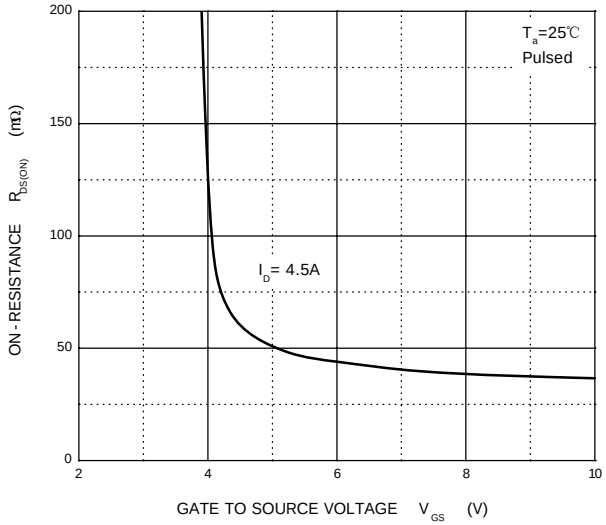
Output Characteristics



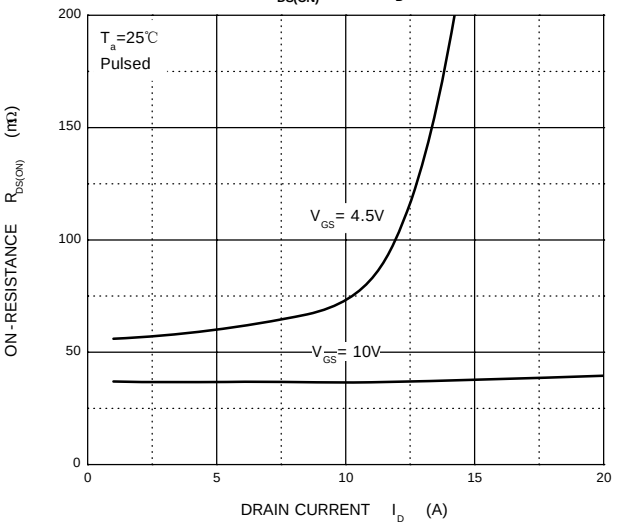
Transfer Characteristics



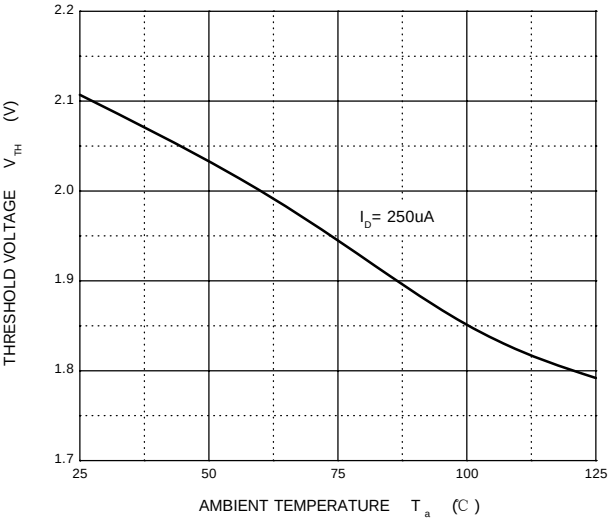
$R_{DS(ON)}$ —— V_{GS}



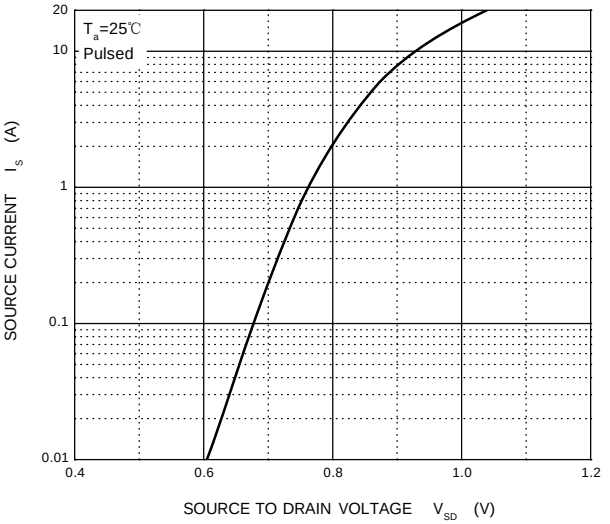
$R_{DS(ON)}$ —— I_D



Threshold Voltage

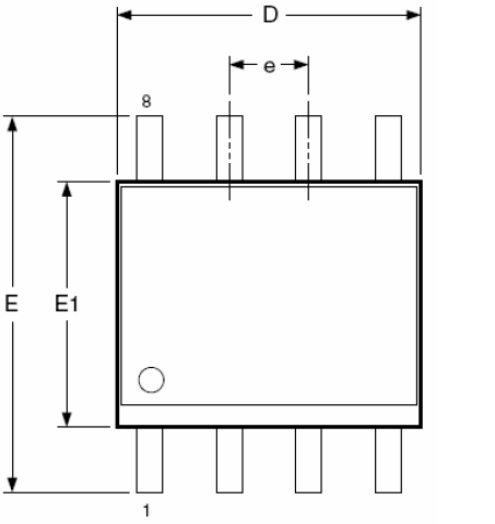


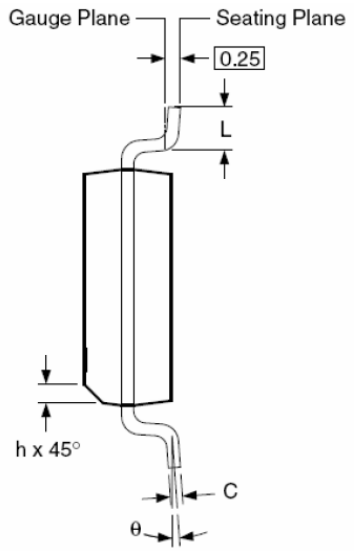
I_S —— V_{SD}

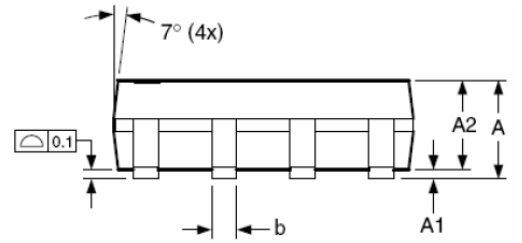


SOP-8 PACKAGE INFORMATION

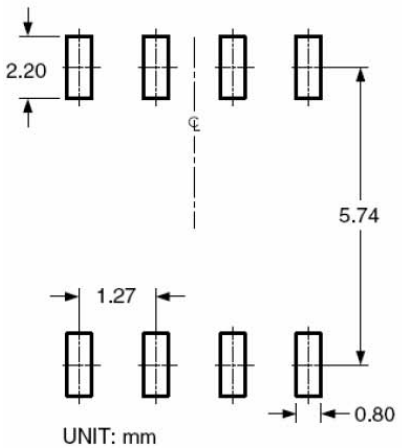
Dimensions in Millimeters (UNIT:mm)







RECOMMENDED LAND PATTERN



Dimensions in millimeters

Symbols	Min.	Nom.	Max.
A	1.35	1.65	1.75
A1	0.10	—	0.25
A2	1.25	1.50	1.65
b	0.31	—	0.51
c	0.17	—	0.25
D	4.80	4.90	5.00
E1	3.80	3.90	4.00
e	1.27 BSC		
E	5.80	6.00	6.20
h	0.25	—	0.50
L	0.40	—	1.27
θ	0°	—	8°

Dimensions in inches

Symbols	Min.	Nom.	Max.
A	0.053	0.065	0.069
A1	0.004	—	0.010
A2	0.049	0.059	0.065
b	0.012	—	0.020
c	0.007	—	0.010
D	0.189	0.193	0.197
E1	0.150	0.154	0.157
e	0.050 BSC		
E	0.228	0.236	0.244
h	0.010	—	0.020
L	0.016	—	0.050
θ	0°	—	8°

NOTES:

1. All dimensions are in millimeters.
2. Dimensions are inclusive of plating
3. Package body sizes exclude mold flash and gate burrs. Mold flash at the non-lead sides should be less than 6 mils.
4. Dimension L is measured in gauge plane.
5. Controlling dimension is millimeter, converted inch dimensions are not necessarily exact.