

## 100V N-Channel Enhancement Mode Power MOSFET

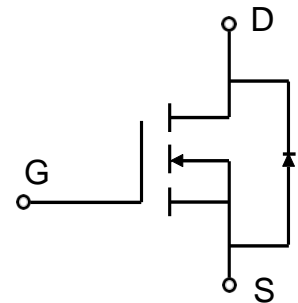
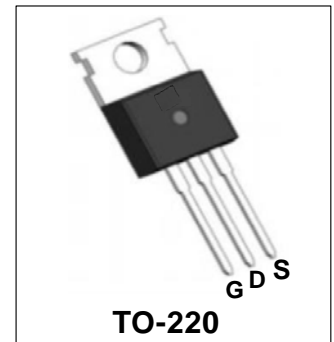
WMK028N10HG2 uses Wayon's 2<sup>nd</sup> generation power trench MOSFET technology that has been especially tailored to minimize the on-state resistance and yet maintain superior switching performance. This device is well suited for high efficiency fast switching applications.

### Features

- $V_{DS}=100V$ ,  $I_D=245A$ (Silicon Limited)  
 $R_{DS(on)} < 2.8m\Omega @ V_{GS}=10V$
- High Speed Power Switching
- Low  $R_{DS(on)}$
- Low Gate Charge
- 100% EAS Guaranteed

### Applications

- Hard Switching and High Speed Circuit
- DC/DC Converters
- Synchronous Rectification in SMPS



### Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup> (Silicon Limited)	$I_D$	$T_C=25^\circ C$	245
		$T_C=100^\circ C$	170
Continuous Drain Current <sup>1</sup> (Package Limited)		$T_C=25^\circ C$	175
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	780	A
Single Pulse Avalanche Energy <sup>3</sup>	<b>EAS</b>	845	mJ
Avalanche Current	$I_{AS}$	65	A
Total Power Dissipation <sup>4</sup>	$P_D$	$T_C=25^\circ C$	278
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ C$

### Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance from Junction-to-Ambient <sup>1</sup>	$R_{\theta JA}$	61	$^\circ C/W$
Thermal Resistance from Junction-to-Case <sup>1</sup>	$R_{\theta JC}$	0.45	$^\circ C/W$



# WMK028N10HG2

## Electrical Characteristics (T<sub>C</sub> = 25°C, unless otherwise noted)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	100	-	-	V
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±20V	-	-	±100	nA
Zero Gate Voltage Drain Current	T <sub>J</sub> =25°C	I <sub>DSS</sub> V <sub>DS</sub> = 100V, V <sub>GS</sub> = 0V	-	-	10	μA
	T <sub>J</sub> =100°C		-	-	100	
Gate-Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2	3	4	V
Drain-Source on-Resistance <sup>2</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 20A	-	2.4	2.8	mΩ
Forward Transconductance <sup>2</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 5V, I <sub>D</sub> = 20A	-	70	-	S
<b>Dynamic Characteristics</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 50V, V <sub>GS</sub> = 0V, f = 1MHz	-	7735	-	pF
Output Capacitance	C <sub>oss</sub>		-	1190	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	25	-	
<b>Switching Characteristics</b>						
Gate Resistance	R <sub>g</sub>	V <sub>GS</sub> = 0V, V <sub>DS</sub> = 0V, f = 1MHz	-	1.4	-	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, I <sub>D</sub> = 20A	-	98	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	20	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	18	-	
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 50V, R <sub>G</sub> = 10Ω, I <sub>D</sub> = 20A	-	25	-	nS
Rise Time	t <sub>r</sub>		-	20	-	
Turn-off Delay Time	t <sub>d(off)</sub>		-	50	-	
Fall Time	t <sub>f</sub>		-	11	-	
<b>Drain-Source Body Diode Characteristics</b>						
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	I <sub>S</sub> = 20A, V <sub>GS</sub> = 0V	-	-	1.2	V
Continuous Source Current <sup>1,5</sup>	I <sub>S</sub>	V <sub>G</sub> = V <sub>D</sub> = 0V, Force Current	-	-	245	A
Reverse Recovery Time	t <sub>rr</sub>	V <sub>R</sub> = 50V, I <sub>F</sub> = 20A, dI/dt = 500A/μs	-	60	-	nS
Reverse Recovery Charge	Q <sub>rr</sub>		-	438	-	nC

### Notes:

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
3. The EAS data shows Max. rating. The test condition is V<sub>DD</sub> = 25V, V<sub>GS</sub> = 10V, L = 0.4mH, I<sub>AS</sub> = 65A
4. The power dissipation is limited by 150°C junction temperature
5. The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

## Typical Performance Characteristics

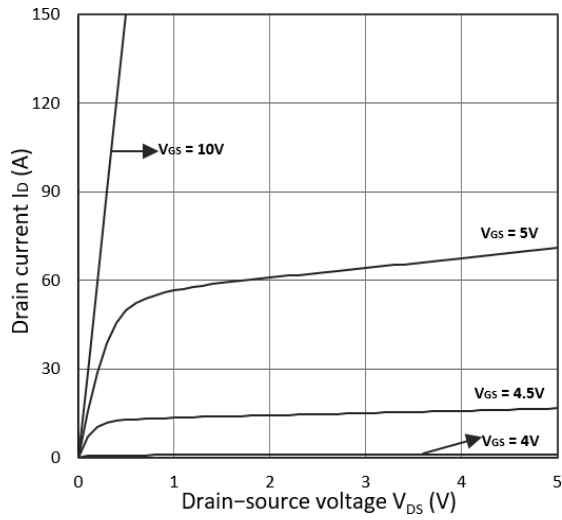


Figure 1. Output Characteristics

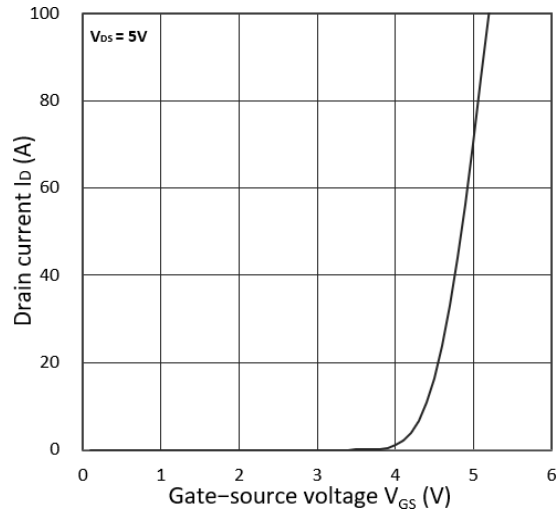


Figure 2. Transfer Characteristics

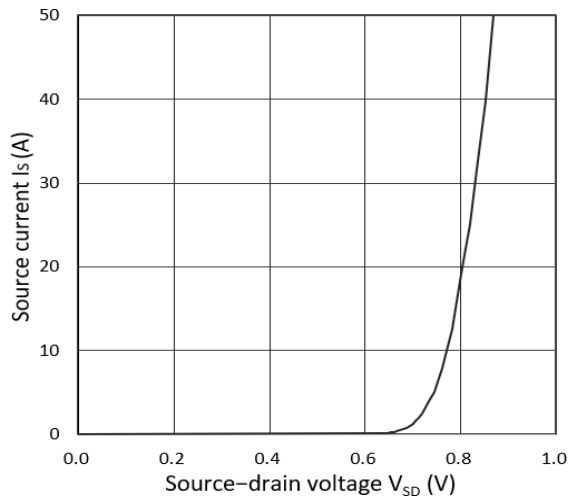


Figure 3. Forward Characteristics of Reverse

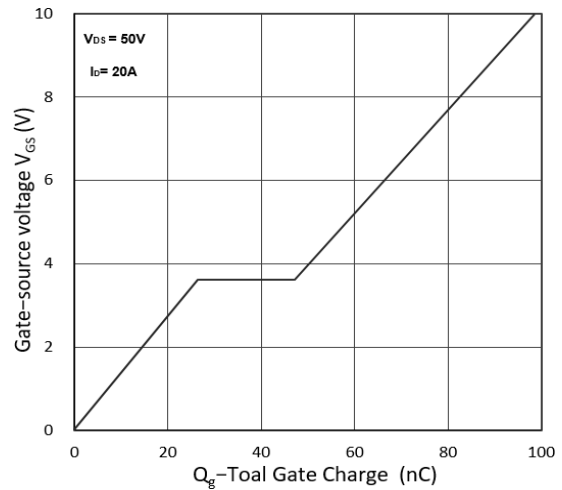


Figure 4. Gate Charge Characteristics

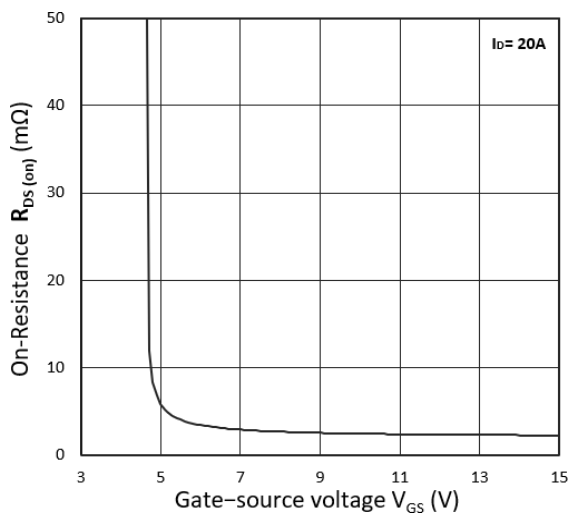


Figure 5.  $R_{DS(on)}$  vs.  $V_{GS}$

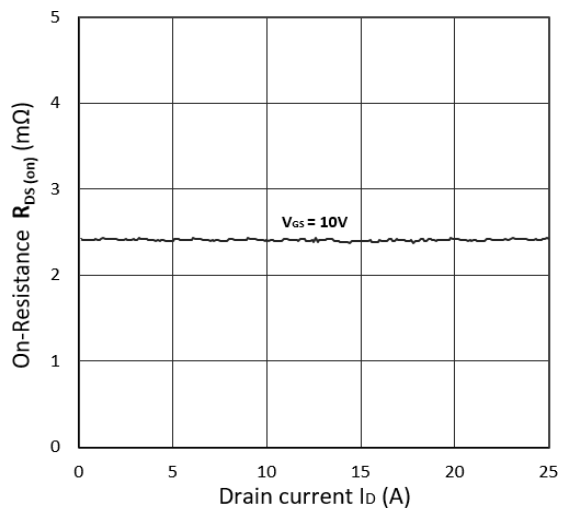


Figure 6.  $R_{DS(on)}$  vs.  $I_D$

## Typical Performance Characteristics(Con.)

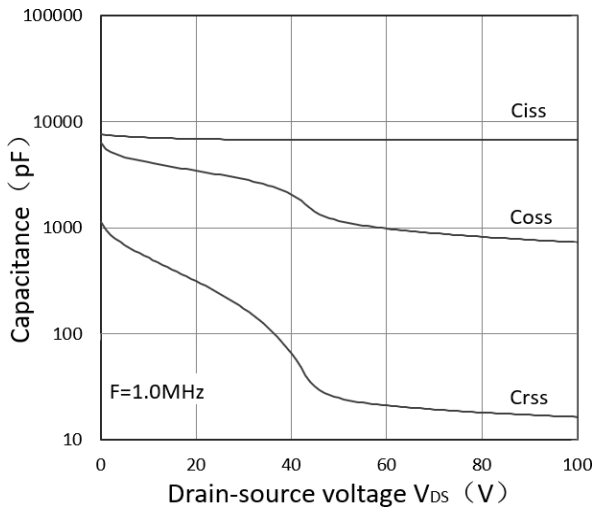


Figure 7. Capacitance Characteristics

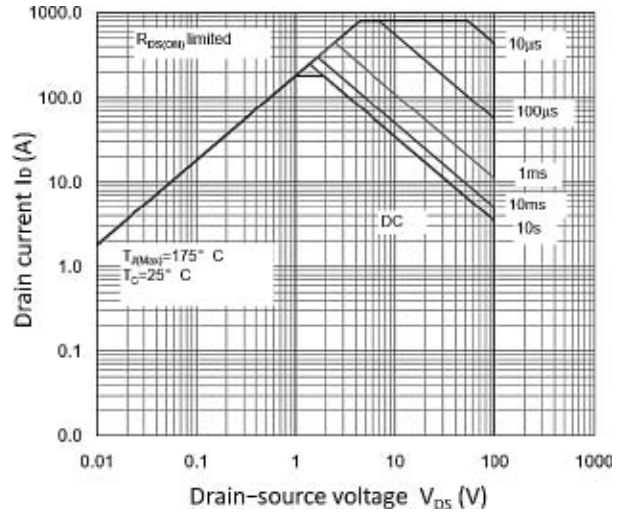


Figure 8. Safe Operating Area

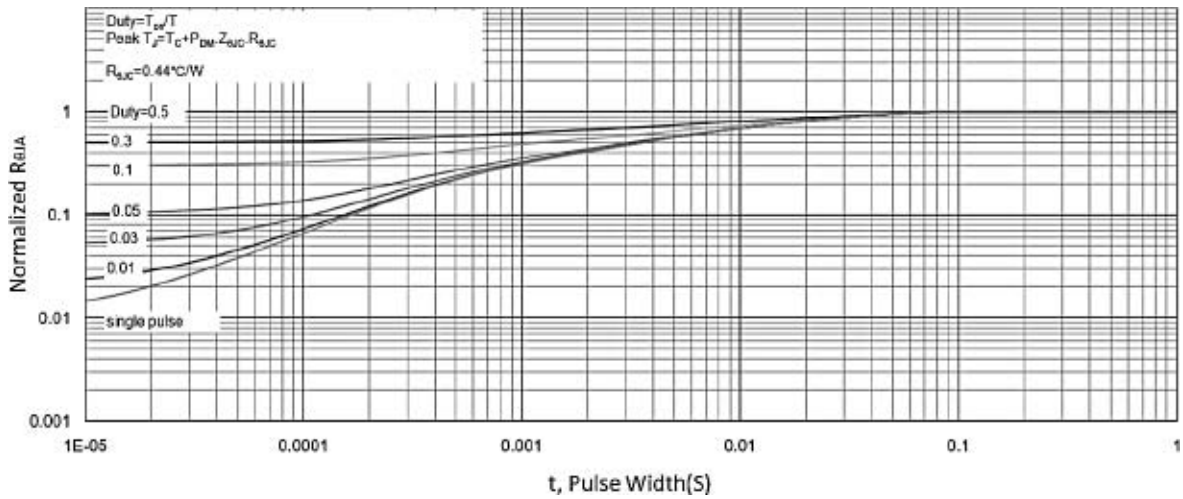


Figure 9. Normalized Maximum Transient Thermal Impedance

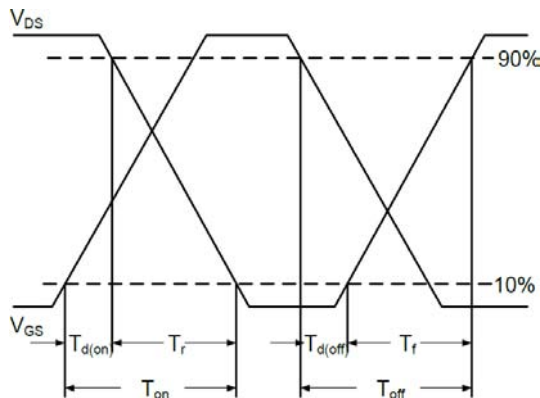


Figure 10. Switching Time Waveform

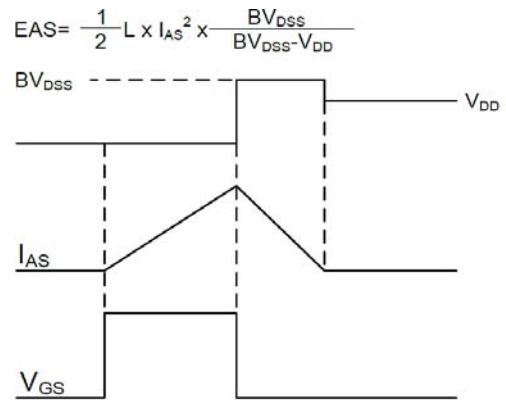
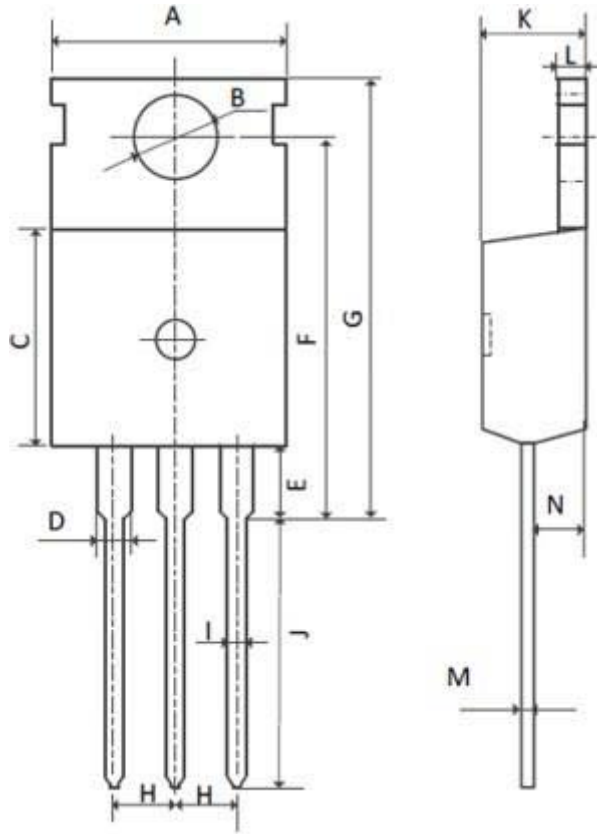


Figure 11. Unclamped Inductive Switching Waveform

## Mechanical Dimensions for TO-220



### COMMON DIMENSIONS

SYMBOL	MM	
	MIN	MAX
A	9.70	10.30
B	3.40	3.80
C	8.80	9.40
D	1.17	1.47
E	2.60	3.50
F	15.10	16.70
G	19.55MAX	
H	2.54REF	
I	0.70	0.95
J	9.35	11.00
K	4.30	4.77
L	1.20	1.45
M	0.40	0.65
N	2.20	2.60