

## 100V P-Channel MOSFETs

### General Description

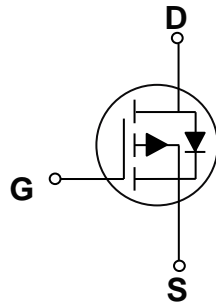
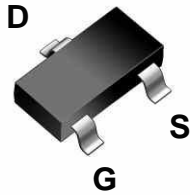
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	RDSON	ID
-100V	650mΩ	-840mA

### Features

- -100V,-840mA , RDS(ON)=650mΩ@VGS=-10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

### SOT23S Pin Configuration



### Applications

- Networking
- Load Switch
- LED applications

### Absolute Maximum Ratings $T_c=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-100	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_A=25^\circ\text{C}$ )	-840	mA
	Drain Current – Continuous ( $T_A=70^\circ\text{C}$ )	-670	mA
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	-3.36	A
$P_D$	Power Dissipation ( $T_A=25^\circ\text{C}$ )	1.56	W
	Power Dissipation – Derate above $25^\circ\text{C}$	0.008	W/ $^\circ\text{C}$
$T_{STG}$	Storage Temperature Range	-50 to 150	$^\circ\text{C}$
$T_J$	Operating Junction Temperature Range	-50 to 150	$^\circ\text{C}$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	125	$^\circ\text{C}/\text{W}$



# FTK0953S

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Electrical Characteristics ( $T_J=25\text{ }^\circ\text{C}$ , unless otherwise noted)

### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V$ , $I_D=-250\mu A$	-100	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^\circ\text{C}$ , $I_D=1mA$	---	0.08	---	V/ $^\circ\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=-100V$ , $V_{GS}=0V$ , $T_J=25\text{ }^\circ\text{C}$	---	---	1	$\mu A$
		$V_{DS}=-80V$ , $V_{GS}=0V$ , $T_J=125\text{ }^\circ\text{C}$	---	---	10	$\mu A$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$	---	---	$\pm 100$	nA

### On Characteristics

$R_{DS(ON)}$	Static Drain-Source On-Resistance	$V_{GS}=-10V$ , $I_D=-600mA$	---	540	650	m $\Omega$
		$V_{GS}=-4.5V$ , $I_D=-400mA$	---	590	760	m $\Omega$
$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D=-250\mu A$	-1.2	-1.8	-2.5	V
$\Delta V_{GS(th)}$	$V_{GS(th)}$ Temperature Coefficient		---	-4.6	---	mV/ $^\circ\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=-10V$ , $I_D=-0.5A$	---	2	---	S

### Dynamic and switching Characteristics

$Q_g$	Total Gate Charge <sup>2, 3</sup>	$V_{DS}=-50V$ , $V_{GS}=-10V$ , $I_D=-500mA$	---	4.4	8.8	nC
$Q_{gs}$	Gate-Source Charge <sup>2, 3</sup>		---	0.5	1	
$Q_{gd}$	Gate-Drain Charge <sup>2, 3</sup>		---	1.8	3.6	
$T_{d(on)}$	Turn-On Delay Time <sup>2, 3</sup>	$V_{DD}=-50V$ , $V_{GS}=-10V$ , $R_G=3.3\Omega$ $I_D=-0.5A$	---	5	10	ns
$T_r$	Rise Time <sup>2, 3</sup>		---	14.5	29	
$T_{d(off)}$	Turn-Off Delay Time <sup>2, 3</sup>		---	20	40	
$T_f$	Fall Time <sup>2, 3</sup>		---	8	16	
$C_{iss}$	Input Capacitance	$V_{DS}=-50V$ , $V_{GS}=0V$ , $F=1MHz$	---	382	760	pF
$C_{oss}$	Output Capacitance		---	29	60	
$C_{rss}$	Reverse Transfer Capacitance		---	18	36	
$R_g$	Gate resistance	$V_{GS}=0V$ , $V_{DS}=0V$ , $F=1MHz$	---	31	---	$\Omega$

### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_S$	Continuous Source Current	$V_G=V_D=0V$ , Force Current	---	---	-0.84	A
$I_{SM}$	Pulsed Source Current		---	---	-1.68	A
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V$ , $I_S=-1A$ , $T_J=25\text{ }^\circ\text{C}$	---	---	-1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.



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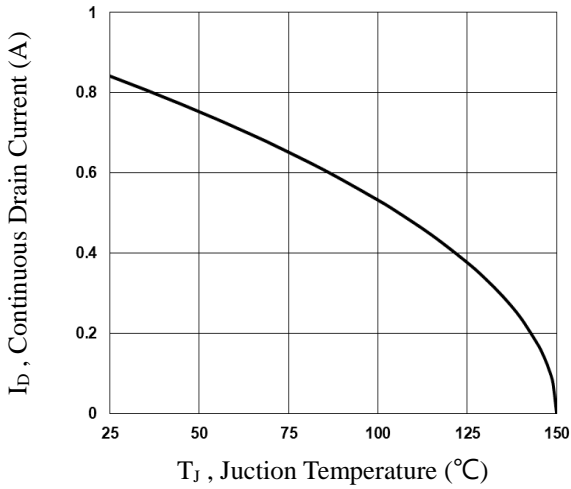


Fig.1 Continuous Drain Current vs.  $T_J$

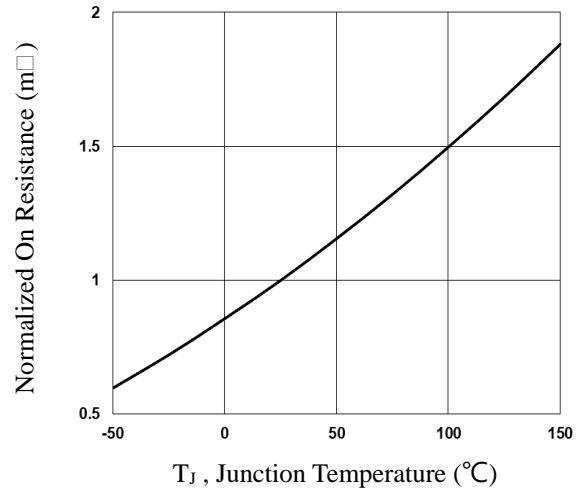


Fig.2 Normalized  $R_{DS(on)}$  vs.  $T_J$

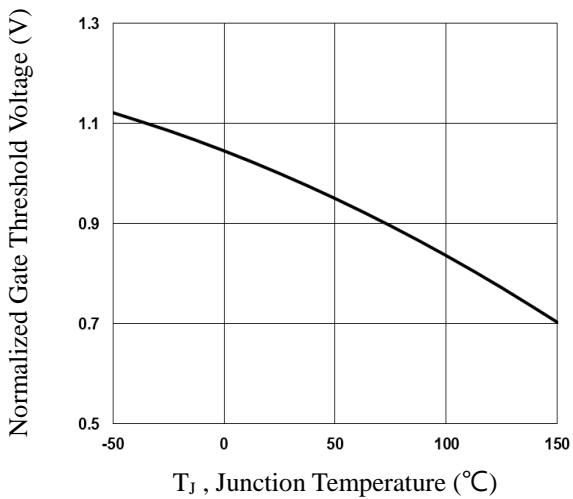


Fig.3 Normalized  $V_{th}$  vs.  $T_J$

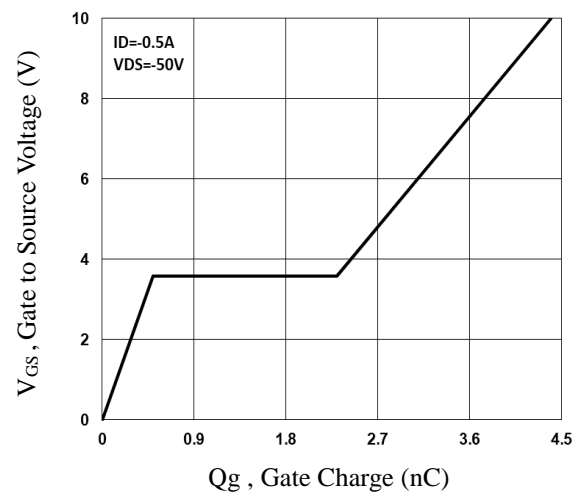


Fig.4 Gate Charge Waveform

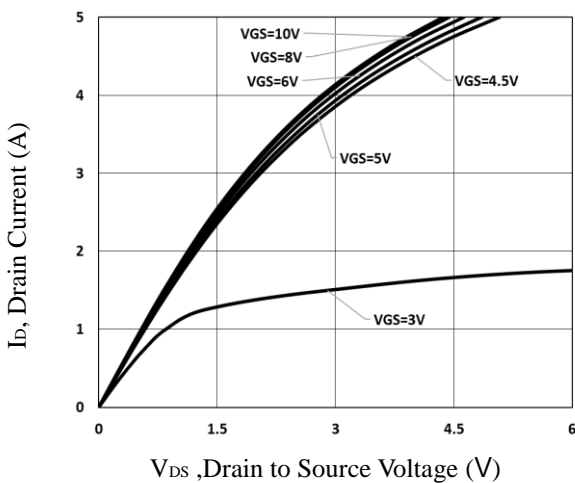


Fig.5 Typical Output Characteristics

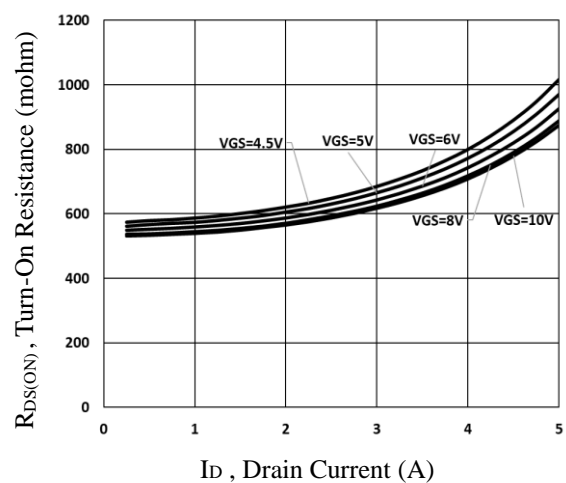


Fig.6 Turn-On Resistance vs.  $I_D$

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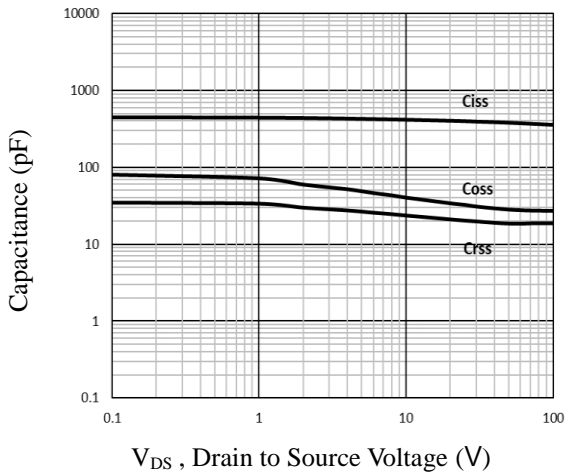


Fig.7 Capacitance Characteristics

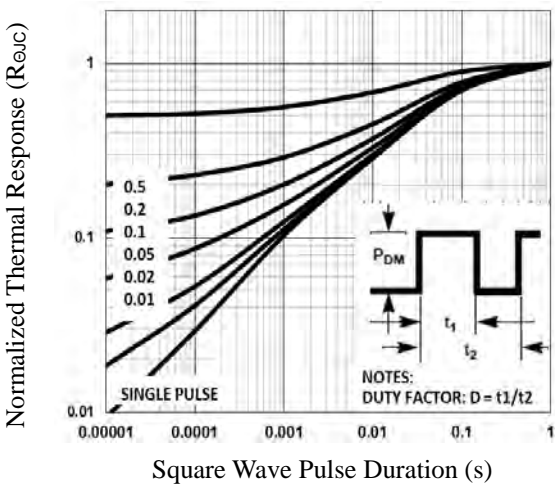


Fig.8 Normalized Transient Response

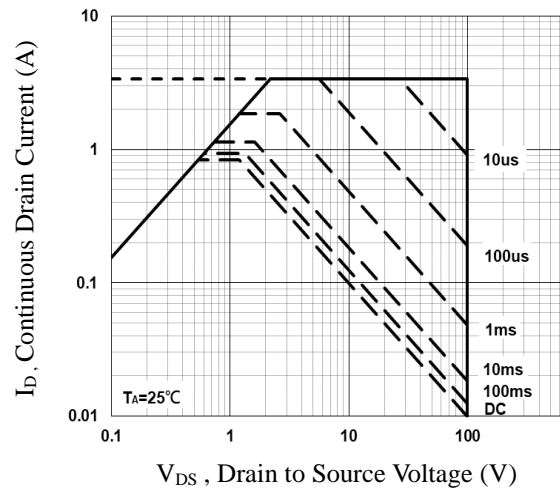


Fig.9 Maximum Safe Operation Area

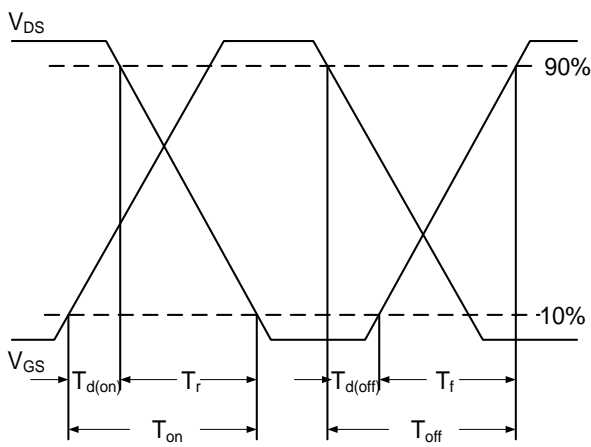


Fig.10 Switching Time Waveform

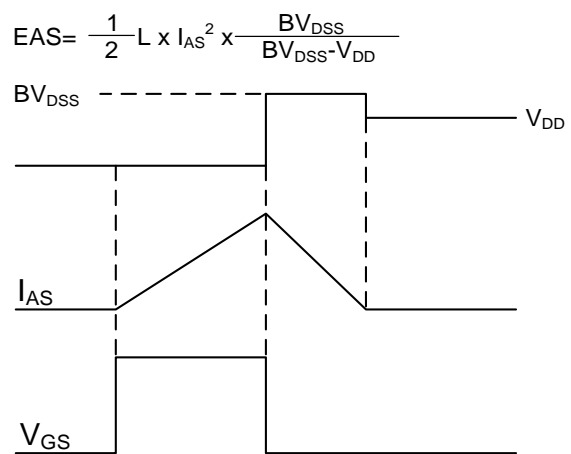
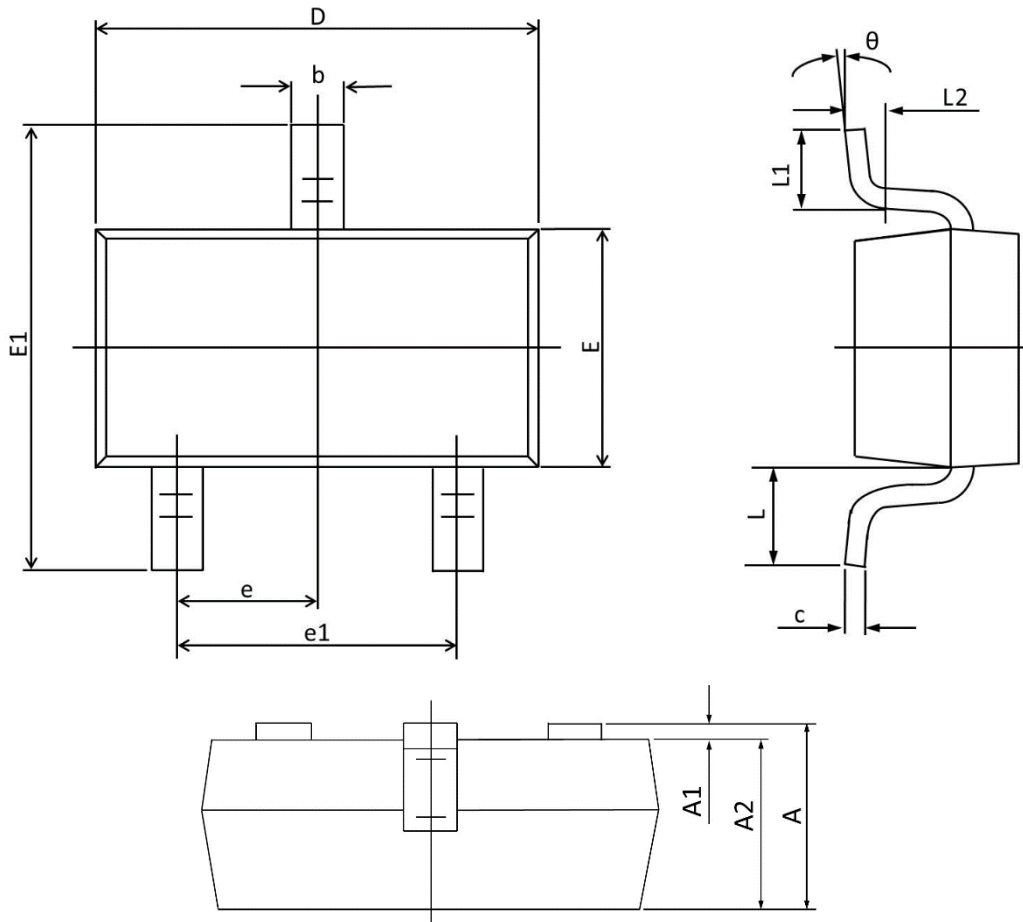


Fig.11 EAS Waveform

## 100V P-Channel MOSFETs

### SOT23S PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.000	0.035	0.039
A1	0.000	0.100	0.000	0.004
b	0.300	0.500	0.012	0.020
c	0.090	0.110	0.003	0.004
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP.		0.037 TYP.	
e1	1.800	2.000	0.071	0.079
L	0.550 REF.		0.022 REF.	
L1	0.300	0.500	0.012	0.020
θ	1°	7°	1°	7°