

## 40V N-Channel MOSFETs

### General Description

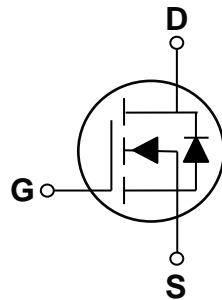
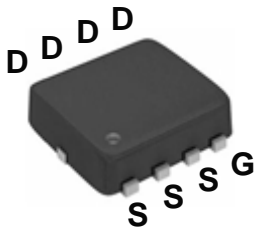
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R <sub>DS(ON)</sub>	I <sub>D</sub>
40V	5.5mΩ	70A

### Features

- 40V, 70A, R<sub>DS(ON)</sub>=5.5mΩ @V<sub>GS</sub> = 10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

### PPAK3X3 Pin Configuration



### Applications

- Notebook
- Load Switch
- LED applications
- Hand-Held Device

### Absolute Maximum Ratings T<sub>c</sub>=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	40	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub>	Drain Current – Continuous (T <sub>c</sub> =25°C)	70	A
	Drain Current – Continuous (T <sub>c</sub> =100°C)	44.3	A
I <sub>DM</sub>	Drain Current – Pulsed <sup>1</sup>	280	A
P <sub>D</sub>	Power Dissipation (T <sub>c</sub> =25°C)	52	W
	Power Dissipation – Derate above 25°C	0.42	W/°C
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>J</sub>	Operating Junction Temperature Range	-55 to 150	°C

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R <sub>θJA</sub>	Thermal Resistance Junction to ambient	---	62	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction to Case	---	2.4	°C/W



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### Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)

#### Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	40	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25 °C, I <sub>D</sub> =1mA	---	0.03	---	V/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V, T <sub>J</sub> =25 °C	---	---	1	uA
		V <sub>DS</sub> =32V, V <sub>GS</sub> =0V, T <sub>J</sub> =85 °C	---	---	10	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA

#### On Characteristics

R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	4.2	5.5	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	---	5.3	7	mΩ
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	1.6	2.5	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	-5	---	mV/°C
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =10V, I <sub>D</sub> =10A	---	16	---	S

#### Dynamic and switching Characteristics

Q <sub>g</sub>	Total Gate Charge <sup>2, 3</sup>	V <sub>DS</sub> =32V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	---	25	50	nC
Q <sub>gs</sub>	Gate-Source Charge <sup>2, 3</sup>		---	6.4	13	
Q <sub>gd</sub>	Gate-Drain Charge <sup>2, 3</sup>		---	12.1	24	
T <sub>d(on)</sub>	Turn-On Delay Time <sup>2, 3</sup>	V <sub>DD</sub> =20V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω I <sub>D</sub> =1A	---	14.2	28	ns
T <sub>r</sub>	Rise Time <sup>2, 3</sup>		---	18.3	36	
T <sub>d(off)</sub>	Turn-Off Delay Time <sup>2, 3</sup>		---	38.8	76	
T <sub>f</sub>	Fall Time <sup>2, 3</sup>		---	13.9	28	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, F=1MHz	---	2410	3600	pF
C <sub>oss</sub>	Output Capacitance		---	233	400	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	152	230	
R <sub>g</sub>	Gate resistance	V <sub>GS</sub> =0V, V <sub>DS</sub> =0V, F=1MHz	---	1.6	3.2	Ω

#### Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I <sub>S</sub>	Continuous Source Current	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	70	A
I <sub>SM</sub>	Pulsed Source Current		---	---	140	A
V <sub>SD</sub>	Diode Forward Voltage	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25 °C	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.

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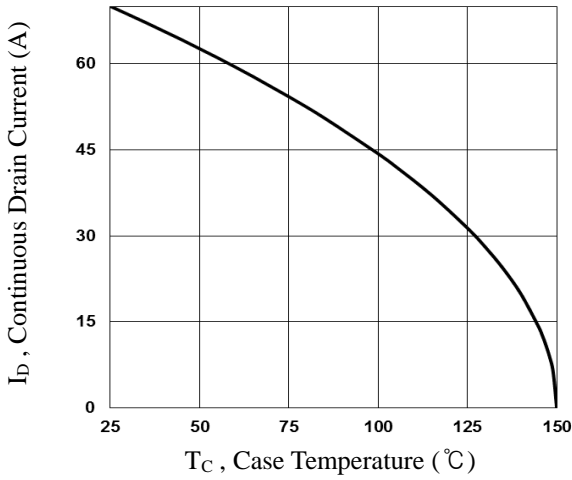


Fig. 1 Continuous Drain Current vs.  $T_c$

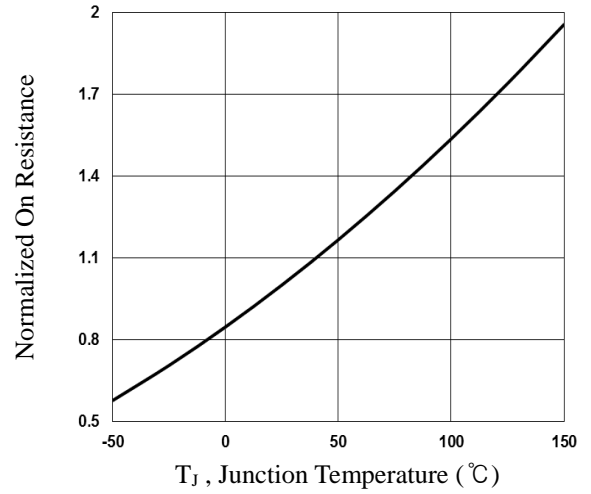


Fig. 2 Normalized  $R_{DS(on)}$  vs.  $T_j$

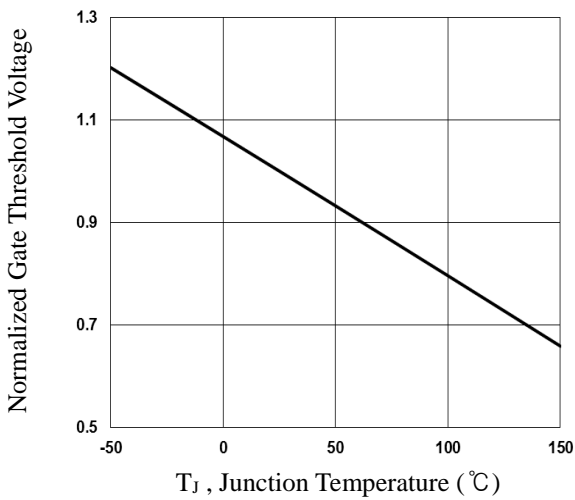


Fig. 3 Normalized  $V_{th}$  vs.  $T_j$

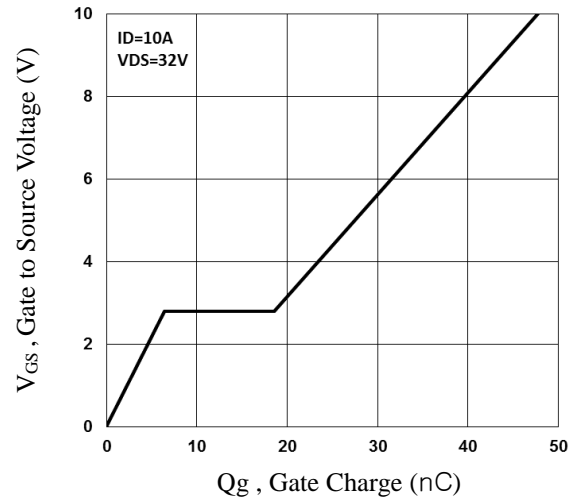


Fig. 4 Gate Charge Waveform

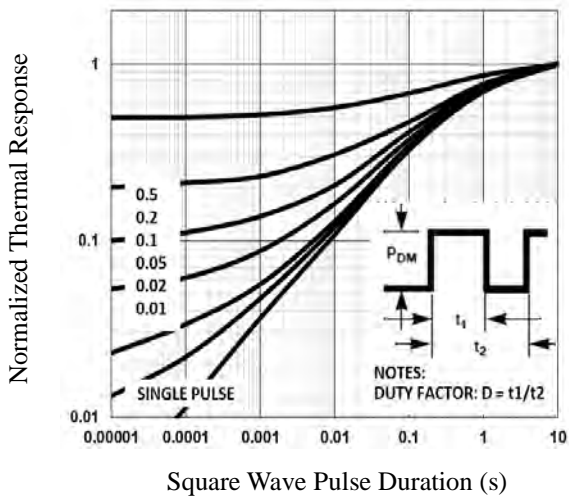


Fig. 5 Normalized Transient Impedance

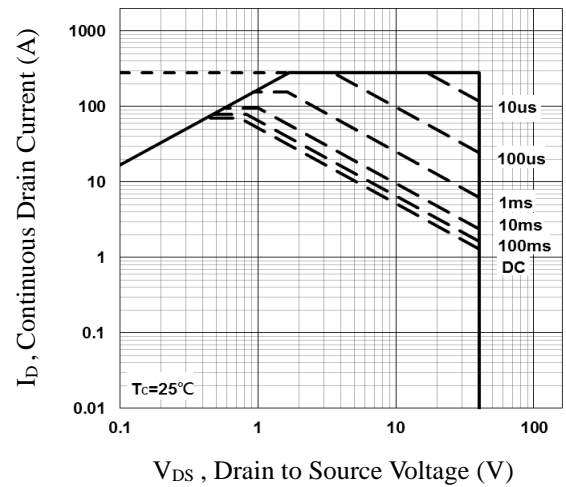


Fig. 6 Maximum Safe Operation Area

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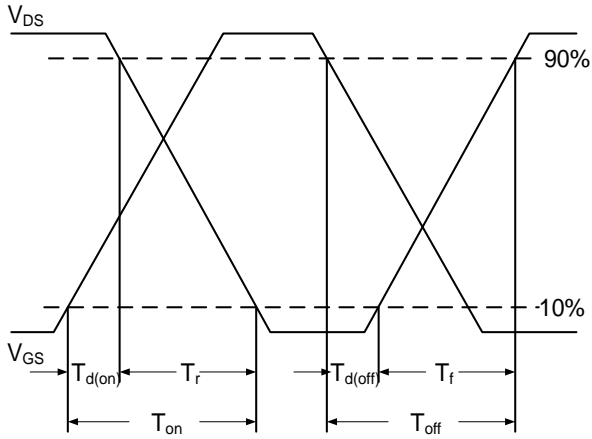


Fig. 7 Switching Time Waveform

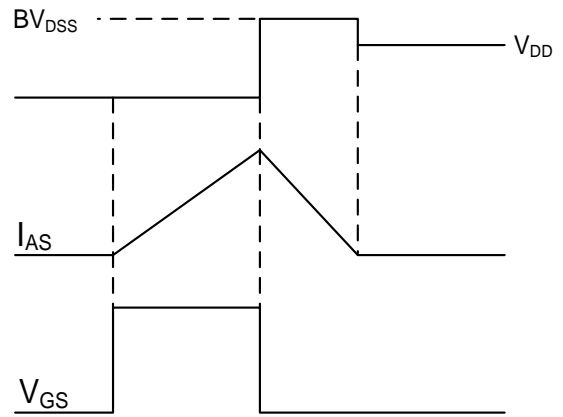
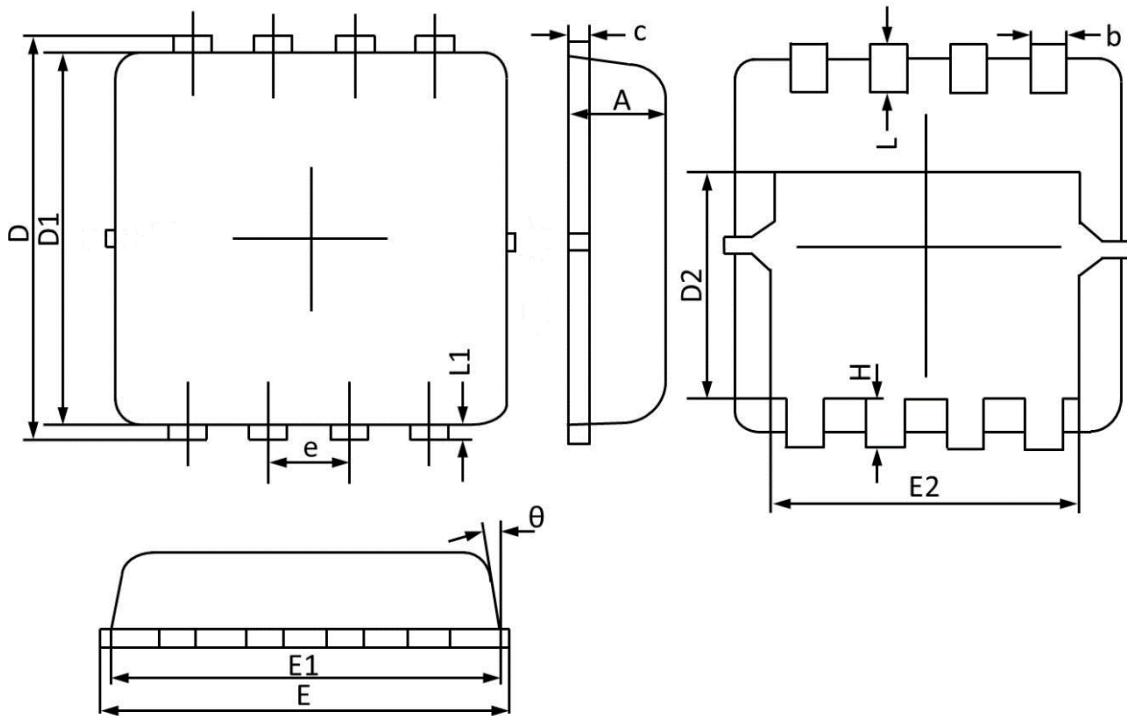


Fig. 8 EAS Waveform

## 40V N-Channel MOSFETs

### PPAK3x3 PACKAGE INFORMATION



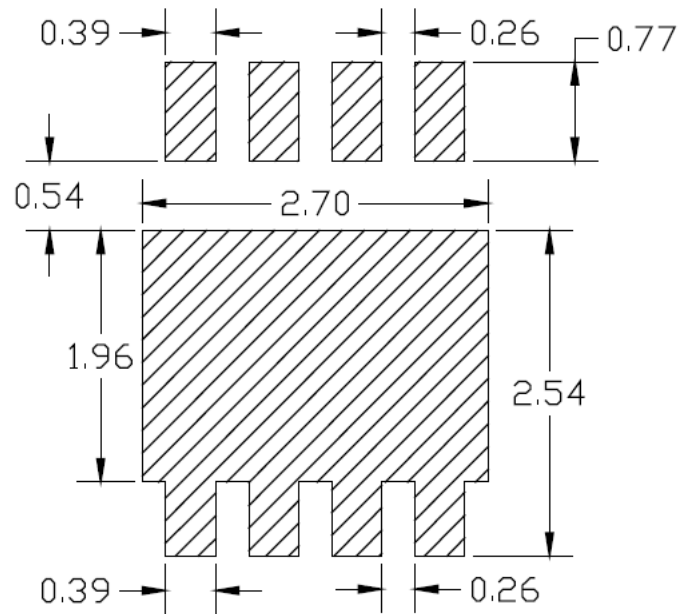
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
c	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
$\theta$	14°	6°	14°	6°



# FTK4904Z

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### PPAK3X3 RECOMMENDED LAND PATTERN



unit : mm