

20V N-Channel MOSFETs

General Description

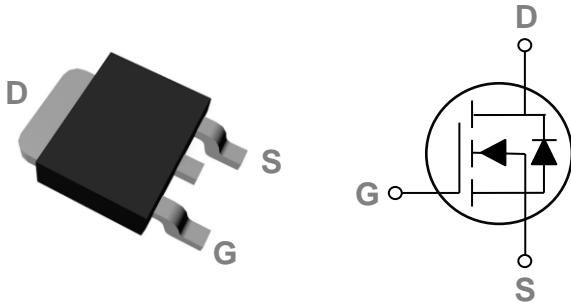
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
20V	17mΩ	30A

Features

- 20V,30A, R_{DS(ON)} =17mΩ @V_{GS} = 4.5V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO252 Pin Configuration



Applications

- POL Applications
- SMPS 2nd SR

Absolute Maximum Ratings T_c=25 °C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	20	V
V _{GS}	Gate-Source Voltage	±12	V
I _D	Drain Current – Continuous (T _C =25 °C)	30	A
	Drain Current – Continuous (T _C =100 °C)	19	A
I _{DM}	Drain Current – Pulsed ¹	120	A
P _D	Power Dissipation (T _C =25 °C)	24.9	W
	Power Dissipation – Derate above 25 °C	0.20	W/°C
T _{STG}	Storage Temperature Range	-55 to 175	°C
T _J	Operating Junction Temperature Range	-55 to 175	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	5.02	°C/W



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Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	20	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =20V, V _{GS} =0V, T _J =25 °C	---	---	1	μA
		V _{DS} =16V, V _{GS} =0V, T _J =125 °C	---	---	10	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±12V, V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =4.5V, I _D =10A	---	14	17	mΩ
		V _{GS} =2.5V, I _D =5A	---	18.5	24	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	0.5	0.8	1.2	V

Dynamic and switching Characteristics²

Q _g	Total Gate Charge	V _{DS} =10V, V _{GS} =4.5V, I _D =5A	---	5.9	12	nC
Q _{gs}	Gate-Source Charge		---	0.7	2	
Q _{gd}	Gate-Drain Charge		---	1.8	4	
T _{d(on)}	Turn-On Delay Time	V _{DD} =10V, V _{GS} =4.5V, R _G =3.3Ω I _D =1A	---	4.8	10	ns
T _r	Rise Time		---	7.5	15	
T _{d(off)}	Turn-Off Delay Time		---	16.5	32	
T _f	Fall Time		---	4.8	10	
C _{iss}	Input Capacitance	V _{DS} =10V, V _{GS} =0V, F=1MHz	---	775	1440	pF
C _{oss}	Output Capacitance		---	95	190	
C _{rss}	Reverse Transfer Capacitance		---	58	120	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	2.2	---	Ω

Drain-Source Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	30	A
I _{SM}	Pulsed Source Current ²		---	---	60	A
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V, I _S =1A, T _J =25 °C	---	---	1	V

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. Essentially independent of operating temperature.

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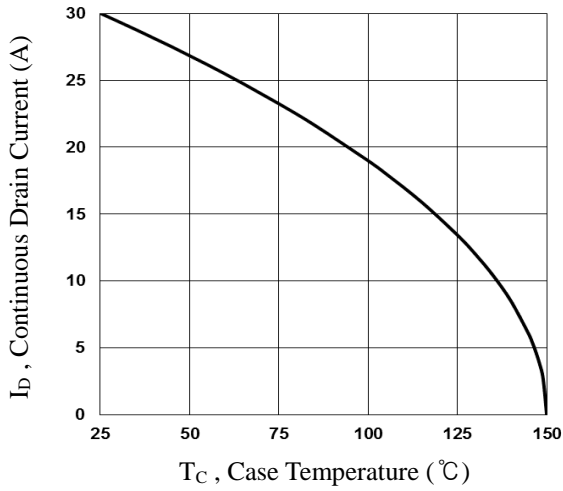


Fig. 1 Continuous Drain Current vs. T_C

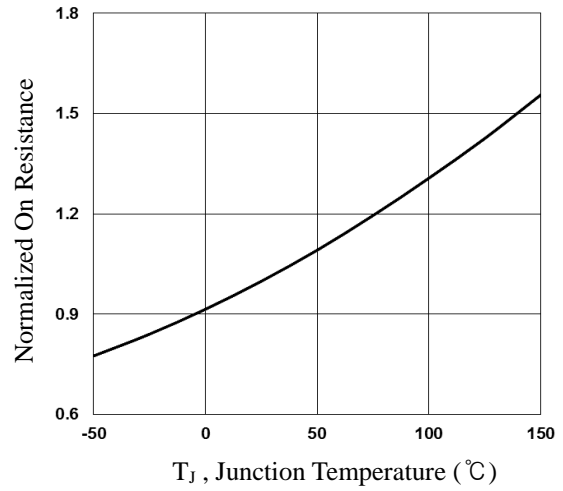


Fig. 2 Normalized $R_{DS(on)}$ vs. T_J

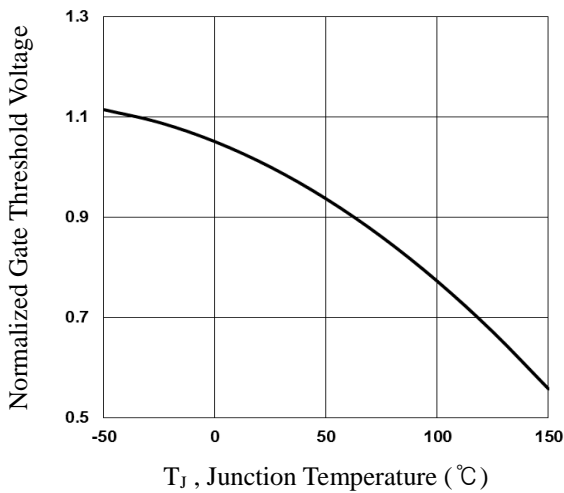


Fig. 3 Normalized V_{th} vs. T_J

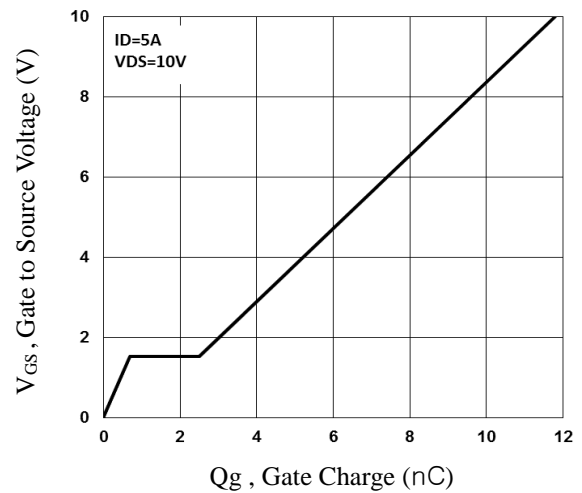


Fig. 4 Gate Charge Waveform

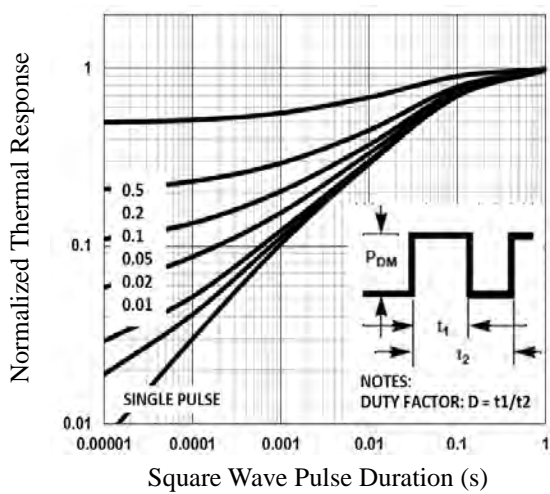


Fig. 5 Normalized Transient Impedance

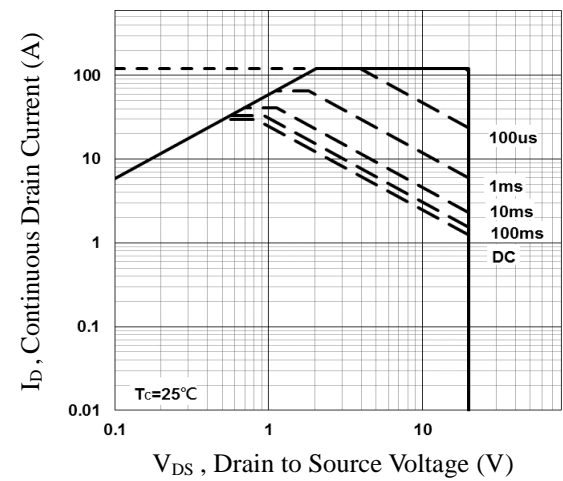


Fig. 6 Maximum Safe Operation Area



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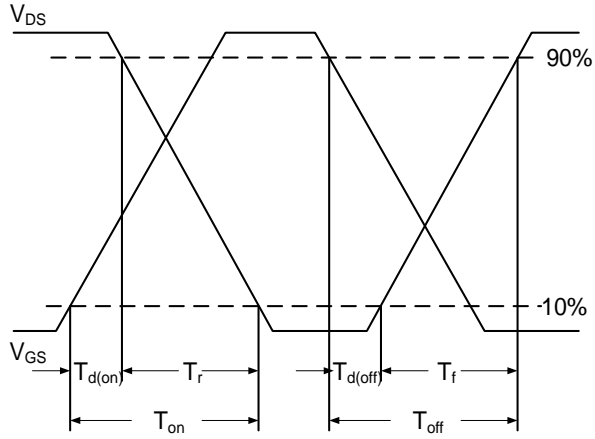


Fig. 7 Switching Time Waveform

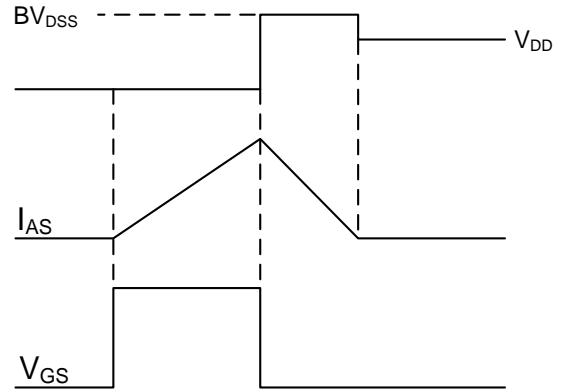
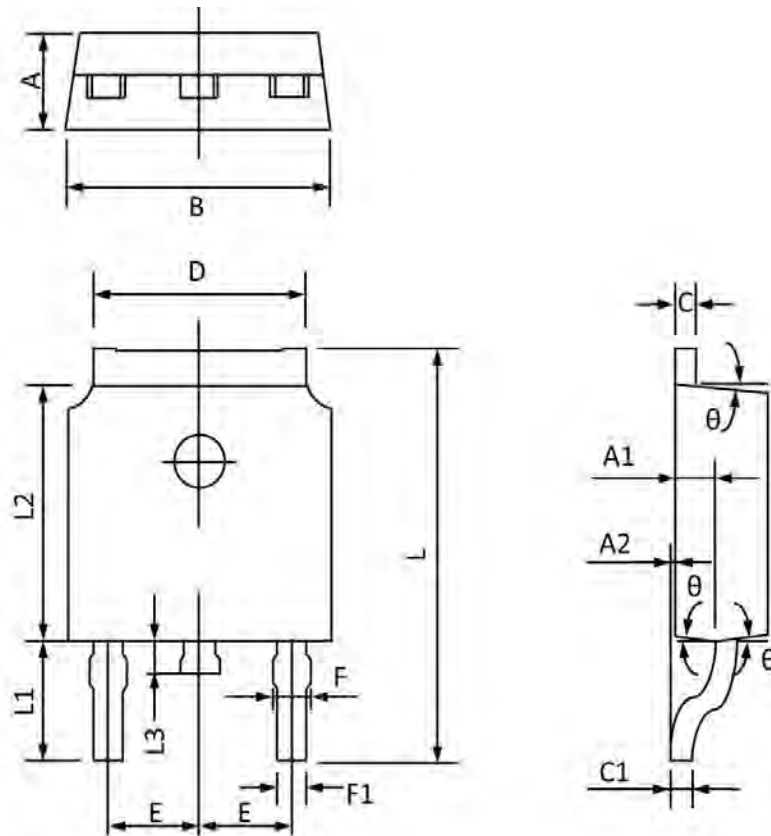


Fig. 8 EAS Waveform

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TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.450	2.150	0.096	0.085
A1	1.200	0.900	0.047	0.035
A2	0.250	0.000	0.010	0.000
B	6.800	6.300	0.268	0.248
C	0.600	0.350	0.024	0.014
C1	0.600	0.380	0.024	0.015
D	5.500	5.100	0.217	0.201
E	2.400	2.000	0.094	0.079
F	1.150	0.600	0.045	0.024
F1	0.900	0.500	0.035	0.020
L	10.400	9.400	0.409	0.370
L1	3.100	2.400	0.122	0.094
L2	6.300	5.300	0.248	0.209
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°