



**Super Fast Rectifiers Reverse Voltage 50V~1000V, Forward Current 1.0Amp**

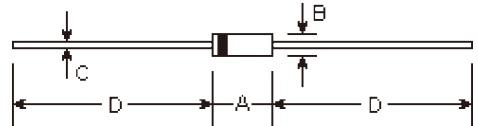
**Features**

- ◆ Low forward voltage drop
- ◆ High current capability
- ◆ High reliability
- ◆ High surge current capability

**DO-41**

**Mechanical Data**

- ◆ Case: Molded plastic DO-204AL(DO-41)
- ◆ Epoxy: UL 94V-O rate flame retardant
- ◆ Lead: Axial leads, solderable per MIL-STD-202, Method 208 guaranteed
- ◆ Polarity: Color band denotes cathode end
- ◆ High temperature soldering guaranteed:  
250°C/10 seconds .375" (9.5mm) lead lengths at 5 lbs., (2.3kg) tension
- ◆ Mounting position: Any
- ◆ Weight: DO-41 - 0.012 ounce, 0.335 gram



DIMENSIONS					
DIM	inches		mm		Note
	Min.	Max.	Min.	Max.	
A	0.165	0.205	4.2	5.2	
B	0.079	0.106	2.0	2.7	ϕ
C	0.028	0.034	0.71	0.86	ϕ
D	1.000	-	25.40	-	

**Maximum Ratings and Electrical Characteristics**

Ratings at 25°C ambient temperature unless otherwise specified.

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Single phase, half wave, 60Hz, resistive or inductive load.

Type Number		SF11	SF12	SF13	SF14	SF15	SF16	SF17	SF18	UNITS
Maximum Repetitive Peak Reverse Voltage	VRRM	50	100	150	200	300	400	500	600	V
Maximum RMS Voltage	VRMS	35	70	105	140	210	280	350	420	V
Maximum DC Blocking Voltage	VDC	50	100	150	200	300	400	500	600	V
Maximum Average Forward Rectified Current .375"(9.5mm) Lead Length @TA = 55°C	IF(AV)	1.0								A
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	IFSM	30								A
Maximum Instantaneous Forward Voltage @1.0A	VF	0.95			1.3		1.7			V
Maximum DC Reverse Current @ TA = 25°C at Rated DC Blocking Voltage @ TA = 100°C	IR	5.0				100				uA uA
Maximum Reverse Recovery Time (Note 1)	T <sub>RR</sub>	35								nS
Typical Junction Capacitance (Note 2)	C <sub>J</sub>	40				25				pF
Operating Temperature Range	T <sub>J</sub>	-55 to +125								°C
Storage Temperature Range	T <sub>STG</sub>	-55 to +150								°C

NOTES: 1. Reverse Recovery Test Conditions: IF=0.5A,IR=1.0A,IRR=0.25A

2.Measured at 1 MHz and Applied Reverse Voltage of 4.0 Volts D.C.

## RATINGS AND CHARACTERISTIC CURVES

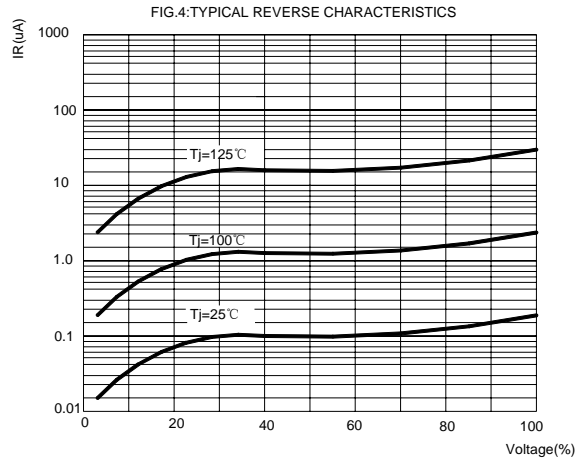
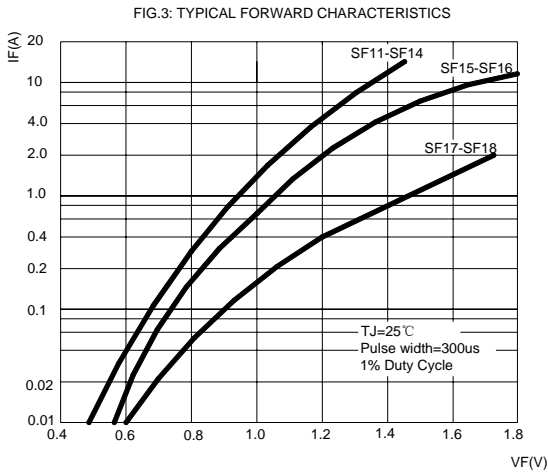
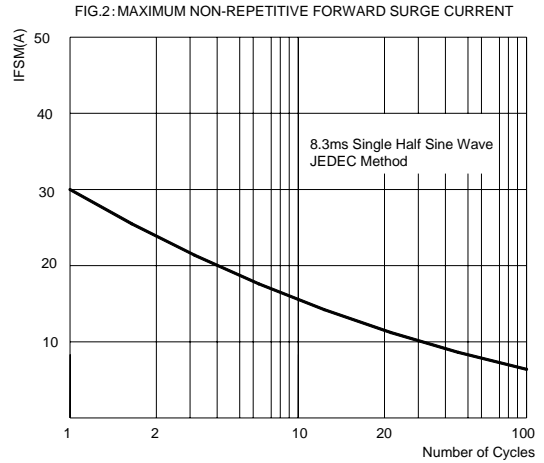
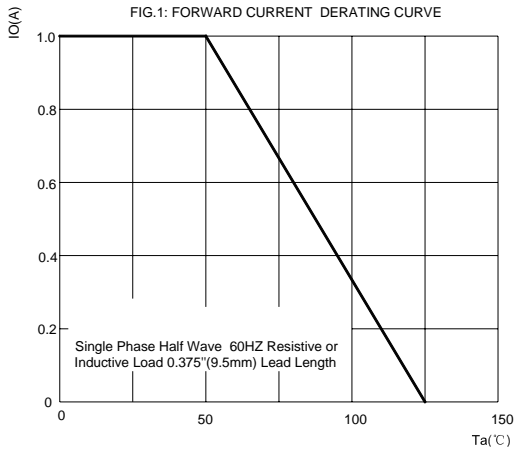


FIG.5: Diagram of circuit and Testing wave form of reverse recovery time

