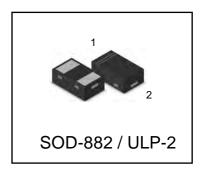
# SEMICONDUCTOR TECHNICAL DATA

## FTV05BAUL2

## Transient Voltage Suppressors for ESD Protection General Description

The FTV05BAUL2 is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium. Because of its small size, it is suited for use in cellular phones, MP3 players, digital cameras and many other portable applications where board space is at a premium.





## **Applications**

- Cellular phones audio
- MP3 players
- Digital cameras
- Portable applicationss
- Mobile telephone

#### **Features**

- Low Leakage
- Response Time is Typically < 1 ns</li>
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- These are Pb—Free Devices
- We declare that the material of product compliance with RoHS requirements.

### **Ordering information**

Device	Marking	Shipping		
FTV05BAUL2	PB	10000/Tape&Reel		

## Absolute Ratings (T<sub>amb</sub>=25°C)

Ratir	ng	Symbol	Value	Unit
IEC 61000-4-2 (ESD)	Air discharge		±30	kV
	Contact discharge		±30	kV
Total Power Dissipation on FR-5 Board (Note 1) @ T <sub>A</sub> =25 ℃		PD	200	mW
Junction and Storage Temperature Range		TJ,TSTG	-55 to 150	
Lead Solder Temperature – Maximum (10 Second Duration)		TL	260	

Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. FR-5 = 1.0\*0.75\*0.62 in.

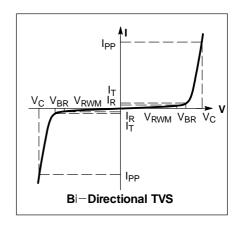




#### **ELECTRICAL CHARACTERISTICS**

(TA = 25°C unless otherwise noted)

Symbol	Parameter					
IPP	Maximum Reverse Peak Pulse Current					
Vc	Clamping Voltage @ IPP					
VRWM Working Peak Reverse Voltage						
I <sub>R</sub>	I <sub>R</sub> Maximum Reverse Leakage Current @ VRWM					
VBR	Breakdown Voltage @ IT					
Ι <sub>Τ</sub>	Test Current					
Ppk	Peak Power Dissipation					
С	Capacitance @ VR = 0 and f = 1.0 MHz					



#### **ELECTRICAL CHARACTERISTICS**

	V <sub>RWM</sub> (V)	IR1 (uA) @ VRWM	IR2 (uA) @VR=3.5V	VBR (V) @ IT (Note 2)		Ιτ	V <sub>C</sub> (V) @ IPP =1A (Note 3)	Vc (V) @MAX IPP (Note 3)	` ,	<b>Ррк (W)</b> (Note 3)	C (pF)
Device	Max	Max	Max	Min	Max	mA	Max	Max	Max	Max	Max
FTV05BAUL2	5.0	0.5	0.3	5.6	8.0	1.0	6.0	10	8	80	15

Other voltage available upon request.

- 2.  $V_{BR}$  is measured with a pulse test current IT at an ambient temperature of 25  $^{\circ}{\rm C}$
- 3. Surge current waveform per Figure 3.

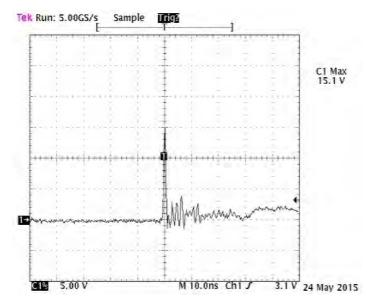


Fig1. ESD Clamping Voltage Screenshot Positive 8 kV Contact per IEC61000- 4- 2

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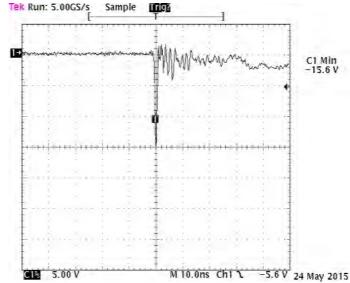


Fig2. ESD Clamping Voltage Screenshot Negative 8 kV Contact per IEC61000- 4- 2



# FTV05BAUL2

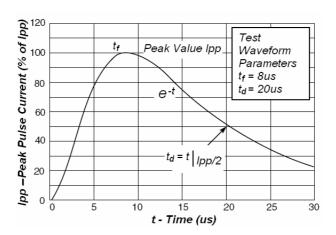


Fig3. Pulse Waveform

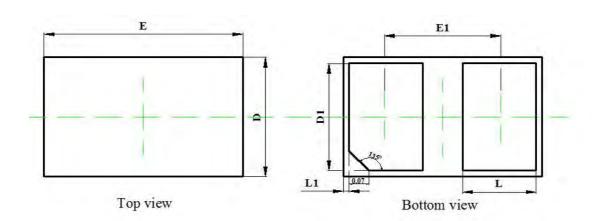
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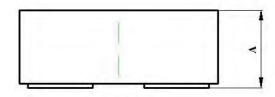


# FTV05BAUL2

# SOD-882 / ULP-2 Package Outline Drawing

# SOD-882 / ULP-2 Package Outline





Side view

Symbol		sions In neters	Dimensions In Inches		
	Min	Max	Min	Max	
Α	0.350	0.450	0.014	0.018	
D	<b>D</b> 0.550		0.022	0.026	
E	0.950	1.050	0.037	0.041	
D1	0.420	0.520	0.017	0.020	
E1	0.550	0.650	0.022	0.026	
L	0.270	0.370	0.011	0.015	
L1	0.000	0.100	0.000	0.004	

4/4

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